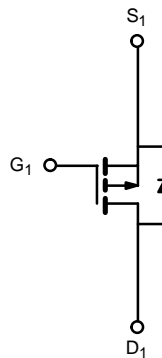
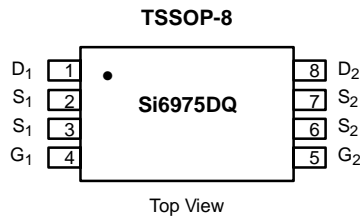


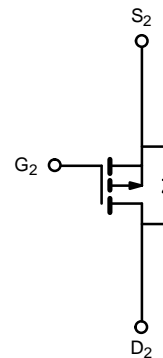
Dual P-Channel 12-V (D-S) MOSFET

TrenchFET[®]
Power MOSFETs
1.8-V Rated

PRODUCT SUMMARY		
V _{DS} (V)	r _{DS(on)} (Ω)	I _D (A)
-12	0.027 @ V _{GS} = -4.5 V	-5.1
	0.035 @ V _{GS} = -2.5 V	-4.5
	0.046 @ V _{GS} = -1.8 V	-3.9



P-Channel MOSFET



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS (T _A = 25 °C UNLESS OTHERWISE NOTED)					
Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V _{DS}	-12		V
Gate-Source Voltage		V _{GS}	±8		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 25 °C	I _D	-5.1	-4.3	A
	T _A = 70 °C		-4.1	-3.5	
Pulsed Drain Current (10 μs Pulse Width)		I _{DM}	-30		
Continuous Source Current (Diode Conduction) ^a		I _S	-1.0	-0.7	W
Maximum Power Dissipation ^a	T _A = 25 °C	P _D	1.14	0.83	
	T _A = 70 °C		0.73	0.53	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	-55 to 150		°C

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	t ≤ 10 sec	R _{thJA}	86	110	°C/W
	Steady State		124	150	
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	52	65	

Notes

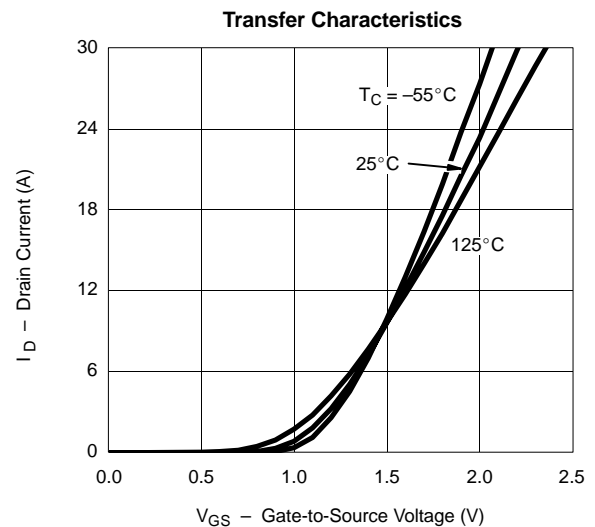
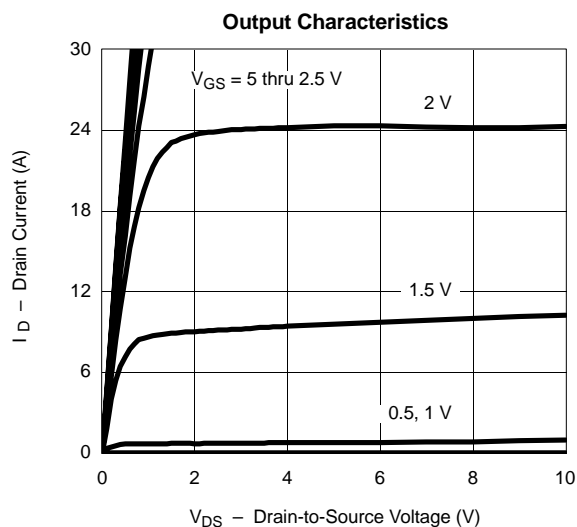
a. Surface Mounted on 1" x 1" FR4 Board.

SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)

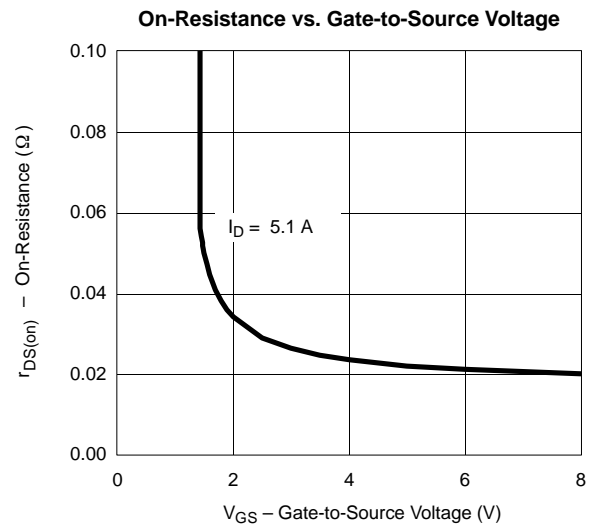
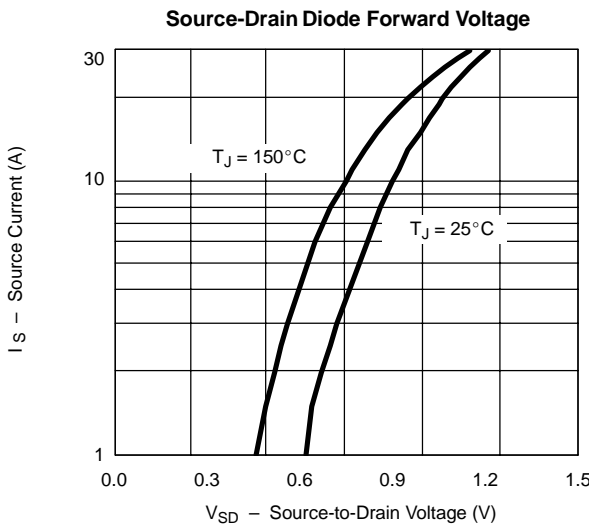
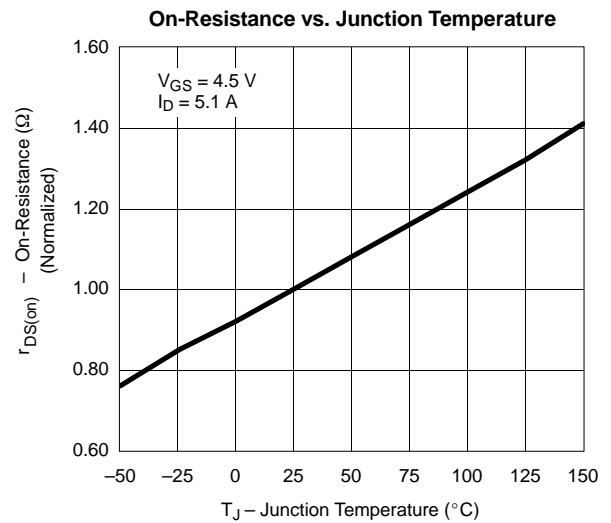
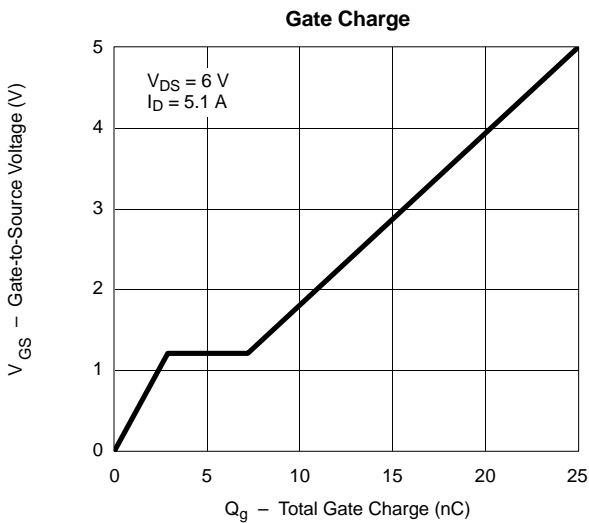
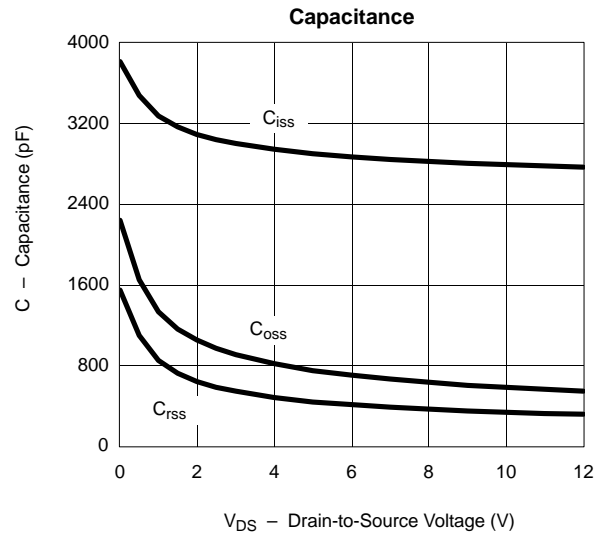
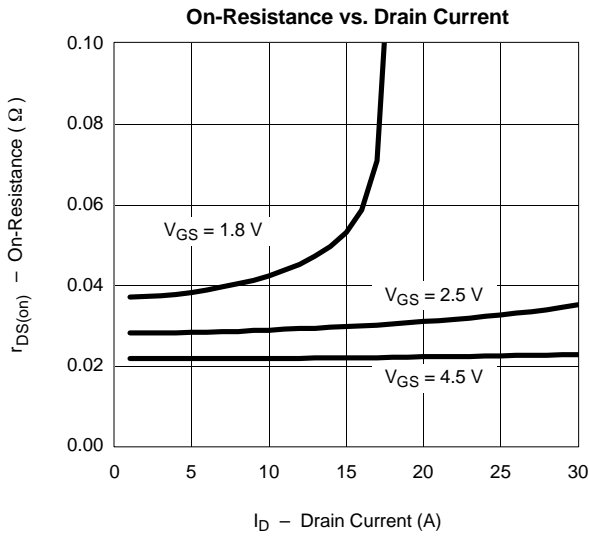
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -5 mA	-0.45			V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 8 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -9.6 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -9.6 V, V _{GS} = 0 V, T _J = 70 °C			-25	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -4.5 V	-20			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -4.5 V, I _D = -5.1 A		0.022	0.027	Ω
		V _{GS} = -2.5 V, I _D = -4.5 A		0.028	0.035	
		V _{GS} = -1.8 V, I _D = -3.9 A		0.037	0.046	
Forward Transconductance ^a	g _{fs}	V _{DS} = -5 V, I _D = -5.1 A		20		S
Diode Forward Voltage ^a	V _{SD}	I _S = -1.0 A, V _{GS} = 0 V		-0.65	-1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -6 V, V _{GS} = -4.5 V, I _D = -5.1 A		23	30	nC
Gate-Source Charge	Q _{gs}		3.0			
Gate-Drain Charge	Q _{gd}		4.3			
Turn-On Delay Time	t _{d(on)}	V _{DD} = -6 V, R _L = 6 Ω I _D ≅ -1 A, V _{GEN} = -4.5 V, R _G = 6 Ω		25	40	ns
Rise Time	t _r		32	50		
Turn-Off Delay Time	t _{d(off)}		96	140		
Fall Time	t _f		62	95		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -1.0 A, di/dt = 100 A/μs		60	100	

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)





TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)

