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## 2SJ574

# Silicon P Channel MOS FET High Speed Switching



ADE-208-739B (Z) 3rd.Edition. June 1999

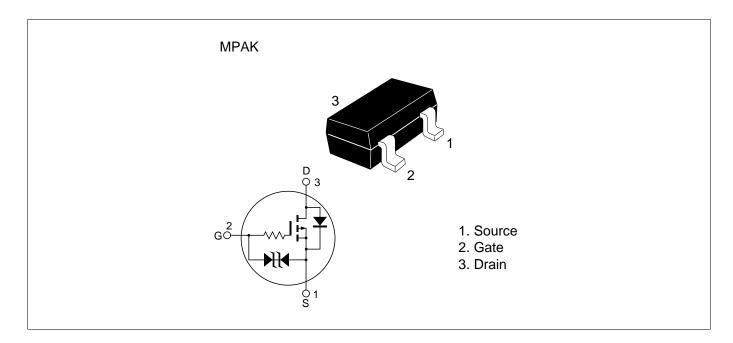
#### **Features**

• Low on-resistance

$$R_{DS}=1.1~\Omega$$
 typ. (V  $_{GS}=$  -10 V ,  $I_{D}=$  -150 mA) 
$$R_{DS}=2.2~\Omega$$
 typ. (V  $_{GS}=$  -4 V ,  $I_{D}=$  -150 mA)

- 4 V gate drive device.
- Small package (MPAK)

### **Outline**



## 2SJ574

## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit
Drain to source voltage	$V_{\scriptscriptstyle DSS}$	-30	V
Gate to source voltage	$V_{GSS}$	±20	V
Drain current	I <sub>D</sub>	-300	mA
Drain peak current	Note1 D(pulse)	-1.2	A
Body-drain diode reverse drain current	I <sub>DR</sub>	-300	mA
Channel dissipation	Pch Note 2	400	mW
Channel temperature	Tch	150	°C
Storage temperature	Tstg	-55 to +150	°C

Note: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value on the alumina ceramic board (12.5x20x0.7mm)

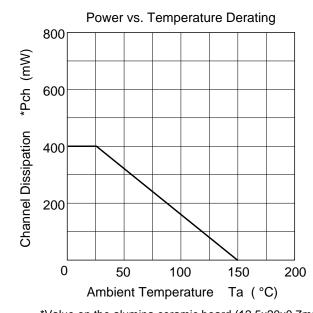
## **Electrical Characteristics** (Ta = 25°C)

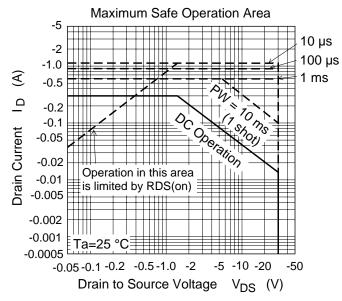
Item	Symbol	Min	Тур	Max	Unit	<b>Test Conditions</b>
Drain to source breakdown voltage	$V_{(BR)DSS}$	-30	_	_	V	$I_D = -100 \ \mu A, \ V_{GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>	_	_	±5	μΑ	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	_	_	-1	μΑ	$V_{DS} = -30 \text{ V}, V_{GS} = 0$
Gate to source cutoff voltage	$V_{GS(off)}$	-1.3	_	-2.3	V	$I_D = -10\mu A, V_{DS} = -5 V$
Static drain to source on state	$R_{\text{DS(on)}}$	_	1.1	1.3	Ω	$I_D = -150 \text{ mA}, V_{GS} = -10 \text{ V}^{\text{Note 3}}$
resistance	R <sub>DS(on)</sub>	_	2.2	3.1	Ω	$I_{D} = -150 \text{ mA}, V_{GS} = -4 \text{ V}^{\text{Note 3}}$
Forward transfer admittance	y <sub>fs</sub>	195	300	_	mS	$I_D = -150 \text{ mA}, V_{DS} = -10 \text{ V}^{\text{Note 3}}$
Input capacitance	Ciss	_	50	_	pF	V <sub>DS</sub> = -10 V
Output capacitance	Coss	_	40	_	pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss	_	15	_	pF	f = 1 MHz
Turn-on delay time	$t_{\text{d(on)}}$	_	20	_	ns	$I_{D} = -150 \text{ mA}, V_{GS} = -10 \text{ V}$
Rise time	t <sub>r</sub>	_	50	_	ns	$R_L = 66.6 \Omega$
Turn-off delay time	t <sub>d(off)</sub>		110		ns	
Fall time	t <sub>f</sub>	_	105	_	ns	

Note: 3. Pulse test

4. Marking is BP

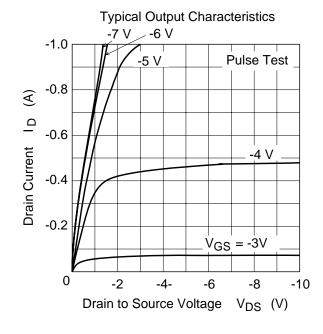
#### **Main Characteristics**

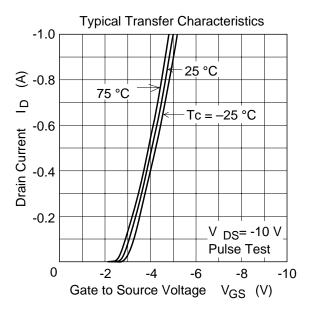


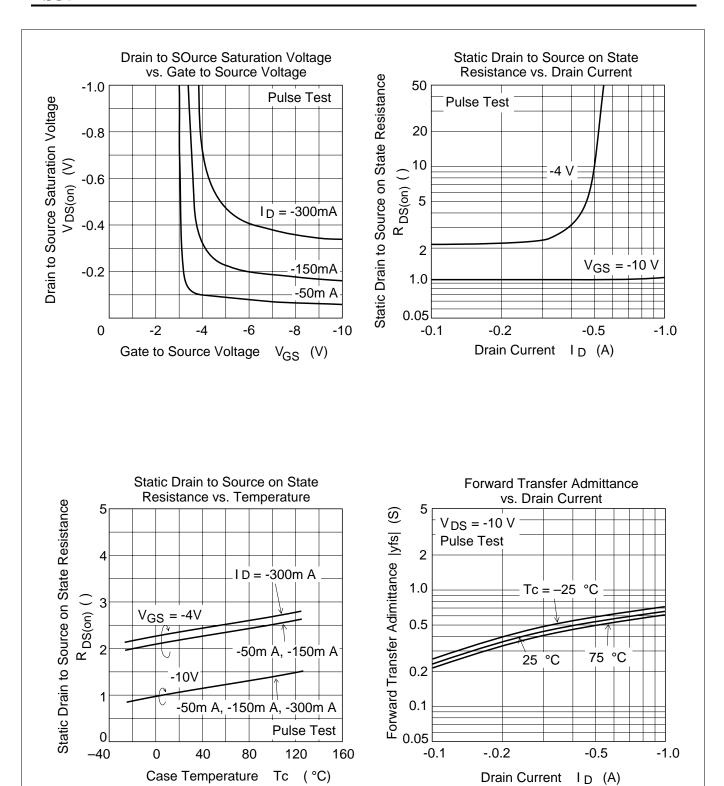


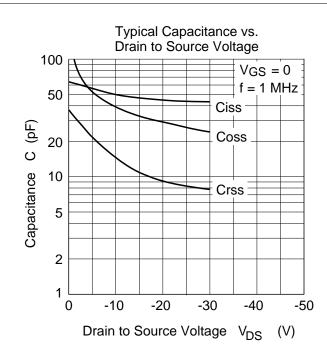
\*Value on the alumina ceramic board.(12.5x20x0.7mm)

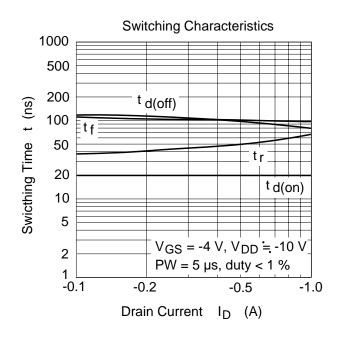
Value on the alumina ceramic board.(12.5x20x0.7mm)

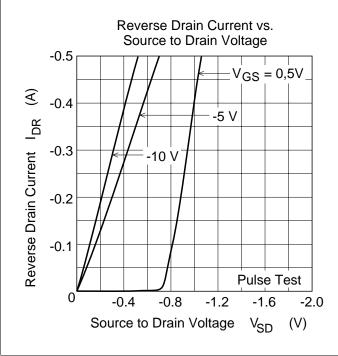




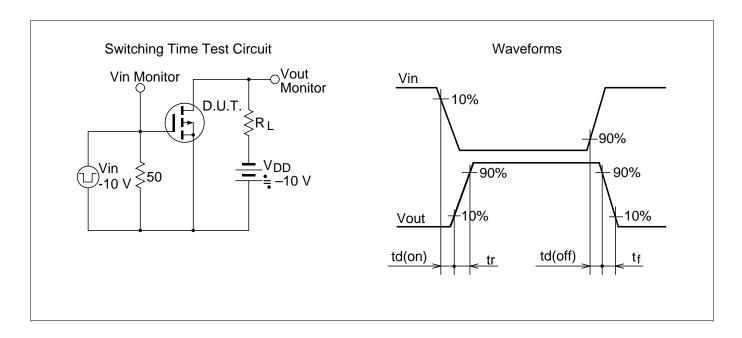




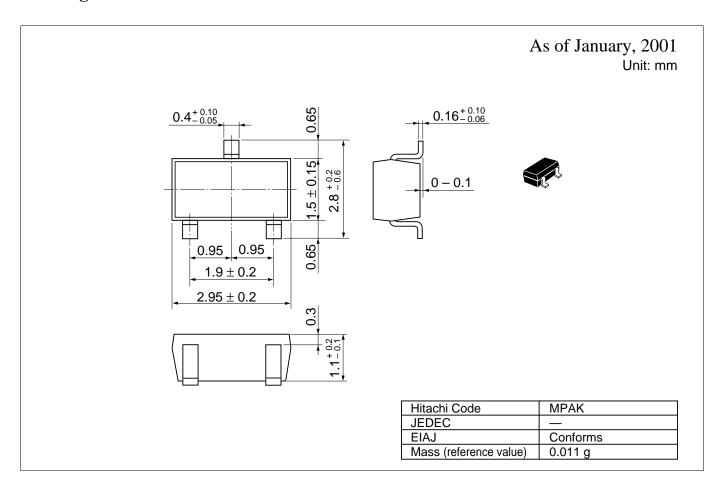




## 2SJ574



## **Package Dimensions**



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