



# **Ultrahigh-Speed Switching Applications**

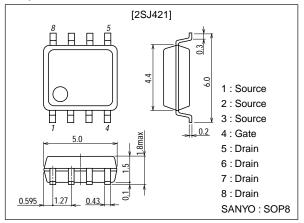
### **Features**

- · Low ON resistance.
- · Ultrahigh-speed switching.
- · 4V drive.

## **Package Dimensions**

unit:mm

2116



# **Specifications**

#### Absolute Maximum Ratings at Ta = 25°C

Parameter Symbol Conditions	Ratings	Unit
Drain-to-Source Voltage VDSS	-30	V
Gate-to-Source Voltage V <sub>GSS</sub>	±20	V
Drain Current (DC)	-5	Α
Drain Current (Pulse) I <sub>DP</sub> PW≤10μs, duty cycle≤1%	-32	Α
Allowable Power Dissipation PD Mounted on ceramic board (1000mm²×0.8mm)	2.0	W
Channel Temperature Tch	150	°C
Storage Temperature Tstg	-55 to +150	°C

#### Electrical Characteristics at Ta = 25°C

Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	O'III
Drain-to-Source Breakdown Voltage	V(BR)DSS	I <sub>D</sub> =-1mA, V <sub>GS</sub> =0	-30			V
Zero-Gate Voltage Drain Current	IDSS	V <sub>DS</sub> =-30V, V <sub>GS</sub> =0			-100	μΑ
Gate-to-Source Leakage Current	I <sub>GSS</sub>	$V_{GS}$ =±16V, $V_{DS}$ =0			±10	μΑ
Cutoff Voltage	VGS(off)	V <sub>DS</sub> =-10V, I <sub>D</sub> =-1mA	-1.0		-2.5	V
Forward Transfer Admittance	yfs	V <sub>DS</sub> =-10V, I <sub>D</sub> =-5A	6	10		S
Static Drain-to-Source ON-State Resistance	R <sub>DS(on)</sub> 1	I <sub>D</sub> =-5A, V <sub>GS</sub> =-10V		45	58	mΩ
	R <sub>DS(on)</sub> 2	I <sub>D</sub> =-2A, V <sub>GS</sub> =-4V		65	107	mΩ

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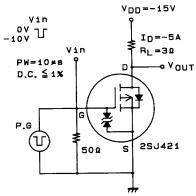
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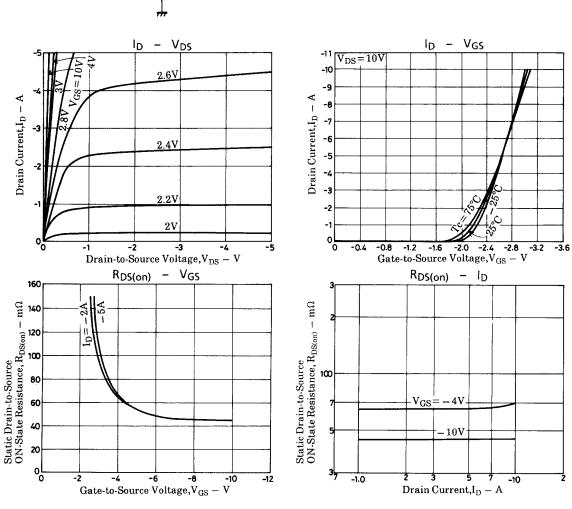
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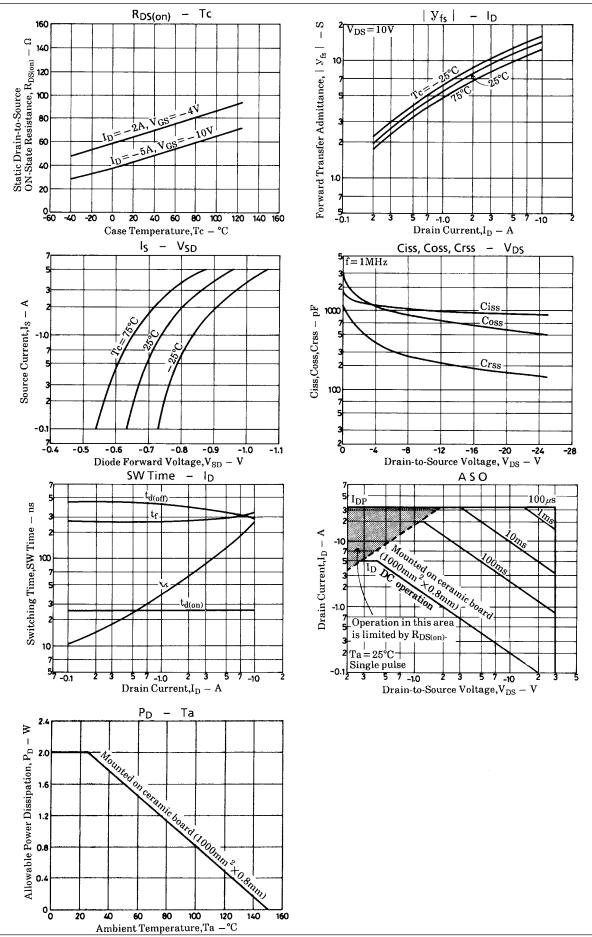
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Parameter	Symbol	Conditions	Ratings			Unit
			min	typ	max	O'III
Input Capacitance	Ciss	V <sub>DS</sub> =-10V, f=1MHz		1000		pF
Output Capacitance	Coss	V <sub>DS</sub> =-10V, f=1MHz		800		pF
Reverse Transfer Capacitance	Crss	V <sub>DS</sub> =-10V, f=1MHz		240		pF
Turn-ON Delay Time	t <sub>d</sub> (on)	See specified Test Circuit		25		ns
Rise Time	t <sub>r</sub>	See specified Test Circuit		140		ns
Turn-OFF Delay Time	td(off)	See specified Test Circuit		350		ns
Fall Time	t <sub>f</sub>	See specified Test Circuit		300		ns
Diode Forward Voltage	V <sub>SD</sub>	I <sub>S</sub> =-5A, V <sub>GS</sub> =0		-1.0	-1.2	V

## **Switching Time Test Circuit**







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