

2SJ246(L), 2SJ246(S)

SILICON P-CHANNEL MOS FET

Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- 4V gate drive device can be driven from 5V source.
- Suitable for Switching regulator, DC – DC converter

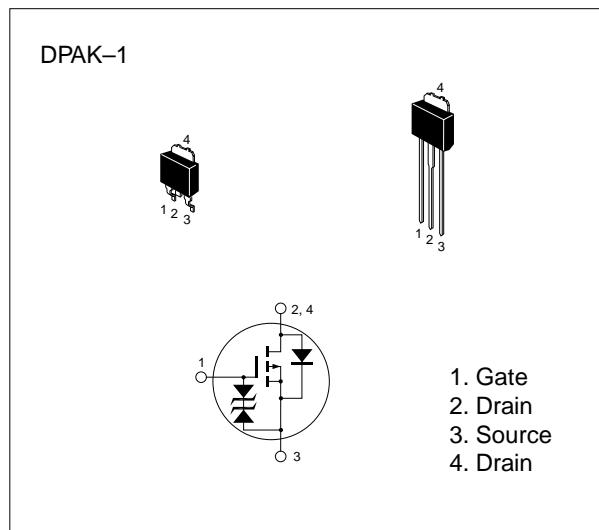


Table 1 Absolute Maximum Ratings (Ta = 25°C)

| Item | Symbol | Ratings | Unit |
|--|-------------------------|-------------|------|
| Drain to source voltage | V _{DSS} | -30 | V |
| Gate to source voltage | V _{GSS} | ±20 | V |
| Drain current | I _D | -7 | A |
| Drain peak current | I _{D(pulse)} * | -28 | A |
| Body-drain diode reverse drain current | I _{DR} | -7 | A |
| Channel dissipation | P _{ch} ** | 20 | W |
| Channel temperature | T _{ch} | 150 | °C |
| Storage temperature | T _{stg} | -55 to +150 | °C |

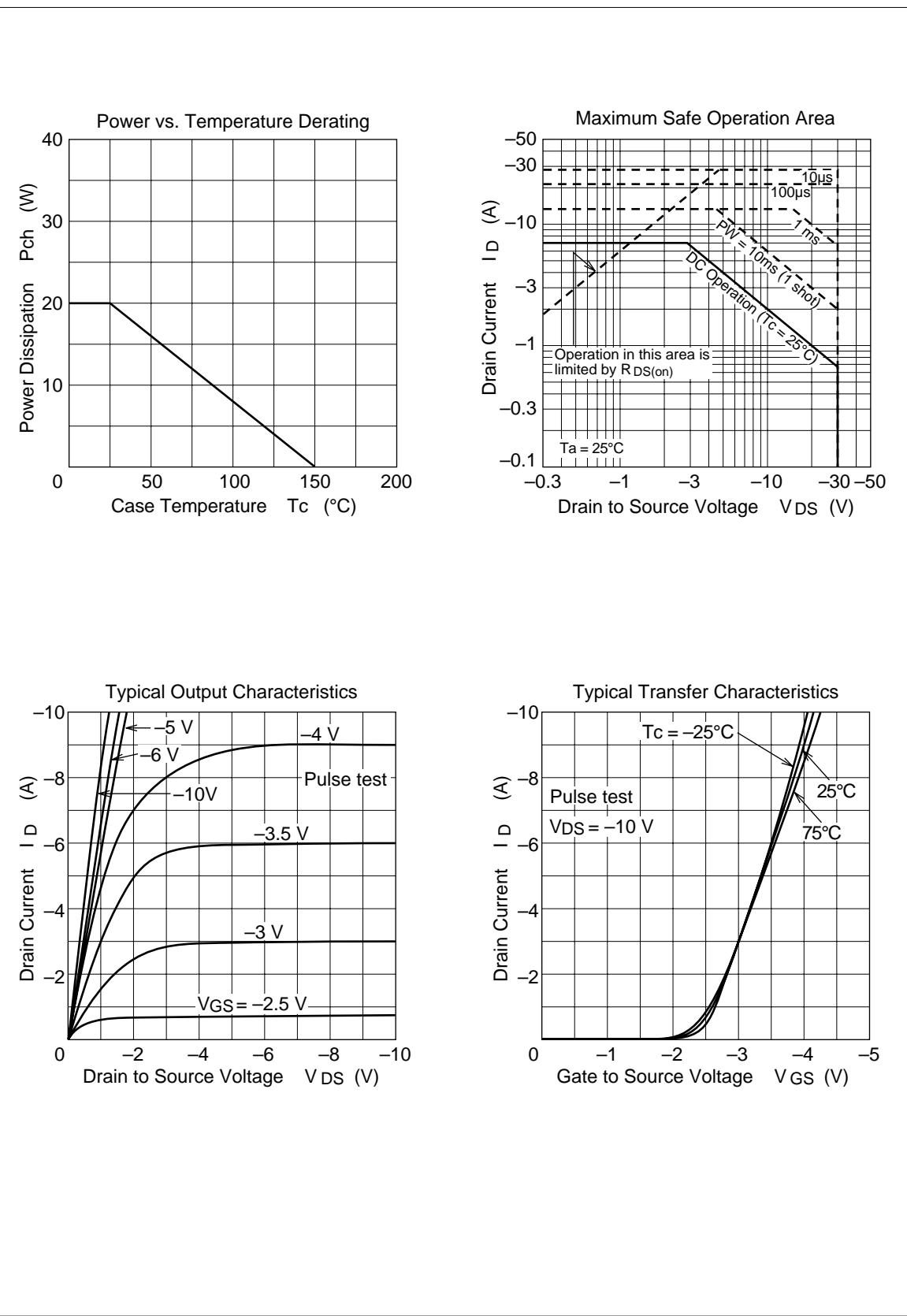
* PW ≤ 10 µs, duty cycle ≤ 1 %

** Value at T_c = 25 °C

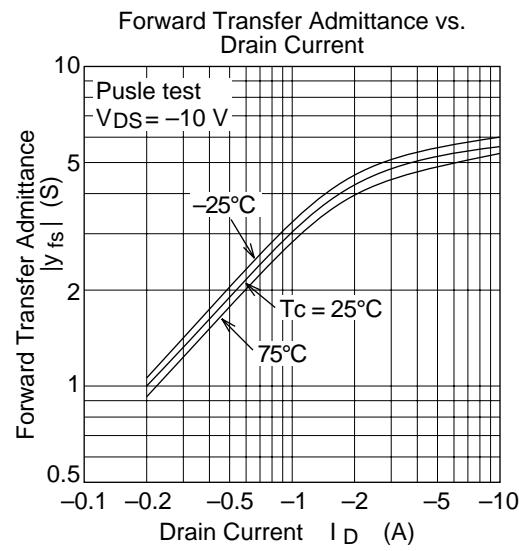
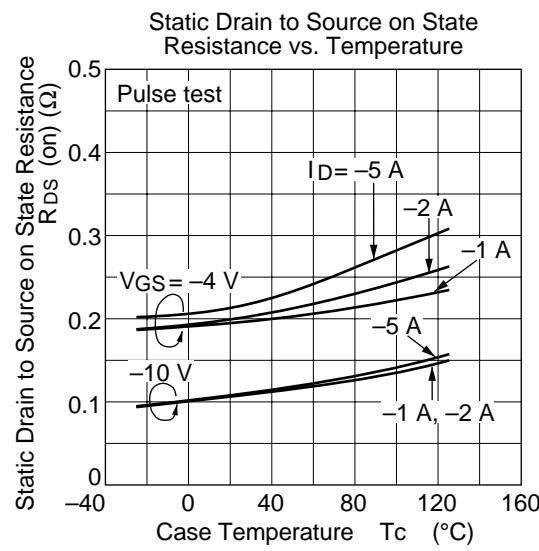
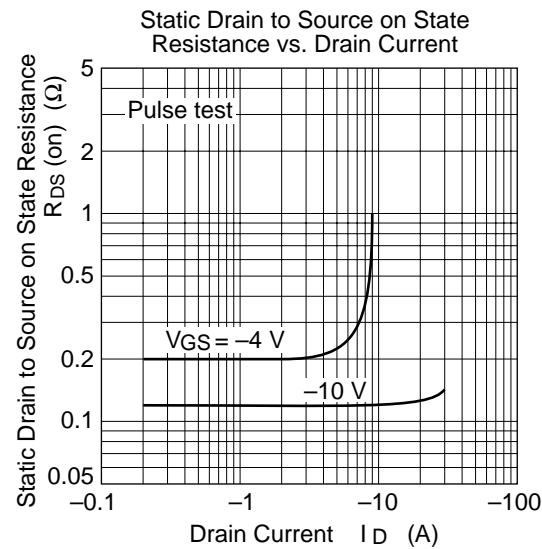
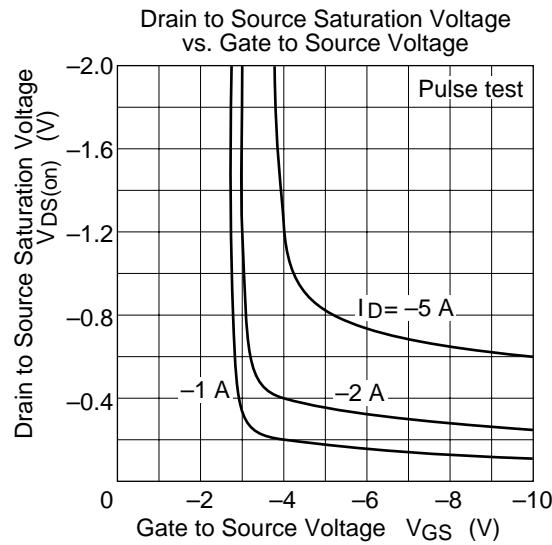
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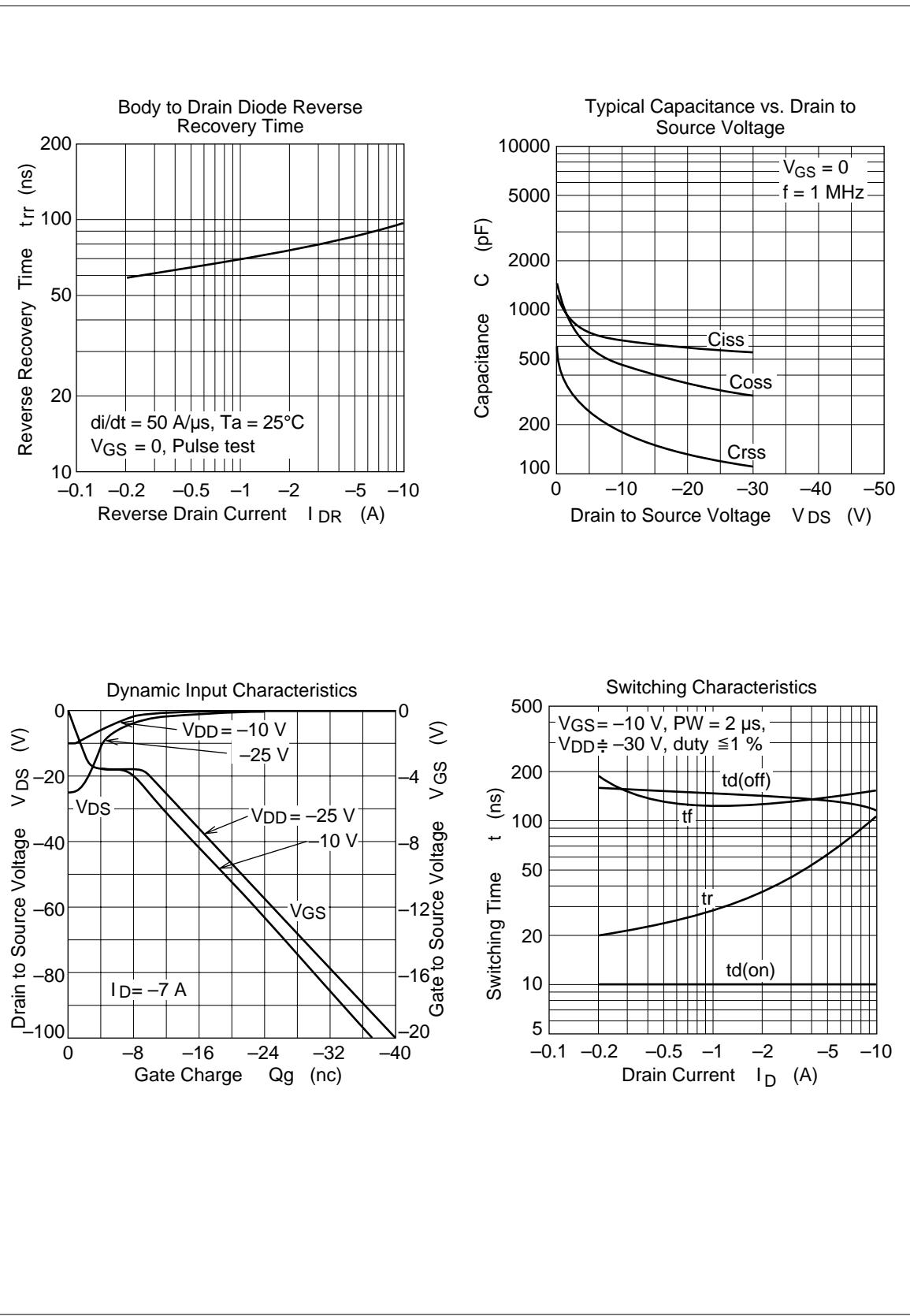
Table 2 Electrical Characteristics (Ta = 25°C)

| Item | Symbol | Min | Typ | Max | Unit | Test conditions |
|--|----------------------|------|------|------|------|---|
| Drain to source breakdown voltage | V _{(BR)DSS} | -30 | — | — | V | I _D = -10 mA, V _{GS} = 0 |
| Gate to source breakdown voltage | V _{(BR)GSS} | ±20 | — | — | V | I _G = ±100 µA, V _{DS} = 0 |
| Gate to source leak current | I _{GSS} | — | — | ±10 | µA | V _{GS} = ±16 V, V _{DS} = 0 |
| Zero gate voltage drain current | I _{DSS} | — | — | -100 | µA | V _{DS} = -25 V, V _{GS} = 0 |
| Gate to source cutoff voltage | V _{GS(off)} | -1.0 | — | -2.5 | V | V _{DS} = -10 V, I _D = -1 mA |
| Static drain to source on state resistance | R _{DS(on)} | — | 0.12 | 0.17 | Ω | I _D = -4 A V _{GS} = -10 V |
| | | — | 0.21 | 0.31 | Ω | I _D = -4 A V _{GS} = -4 V |
| Forward transfer admittance | y _{fs} | 3.0 | 5.0 | — | S | V _{DS} = -10 V I _D = -4 A |
| Input capacitance | C _{iss} | — | 660 | — | pF | V _{DS} = -10 V |
| Output capacitance | C _{oss} | — | 465 | — | pF | V _{GS} = 0 |
| Reverse transfer capacitance | C _{rss} | — | 180 | — | pF | f = 1 MHz |
| Turn-on delay time | t _{d(on)} | — | 10 | — | ns | V _{GS} = -10 V |
| Rise time | t _r | — | 55 | — | ns | I _D = -4 V |
| Turn-off delay time | t _{d(off)} | — | 135 | — | ns | R _L = 7.5 Ω |
| Fall time | t _f | — | 135 | — | ns | |
| Body-drain diode forward voltage | V _{DF} | — | -1.2 | — | V | I _F = -7 A, V _{GS} = 0 |
| Body-drain diode reverse recovery time | t _{rr} | — | 90 | — | µs | I _F = -7 A, V _{GS} = 0, di _F / dt = 50 A / µs |

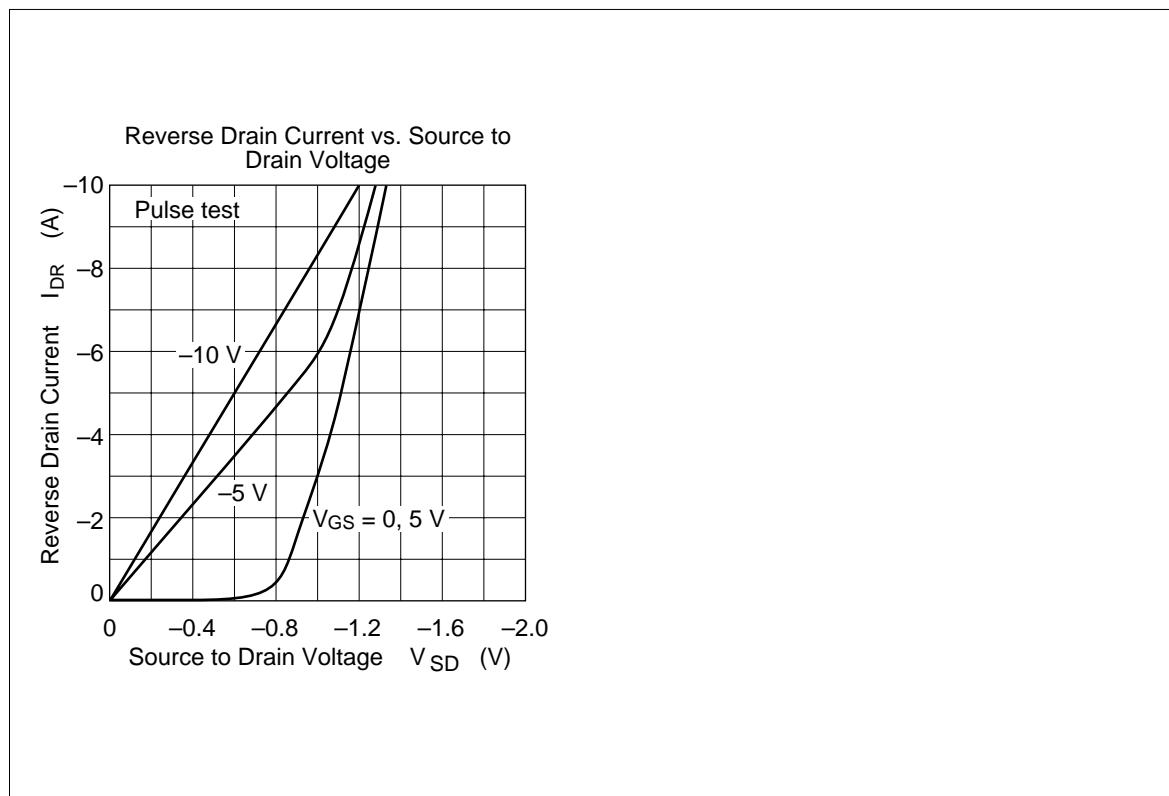


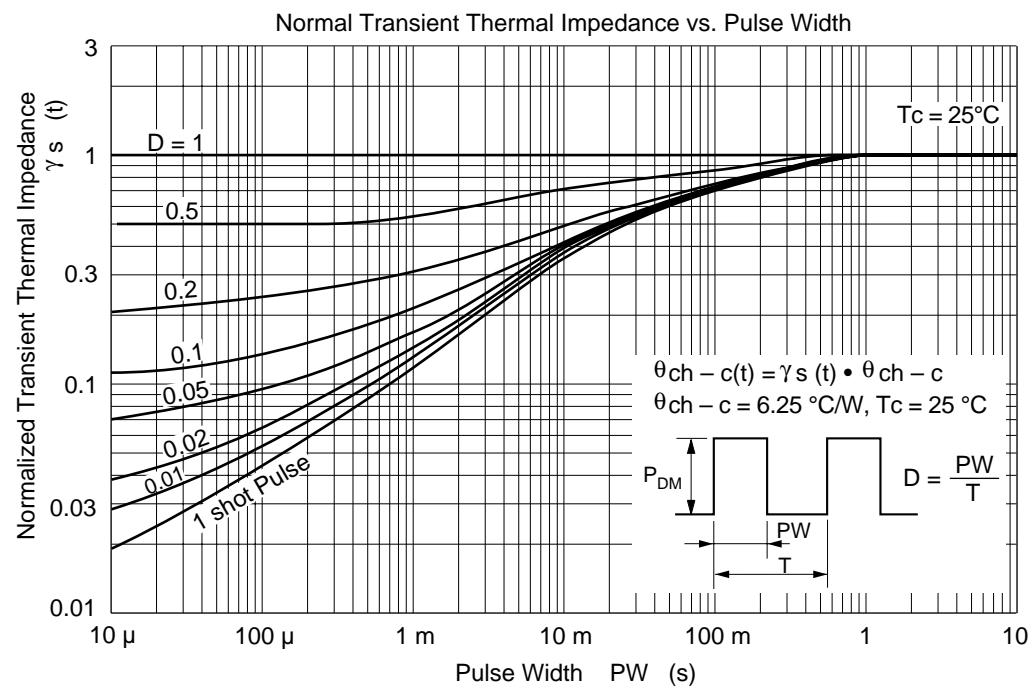
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Switching Time Test Circuit Waveform

