

CCD multichannel detector head C10150, C10151



Designed for back-thinned CCD area image sensor

C10150 and C10151 are high sensitivity multichannel detector heads for use with back-thinned FFT-CCD area image sensors (S10140/S10141 series) that offer high UV sensitivity and quantum efficiency. C10150 is designed for the non-cooled back-thinned CCD image sensor (S10140 series), while C10151 for the one-stage TE-cooled CCD area image sensors (S10141 series) for detection at even lower light levels. Both C10150 and C10151 incorporate a low-noise driver/amplifier circuit that provides reliable operation from simple external signals. C10151 also includes a highly stable temperature controller that cools the sensor to a preset temperature level ($T_s = -10\text{ }^\circ\text{C}$) as soon as the power is turned on. If the cooler fails and causes internal circuitry to overheat, the built-in protection circuit automatically turns off the power to the thermoelectric cooler. Despite its compact size, the housing configuration is designed for good heat dissipation, and threaded mounting holes on the front panel allow connections to other devices such as monochrometers. The table below shows back-thinned CCD image sensors for C10150 and C10151. C10150 and C10151 do not come with a CCD image sensor, so select the desired sensor and order it separately. Controller for multichannel detector head C7557-01 is also available. The software supplied with the C7557-01 allows easy control of the multichannel detector head and data acquisition.

Features

- Designed for back-thinned CCD area image sensor *1
C10150: for non-cooled type (S10140 series)
C10151: for TE-cooled type (S10141 series)
- Line binning operation *2/area scanning operation
- Driver/amplifier circuit for low noise CCD operation
- Highly stable temperature controller (C10151)
Cooling temperature: $-10 \pm 0.05\text{ }^\circ\text{C}$
- Simple signal input operation
- Compact configuration

Applications

- Fluorescence spectroscopy
- Raman spectroscopy
- Other low-light-level detection

■ Selection guide

The table below shows CCD area image sensors applicable for C10150, C10151.

Since C10150, C10151 do not include a CCD area image sensor, so select the desired sensor and order it separately.

Type No.	CCD area image sensor			
	Type No.	Number of pixels	Number of active pixels	Active area [mm (H) × mm (V)]
C10150	S10140-1007	1044 × 128	1024 × 122	12.288 × 1.464
	S10140-1008	1044 × 256	1024 × 250	12.288 × 3.000
	S10140-1009	1044 × 512	1024 × 506	12.288 × 6.072
	S10140-1107	2068 × 128	2048 × 122	24.576 × 1.464
	S10140-1108	2068 × 256	2048 × 250	24.576 × 3.000
	S10140-1109	2068 × 512	2048 × 506	24.576 × 6.072
C10151	S10141-1007	1044 × 128	1024 × 122	12.288 × 1.464
	S10141-1008	1044 × 256	1024 × 250	12.288 × 3.000
	S10141-1009	1044 × 512	1024 × 506	12.288 × 6.072
	S10141-1107	2068 × 128	2048 × 122	24.576 × 1.464
	S10141-1108	2068 × 256	2048 × 250	24.576 × 3.000
	S10141-1109	2068 × 512	2048 × 506	24.576 × 6.072

*1: In normal CCD image sensors that receive light from the front surface, the active area is covered with electrodes for charge transfer. These electrodes cut off UV radiation incident on the image sensor and also reduce the quantum efficiency. In contrast, back-thinned CCD image sensors have a unique structure in which the back of the active area is finely ground to allow light to enter from the back surface. This structure ensures high UV sensitivity and quantum efficiency because incident light need not pass through the electrodes.

*2: The FFT-CCD was originally designed as a two-dimensional image sensor. However, it can be operated like a linear image sensor having a large active area by transferring all the pixel signals in the vertical direction to the horizontal register (this is referred to as line binning).

■ Absolute maximum ratings

Parameter		Symbol	Min.	Typ.	Max.	Unit
Supply voltage (for digital circuitry)		VD1	-0.5	-	+7	V
Supply voltage (for analog circuitry)		VA1+	-	-	+18	
		VA1-	-	-	-18	
Supply voltage		VA2	-	-	+30	
		VD2	-	-	+7	
		Vp	-	-	+7	
		VF	-	-	+14	
Digital input voltage		-	-	-	VD1	
Operating temperature	C10150	Topr	0	-	+50	°C
	C10151		+10	-	+35 *3	
Storage temperature	C10150	Tstg	-20	-	+70	
	C10151		0	-	+50	

*3: The maximum value may be lower, depending on the drive frequency and the number of sensor pixels.

■ Electrical characteristics

(Ta=25 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V, unless otherwise noted)

Parameter		Symbol	Min.	Typ.	Max.	Unit
Digital input	High level	VIH	+2.0	-	+5.0	V
	Low level	VIL	-0.5	-	+0.8	V
CLK frequency		fCLK	-	-	1	MHz
Data video readout frequency		fv	-	-	fCLK/4	Hz
Start pulse width		tst	1/fCLK	-	-	s
Digital output	High level (Io= -6 mA)	VIH	+2.0	-	-	V
	Low level (Io=+6 mA)	VIL	-	-	+0.8	V
Power supply operating conditions						
Voltage	Digital circuitry	VD	+4.75	+5.0	+5.25	V
	Analog circuitry	VA1+	+14.5	+15.0	+15.5	V
		VA1-	-14.5	-15.0	-15.5	V
		VA2	+23.5	+24.0	+24.5	V
	Other	VD2	+4.75	+5.0	+5.25	V
		Vp	+4.75	+5.0	+5.25	V
	VF	+11.75	+12.0	+12.25	V	
Current	VD1 (+5 VDC)	-	-	-	+200	mA
	VA1+ (+15 VDC)	-	-	-	+100	mA
	VA1- (-15 VDC)	-	-	-	-100	mA
	VA2 (+24 VDC)	-	-	-	+30	mA
	VD2 (+5 VDC) *4	-	-	-	+30	mA
	Vp (+5 DC) *4	-	-	-	+2.5	A
	VF (+12 VDC) *4	-	-	+100	-	mA

*4: C10151

■ Electrical and optical characteristics

(Ta=25 °C, Ts=-10 °C, VD1=+5 V, VA1+=+15 V, VA1=-15 V, VA2=+24 V, VD2=+5 V, Vp=+5 V, VF=+12 V, unless otherwise noted)

Parameter		Symbol	Min.	Typ.	Max.	Unit
Spectral response range		λ	-	200 to 1100	-	nm
Full well capacity	Vertical	Fw	60000	75000	-	e ⁻
	Horizontal		120000	150000	-	
Conversion gain *5		Sv	45	56	67	μV/e ⁻
Dark current *6	C10150 (Ta=25 °C)	DS	-	100	1000	e ⁻ /pixels/s
	C10151 (Ta=-10 °C)		-	1.5	15	
Readout noise		Nr	-	20	45	e ⁻ rms
Dynamic range	Line binning	DR	-	7500	-	-
	Area scanning		-	3750	-	
Photo response non-uniformity *7		PRNU	-	±3	±10	%

*5: Including the circuit gain.

*6: At MPP mode. Vertical register value. The actual value equals the sum of the vertical direction because of the binning operation.

*7: Measured at 50 % of the full well capacity.

■ Specifications for temperature controller (C10151)

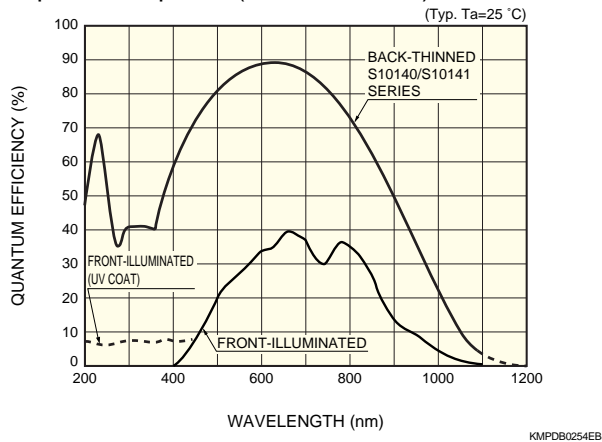
($T_a=25\text{ }^\circ\text{C}$, $V_{D1}=+5\text{ V}$, $V_{A1}=+15\text{ V}$, $V_{A1}=-15\text{ V}$, $V_{A2}=+24\text{ V}$, $V_{D2}=+5\text{ V}$, $V_p=+5\text{ V}$, $V_f=+12\text{ V}$, CLK frequency 250 kHz, unless otherwise noted)

Parameter *8	Symbol	Min.	Typ.	Max.	Unit
Cooling temperature	T_s	-11	-10	-9	$^\circ\text{C}$
Temperature control range	ΔT_s	-0.05	-	+0.05	$^\circ\text{C}$
Power dissipation of TE-cooler element	P_p	-	-	7	W
Cool down time to reset temperature	t_o	-	-	5	min.
Setting temperature for overheat protection *9	T_o	-	+45	-	$^\circ\text{C}$

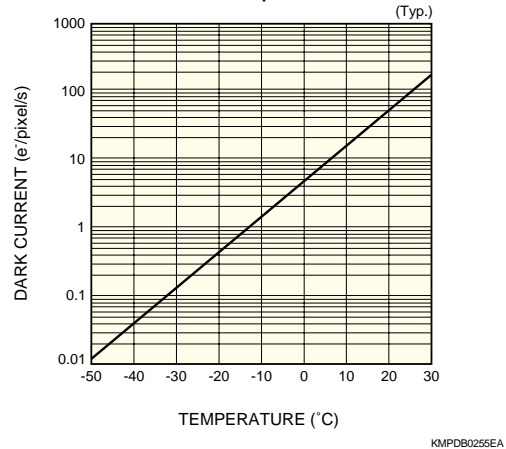
*8: Other functions include error display, automatic power off, and detection of electrical opens and shorts by the thermosensor.

*9: Temperature on the rear of the case (where the fan is installed).

■ Spectral response (without window) *10

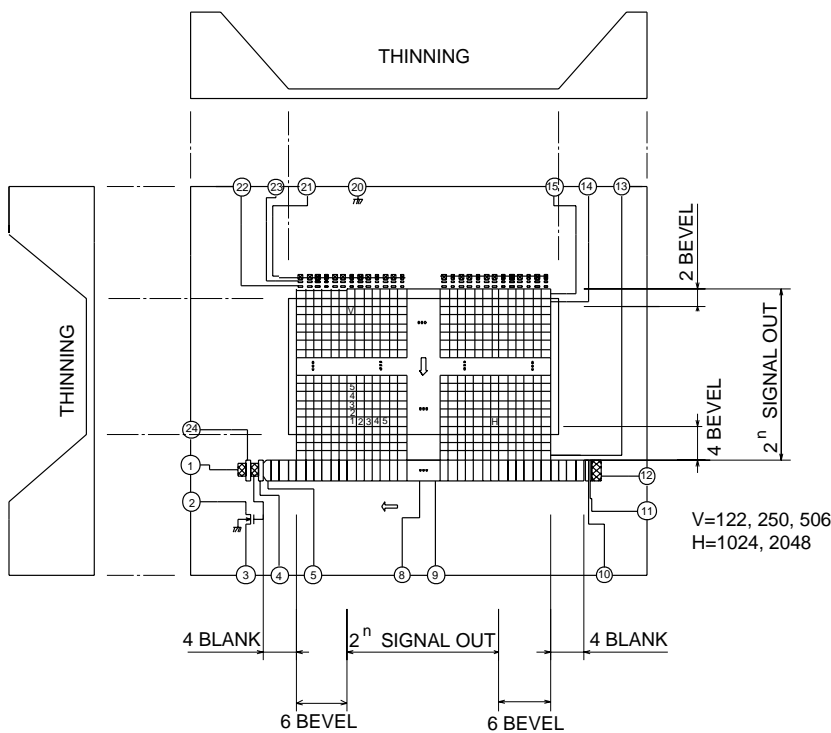


■ Dark current vs. temperature



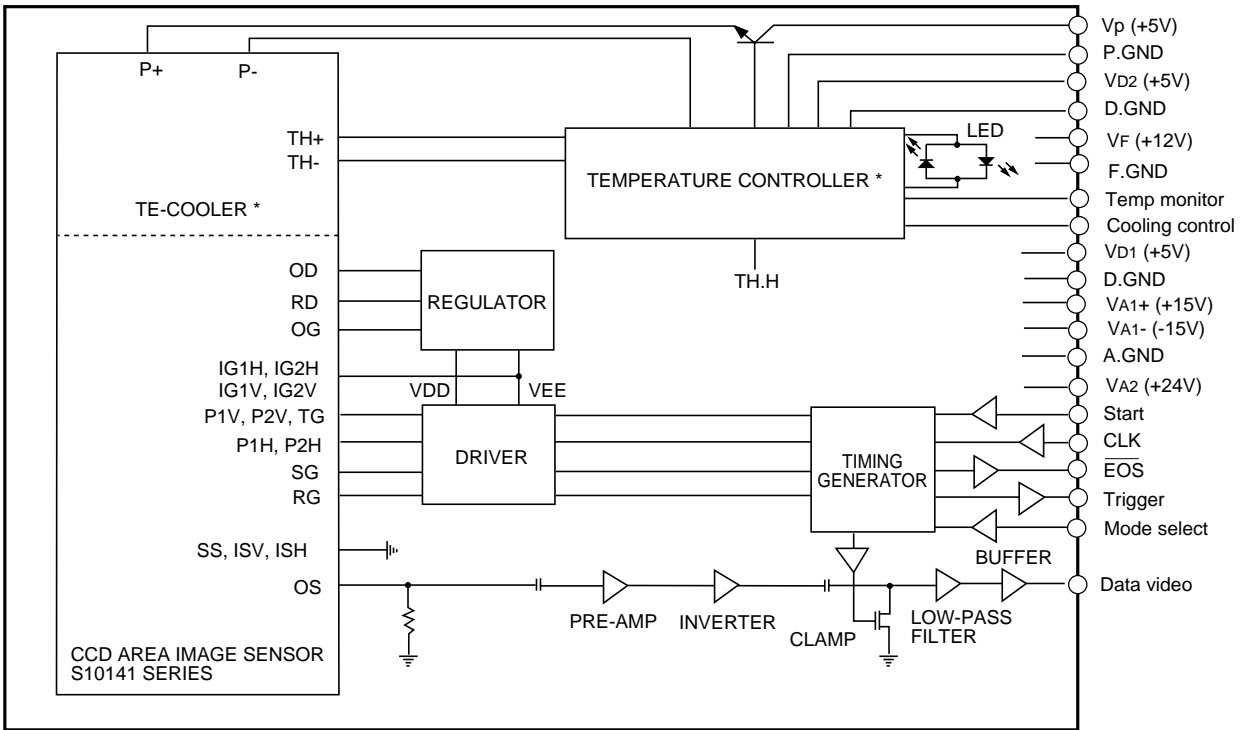
*10: Spectral response with quartz glass (or AR-coated sapphire glass) is decreased by the transmittance

■ Device structure (Conceptual drawing)



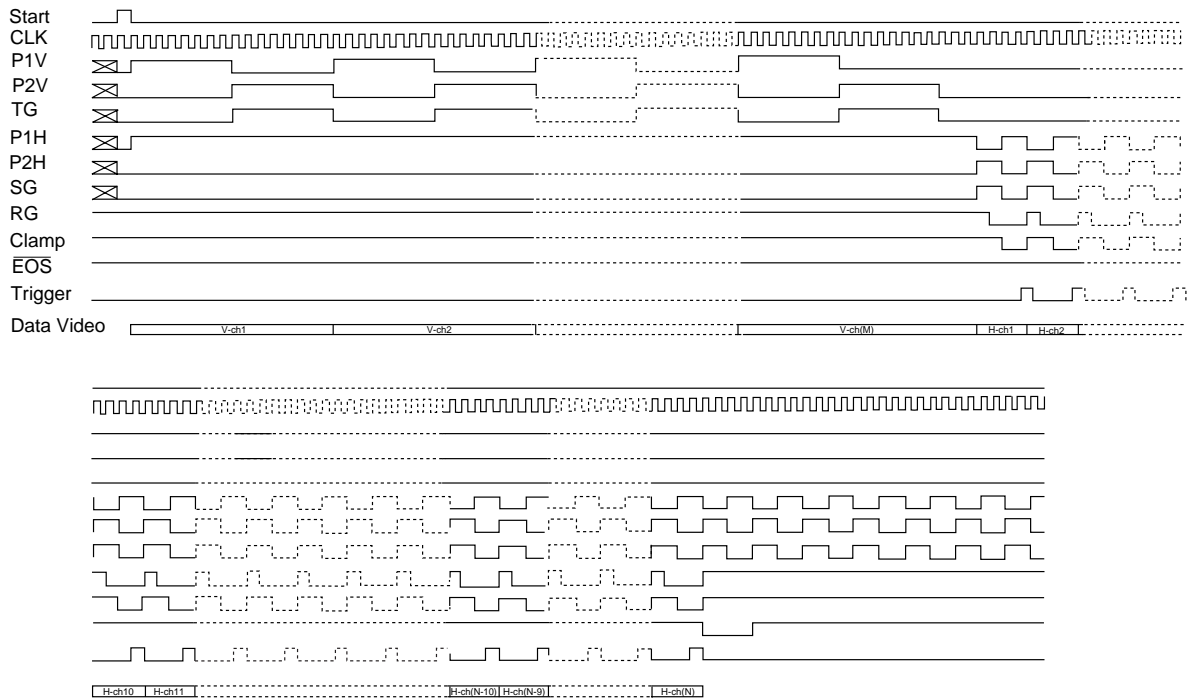
KMPDC0244EA

■ Block diagram (C10151)



KACCC0313EA

■ Timing chart (line binning operation)

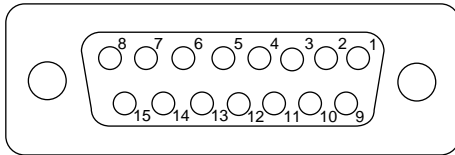


M=128, 256, 512 N=1044, 2068
 V-ch1--V-ch2, V-ch (M-3)--V-ch (M): Isolation pixels
 H-ch1--H-ch4, H-ch (N-3)--H-ch (N): Blank pixels

KACCC0319ED

■ Pin connection of "SIGNAL I/O" connector

15-pin D-sub connector

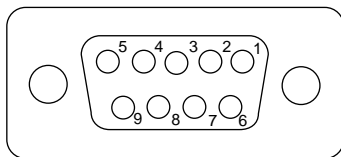


KACCC0069EA

Pin No.	Terminal name	Description
1	Mode select	Digital input signal used to select between the line binning operation and area scanning operation. HCMOS compatible. High level or left open: line binning operation Low level: area scanning operation
2	Data video	Analog video output. Positive polarity
3	VA1+ (+15 V)	Analog power supply
4	VA1- (-15 V)	Analog power supply
5	VD1 (+5 V)	Digital power supply
6	Start	Digital input signal for initializing the circuit. HCMOS compatible. Positive logic. The interval of the Start pulses determines the integration time of the CCD image sensor.
7	CLK	Digital input signal for operating the circuit. HCMOS compatible. Rising edge operation.
8	\overline{EOS}	Digital output signal for indicating end-of-scan of the image sensor. HCMOS compatible. Negative logic.
9	A.GND	Analog ground
10	A.GND	Analog ground
11	VA2 (+24 V)	Analog power supply
12	D.GND	Digital ground
13	D.GND	Digital ground
14	D.GND	Digital ground
15	Trigger	Digital output signal for A/D conversion. HCMOS compatible. Positive logic.

■ Pin connection of "TE CONTROL I/O" connector (C10151)

9-pin D-sub connector



KACCC0075EA

Pin No.	Terminal name	Description
1	VD2 (+5 V)	Digital power supply
2	Temp monitor	Analog output signal of the temperature of the CCD image sensor
3	Cooling control	Digital input signal for starting to cool down. HCMOS compatible. High level or left open: cooling Low level: stand-by
4	Vp (+5 V)	Power supply for the thermoelectric cooler in the CCD image sensor. (Please use AWG 18 wire)
5	VF (+12 V)	Power supply for cooling fan
6	D.GND	Ground
7	D.GND	Ground
8	P.GND	Power supply return of the thermoelectric cooler mounted in the CCD image sensor. (Please use AWG 18 wire)
9	F.GND	Power supply return for cooling fan

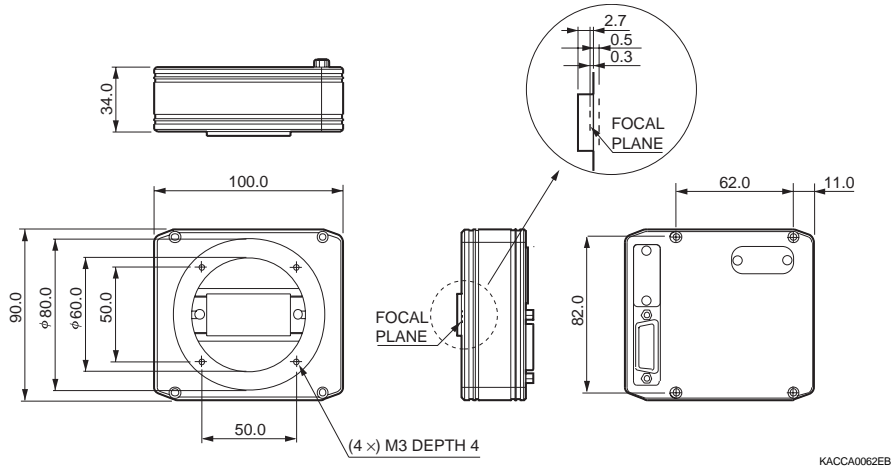
Available for using same power supply (+5 V) for "+VD2" and "+Vp".

Caution) Do not connect "VD2" and "Vp" together on the backside of the 9-pin D-sub connector.

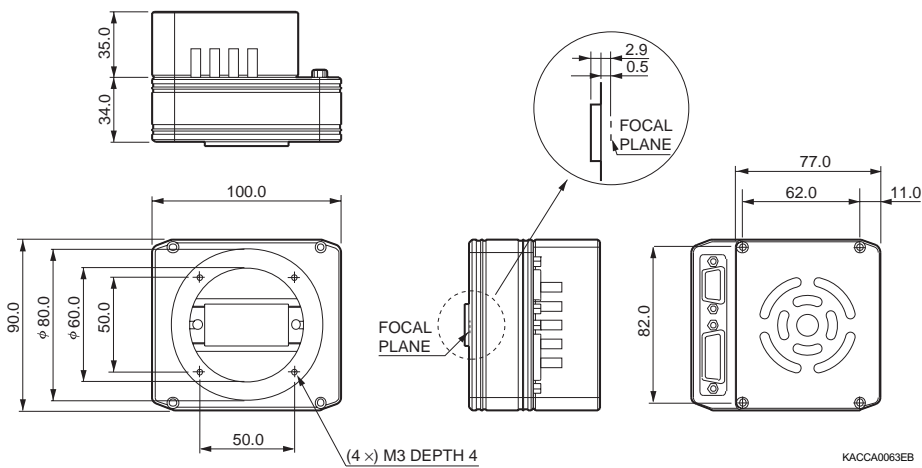
These may be connected (shorted) at the power supply end, not 9-pin D-sub connector.

Dimensional outlines (unit: mm)

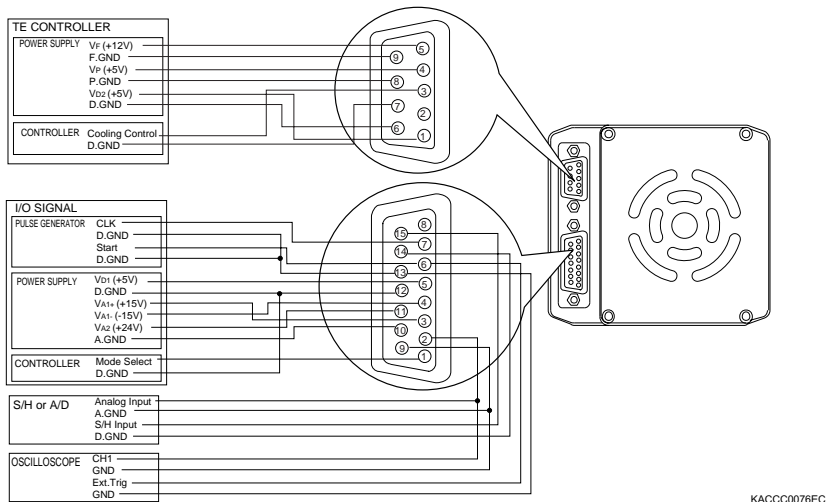
C10150



C10151



Connection example



Multichannel detector head controller C7557-01

When connected to a HAMAMATSU multichannel detector head and a personal computer, C7557 allows easy control of the detector head and data acquisition by using dedicated software that comes with the unit.

Suitable multichannel detector head

C7020/-02, C7021/-02, C7025/-02,
C7040, C7041, C7043, C7044, C7180,
C7181, C8061-01, C8062-01, C10150, C10151
C5964 series, C8892

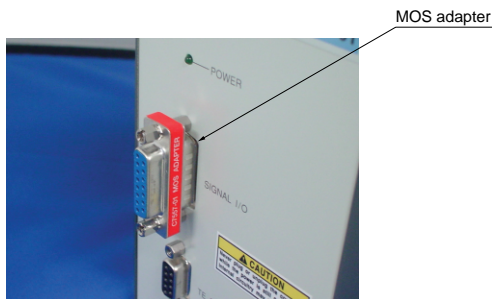
Accessories

- Spare fuse (2.5 A) *11
- AC cable
- 2 to 3 conversion adapter
- USB cable
- Detector head connection cables (for "SIGNAL I/O" and "TE CONTROL I/O" terminal of multichannel detector head)
- CD-R (MCD USB driver, software, operation manual)
- MOS adapter *12

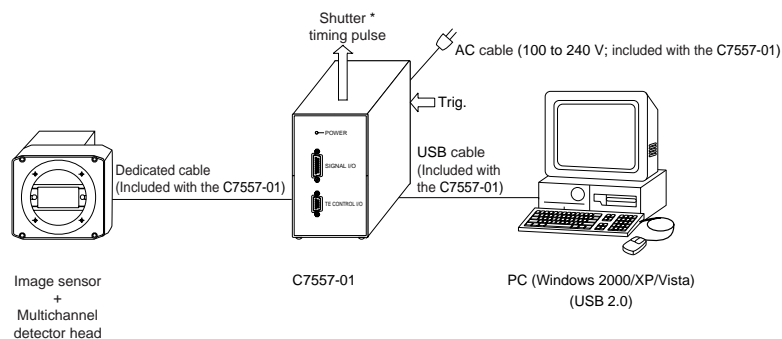


*11: Contained in the holder just above the AC cable connector on the C7557-01 rear panel.

*12: When operating the NMOS multichannel detector head, always be sure to attach the MOS adapter (supplied) to the C7557-01 main unit. If the the NMOS multichannel detector head is connected and the power is turned on without attaching the adapter, the power supply in the C7557-01 main unit may be damaged.



Connection



* Shutter, etc. are not available.

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