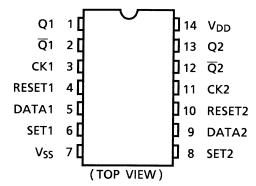
TOSHIBA CMOS Digital Integrated Circuit Silicon Monolithic

TC4013BP,TC4013BF,TC4013BFN

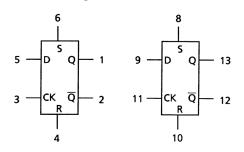
TC4013B Dual D-Type Flip Flop

TC4013B contains two independent circuits of D type flip-flop. The input level applied to DATA input are transferred to Q and \overline{Q} output by rising edge of the clock pulse. When SET input is placed at "H", and RESET input is placed at "L", outputs become Q = "H", and \overline{Q} = "L". When RESET input is placed at "H", and SET input is placed at "L", outputs become Q = "L", and \overline{Q} = "H". When both of RESET input and SET input are at "H", outputs become Q = "H" and \overline{Q} = "H".

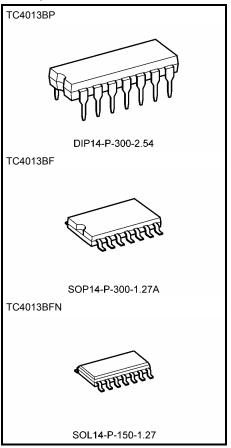
Pin Assignment



Block Diagram



Note: xxxFN (JEDEC SOP) is not available in Japan.



Weight

DIP14-P-300-2.54 : 0.96 g (typ.) SOP14-P-300-1.27A : 0.18 g (typ.) SOL14-P-150-1.27 : 0.12 g (typ.)

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Truth Table

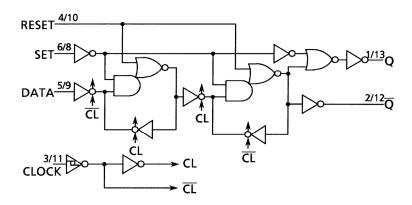
	Inp	Outputs			
RESET	SET	DATA CKA		Qn + 1	Q n + 1
L	Н	*	*	Н	L
Н	L	*	*	L	Н
Н	Н	*	*	Н	Н
L	L	L		L	Н
L	L	Н		Н	L
L	L	*	\neg	Qn [·]	Qn ·

*: Don't care

 Δ : Level change

·: No change

Logic Diagram



Absolute Maximum Ratings (Note)

Characteristics	Symbol	Rating	Unit
DC supply voltage	V_{DD}	V _{SS} - 0.5~V _{SS} + 20	V
Input voltage	V _{IN}	V _{SS} - 0.5~V _{DD} + 0.5	٧
Output voltage	V _{OUT}	V _{SS} – 0.5~V _{DD} + 0.5	٧
DC input current	I _{IN}	±10	mA
Power dissipation	P _D	300 (DIP)/180 (SOIC)	mW
Operating temperature range	T _{opr}	-40~85	°C
Storage temperature range	T _{stg}	-65~150	°C

Note: Exceeding any of the absolute maximum ratings, even briefly, lead to deterioration in IC performance or even destruction.

Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings and the operating ranges.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).



Operating Ranges (V_{SS} = 0 V) (Note)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
DC supply voltage	V_{DD}	_	3	_	18	V
Input voltage	V _{IN}	_	0	_	V_{DD}	V

Note: The operating ranges must be maintained to ensure the normal operation of the device. Unused inputs must be tied to either V_{DD} or V_{SS} .

Static Electrical Characteristics (V_{SS} = 0 V)

Characteristics :		Sym-	Test Condition		-40°C		25°C			85°C		Unit	
		bol		V _{DD} (V)	Min	Max	Min	Тур.	Max	Min	Max	Unit	
		Voн	I _{OUT} < 1 μA	5	4.95	_	4.95	5.00	_	4.95	_		
High-level output voltage	10			9.95	_	9.95	10.00	_	9.95	_	V		
			$V_{IN} = V_{SS}, V_{DD}$	15	14.95		14.95	15.00	_	14.95			
			 I _{OUT} < 1 μA	5		0.05	_	0.00	0.05	_	0.05		
Low-level voltage	output	V_{OL}	$V_{IN} = V_{SS}, V_{DD}$	10	_	0.05		0.00	0.05		0.05	V	
Ü			VIN - VSS, VDD	15	_	0.05	_	0.00	0.05	—	0.05		
			V _{OH} = 4.6 V	5	-0.61	_	-0.51	-1.0	_	-0.42	_		
			V _{OH} = 2.5 V	5	-2.50	_	-2.10	-4.0	_	-1.70	_	mA	
Output hig	h current	loh	V _{OH} = 9.5 V	10	-1.50	_	-1.30	-2.2	_	-1.10	_		
			V _{OH} = 13.5 V	15	-4.00	_	-3.40	-9.0	_	-2.80	_		
			$V_{IN}=V_{SS},V_{DD}$										
		le.	$V_{OL} = 0.4 V$	5	0.61	_	0.51	1.2	_	0.42	_	mA	
Output low	/ current		$V_{OL} = 0.5 V$	10	1.50	_	1.30	3.2	_	1.10	_		
Output low current	l _{OL}	V _{OL} = 1.5 V	15	4.00	_	3.40	12.0	_	2.80	_	ША		
		$V_{IN}=V_{SS},V_{DD}$											
		V _{IH}	V _{OUT} = 0.5 V, 4.5 V	5	3.5	_	3.5	2.75	_	3.50	_	٧	
Input high	voltage		$V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V}$	10	7.0	_	7.0	5.50	_	7.00	_		
input nign	voltage		V _{OUT} = 1.5 V, 13.5 V	15	11.0	_	11.0	8.25	_	11.00	_		
			$ I_{OUT} < 1 \mu A$										
			$V_{OUT} = 0.5 \text{ V}, 4.5 \text{ V}$	5	_	1.5	_	2.25	1.5	_	1.5	٧	
Input low voltage		V _{IL}	$V_{OUT} = 1.0 \text{ V}, 9.0 \text{ V}$	10	_	3.0	_	4.50	3.0	_	3.0		
			V _{OUT} = 1.5 V, 13.5 V	15	_	4.0	_	6.75	4.0	_	4.0		
			$ I_{OUT} < 1 \mu A$										
Input current	"H" level	ΙH	$V_{IH} = 18 V$	18		0.1		10 ⁻⁵	0.1		1.0	μА	
	"L" level	IIL	V _{IL} = 0 V	18		-0.1	_	-10^{-5}	-0.1	_	-1.0	μΛ	
			$V_{IN} = V_{SS}, V_{DD}$ (Note)	5	_	1	_	0.002	1	_	30		
Quiescent current	Quiescent supply current			10	_	2		0.004	2		60	μА	
				15	_	4	_	0.008	4	_	120		

Note: All valid input combinations.



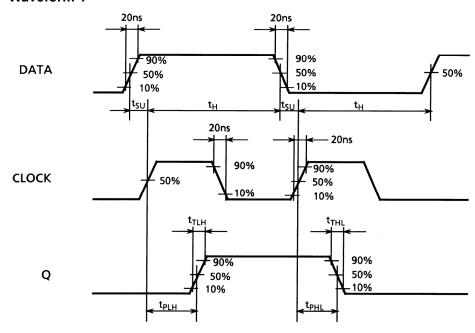
Dynamic Electrical Characteristics (Ta = 25°C, V_{SS} = 0 V, C_L = 50 pF)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit	
Onaraciensiles	Gymbol		V _{DD} (V)	IVIIII	iyρ.	IVIAX	Offic
Output transition time			5	_	70	200	
(low to high)	t _{TLH}	_	10	_	35	100	ns
(low to riigh)			15		30	80	
Output transition time			5	_	70	200	
(high to low)	t _{THL}	_	10	_	35	100	ns
(High to low)			15	_	30	80	
Dronagation dalay time	.		5	_	130	300	
Propagation delay time $(CK-Q, \overline{Q})$	t _{pLH}	_	10	_	65	130	ns
(CK-Q, Q)	t _{pHL}		15	_	50	90	
December delevition			5	_	110	300	
Propagation delay time	t _{pLH}	_	10	_	50	130	ns
(SET, RESET-Q, \overline{Q})			15	_	40	90	
			5	_	110	300	
Propagation delay time	t _{pHL}	_	10	_	50	130	ns
(SET, RESET-Q, \overline{Q})			15	_	40	90	
			5	3.5	8	_	
Max clock frequency	f _{CL}	_	10	8.0	16	_	MHz
			15	12.0	20	_	
		_	5	No limit			
Max clock input rise time	t _{rCL}		10				μS
Max clock input fall time	t _{fCL}		15				
			5	_	60	180	
Min pulse width	t _W	_	10	_	30	80	ns
(SET, RESET)			15	_	25	50	
			5	_	60	140	
Min clock pulse width	t _W	_	10	_	30	60	ns
			15	_	25	40	
			5	_	_	40	
Min set-up time	t _{su}	_	10	_	_	20	ns
(DATA-CK)			15	_	_	15	
			5	_	20	40	
Min hold time	t _H	_	10	_	10	20	ns
(DATA-CK)			15	_	6	15	
			5	_	_	40	
Min removal time	t _{rem}	_	10	_		20	ns
(SET, RESET-CK)	-10111		15	_	_	15	
Input capacitance	C _{IN}	_	I .	_	5	7.5	pF

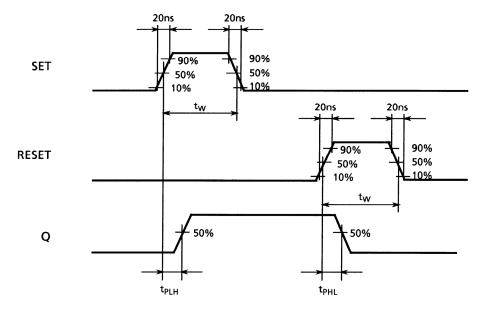
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Waveform for Measurement of Dynamic Characteristics

Waveform 1

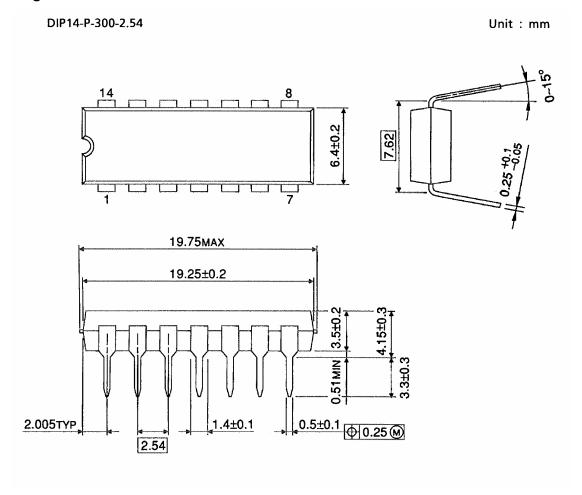


Waveform 2



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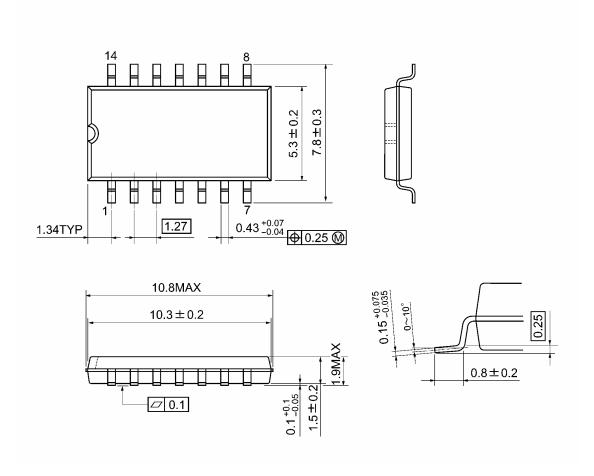
Package Dimensions



Weight: 0.96 g (typ.)

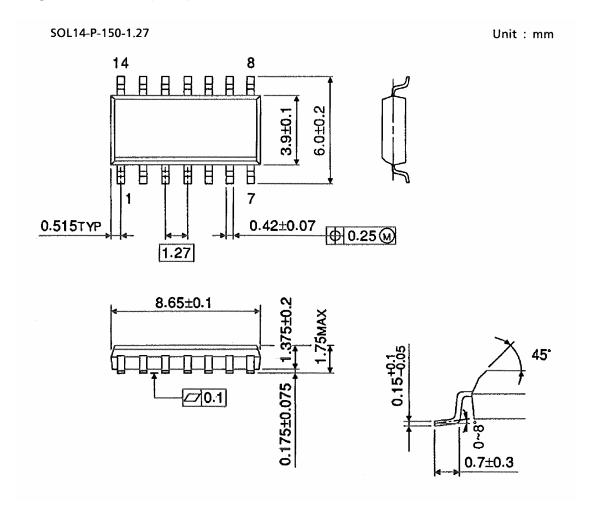
Package Dimensions

SOP14-P-300-1.27A Unit: mm



Weight: 0.18 g (typ.)

Package Dimensions (Note)



Note: This package is not available in Japan.

Weight: 0.12 g (typ.)

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20070701-EN GENERAL

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