DATA SHEET

MOS FIELD EFFECT TRANSISTOR 2SJ211

P-CHANNEL MOSFET FOR SWITCHING

The 2SJ211, P-channel vertical type MOSFET, is a switching device which can be driven directly by the output of ICs having a 5 V power source.

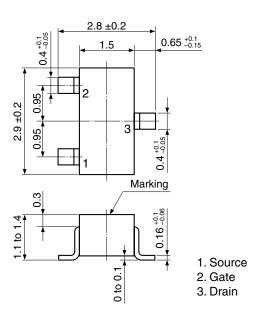
The 2SJ211 has low on-state resistance and excellent switching characteristics, it is suitable for driving actuators such as motors, relays, and solenoids.

FEATURES

NFC

- Directly driven by ICs having a 5 V power supply.
- · Not necessary to consider driving current because of its high input impedance.
- · Possible to reduce the number of parts by omitting the bias resistor.

PACKAGE DRAWING (Unit: mm)



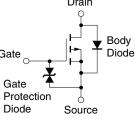
ORDERING INFORMATION

| PART NUMBER | PACKAGE | | |
|-------------|-------------------|--|--|
| 2SJ211 | SC-59 (Mini Mold) | | |

Marking: H18

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^{\circ}C$)

| Drain to Source Voltage (VGs = 0 V) | VDSS | -100 | V | EQUIVALENT CIRCUIT |
|---------------------------------------|----------|-------------|----|--------------------|
| Gate to Source Voltage (VDS = 0 V) | Vgss | ∓20 | V | Drain |
| Drain Current (DC) | ID(DC) | ∓200 | mA | Ŷ |
| Drain Current (pulse) ^{Note} | D(pulse) | ∓400 | mA | ⊢ ↓ Body |
| Total Power Dissipation | Рт | 200 | mW | Gate |
| Channel Temperature | Tch | 150 | °C | Gate |
| Storage Temperature | Tstg | –55 to +150 | °C | Protection |
| | | | | Diode Source |



Note PW \leq 10 ms, Duty Cycle \leq 50%

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Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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The mark <R> shows major revised points.

The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field. Downloaded from Elcodis.com electronic components distributor

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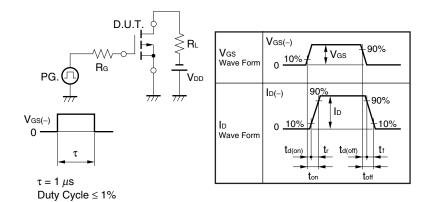
ELECTRICAL CHARACTERISTICS (T_A = 25°C)

| | | | 1 | 1 | 1 | |
|--|----------------------|---|------|------|-------------|------|
| CHARACTERISTICS | SYMBOL | TEST CONDITIONS | MIN. | TYP. | MAX. | UNIT |
| Zero Gate Voltage Drain Current | IDSS | V_{DS} = -100 V, V_{GS} = 0 V | | | -1.0 | μA |
| Gate Leakage Current | lgss | $V_{GS} = \pm 20 \text{ V}, V_{DS} = 0 \text{ V}$ | | | ∓1.0 | μA |
| Gate Cut-off Voltage | V _{GS(off)} | $V_{DS} = -5.0 \text{ V}, \text{ I}_{D} = -1.0 \mu\text{A}$ | -1.4 | -1.8 | -2.4 | V |
| Forward Transfer Admittance Note | y _{fs} | $V_{DS} = -5.0 V$, $I_D = -10 mA$ | 20 | 45 | | mS |
| Drain to Source On-state Resistance Note | RDS(on)1 | V_{GS} = -4.0 V, I _D = -10 mA | | 15 | 30 | Ω |
| | RDS(on)2 | $V_{GS} = -10 \text{ V}, \text{ I}_D = -10 \text{ mA}$ | | 11 | 20 | Ω |
| Input Capacitance | Ciss | V _{DS} = -5.0 V | | 27 | | pF |
| Output Capacitance | Coss | V _{GS} = 0 V | | 16 | | pF |
| Reverse Transfer Capacitance | Crss | f = 1 MHz | | 2 | | pF |
| Turn-on Delay Time | td(on) | V_{GS} = -4.0 V, R_{G} = 10 Ω | | 110 | | ns |
| Rise Time | tr | V _{DD} = -5.0 V | | 150 | | ns |
| Turn-off Delay Time | td(off) | I₀ = –10 mA | | 160 | | ns |
| Fall Time | tr | | | 150 | | ns |

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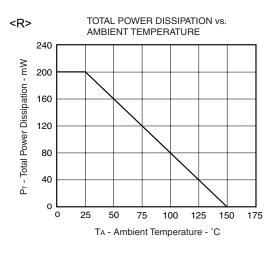
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TEST CIRCUIT SWITCHING TIME

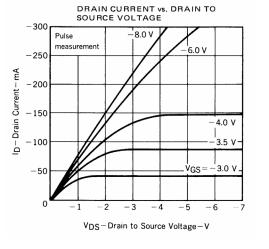


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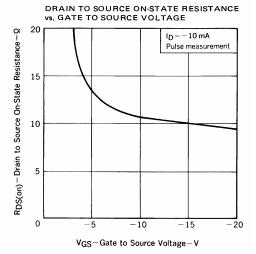


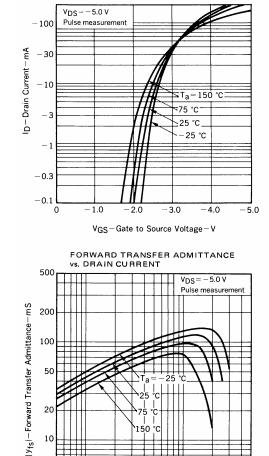


TRANSFER CHARACTERISTICS



GATE TO SOURCE CUTOFF VOLTAGE vs. CHANNEL TEMPERATURE -2.2 $V_{DS} = -5.0 V$ $i_{D} = -1 \mu A$ VGS(off)-Gate Cut-Off Voltage-V -2.0 -1.8-1.6 -1.4-1.2-1.0-0.8 -30 0 30 60 90 120 150 T_{ch}-Channel Temperature-°C





25

50

-50 -100

ID-Drain Current-mA

-200

-500

50

20

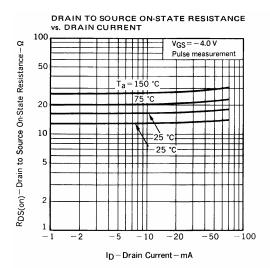
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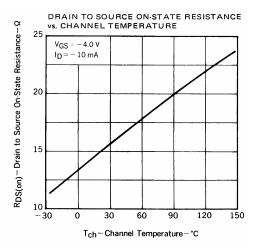
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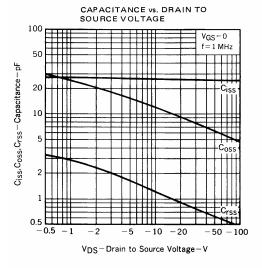
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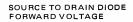
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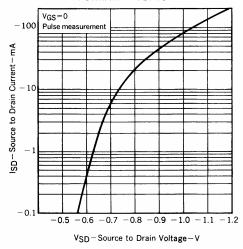


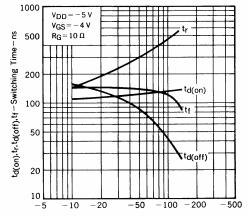


SWITCHING CHARACTERISTICS









ID-Drain Current-mA

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