International Rectifier

IRFK2D150,IRFK2F150

Isolated Base Power HEX-pakTM Assembly - Half Bridge Configuration

- High Current Capability.
- · UL recognised E78996.
- · Electrically Isolated Base Plate.
- · Easy Assembly into Equipment.

Description

The HEX-pakTM utilises the well-proven HEXFETTM die, combining low on-state resistance with high transconductance. These superior technology die are assembled by state of the art techniques into the TO-240 package, featuring 2.5kV rms isolation and solid M5 screw connections. The small footprint means the package is highly suited to power applications where space is a premium. Available in two versions, IRFK.D... for fast switching and IRFK.F... for oscillation sensitive applications.

V _{DS} = 100V
$R_{DS(on)} = 28m\Omega$
I _D = 72A

Absolute Maximum Rating

	Parameter	Max.	Units
I _D @ T _C =25°C	Continuous Drain Current	72	Α
I _D @ T _C =100°C	Continuous Drain Current	45	Α
I _{DM}	Pulse Drain Current	288	A ①
P _D @ T _C =25°C	Maximum Power Dissipation	500	W
V _{GS}	Gate-to-Source Voltage	20	V
V _{INS}	R.M.S. Isolation Voltage, circuit to base	2.5	kV
TJ	Operating Junction Temperature Range	-40 to 150	°C
⊤ _{STG}	Storage Temperature Range	-40 to 150	°C

Thermal and Mechanical Specifications

	Parameter	Min.	Тур.	Max.	Units
R _{thJC}	Junction-to-Case	-	-	0.25	K/W 2
R _{thCS}	Case-to-Sink, smooth & greased surface	-	0.1	-	K/W
Т	Mounting Torque +10%				3
	HEXpak to Heatsink	-	5	=	Nm
	Busbar to HEXpak	-	3		Nm
wt	Approximate Weight	-	140	-	g
		-	5	-	" OZ

Notes:

- ① Repetitive Rating: Pulse width limited by maximum junction temperature see figure 8.
- 2 Per Module.
- ③ A mounting compound is recommended and the torque should be rechecked after a period of three hours to allow for the spread of the compound.

Electrical Characteristics @ T_J = 25°C (Unless otherwise specified)

	Parameter		Min.	Тур.	Max.	Units	Test Conditions
B _{VDSS}	Drain-to-Source Breakdo	own	100	-	-	V	V _{GS} =0V, I _D =1.0mA
R _{DS(on)}	Static Drain-to-Source		-	23	28	mΩ	V _{GS} =10V, I _D =45A
	On-State Resistance				<u> </u>		
I _{D(on)}	On-State Drain Current		72	-	-	Α	V _{DS} > I _{D(on)} x R _{DS(on)} max,
, ,							V _{GS} =10V
V _{GS(th)}	Gate Threshold Voltage		2.0	-	4.0	٧	V _{DS} =V _{GS} , I _D =1.0mA
g _{fs}	Forward Transconductance 4		26	40	-	S	V _{DS} > 50V, I _D =45A
I _{DSS}	Zero Gate Voltage Drain	Current	-	-	0.5	mA	V _{DS} =V _{DS} max, V _{GS} =0v
			-	-	2.0	mA	V _{GS} =10V, T _C =125°C,
						V _{DS} =V _{DS} max x 0.8	
I _{GSS}	Gate-to-Source Leakage Forward		-	-	200	nA	V _{GS} =20V
I _{GSS}	Gate-to-Source Leakage Reverse		<u> </u>	-	-200	nA	V _{GS} =-20V
Q _q	Total Gate Charge		-	180	250	nC	I _D =120A, V _{GS} =10V,
Q _{gs}	Gate-to-Source Charge			40	50	nC	V _{DS} =V _{DS} max x 0.8
Q _{gd}	Gate-to-Drain ("Miller") Charge		-	80	125	nC	
t _{d(on)}	Turn-on Delay Time	IRFK2D150	-	60		ns	V _{DD} =40V, I _D =45A,
, ,		IRFK2F150	-	70	-	ns	
t _r	Rise Time	IRFK2D150	-	150	-	ns	V _{GS} =10V,
		IRFK2F150	-	190	-	ns	
t _{d(off)}	Turn-off Delay Time	IRFK2D150	-	160	-	ns_	R _{SOURCE} =3.3Ω
		IRFK2F150	-	210		ns	
t _f	Fall Time	IRFK2D150	-	60		ns	
		IRFK2F150	-	100	-	ns	
L _{DS}	Drain-to-Source Inductance		-	18	-	nH	_
C _{iss}	Input Capacitance		-	5.3	-	nF	V _{GS} =0V, V _{DS} =25V,
Coss	Output Capacitance		-	2.0	-	nF	f=1.0MHz
C _{rss}	Reverse Transfer Capacitance		-	0.6	-	nF	
-	Linear Derating Factor		-	-	4	W/K	

Source-Drain Diode Ratings and Characteristics

	Parameter	Min.	Тур.	Max.	Units	Test Conditions
Is	Continuous Source Current (Body Diode)	-	-	72	Α	
I _{SM}	Pulsed Source Current (Body Diode)	-	_	250	Α	
V _{SD}	Diode Forward Voltage	-	-	2.5	V	V _{GS} =0V, I _S = 72A, T _C =25°C
t _{rr}	Reverse Recovery Time	90	190	390	ns	di/dt=200A/μs, T _J =150°C
Q _{rr}	Reverse Recovered Charge	1.5	3.2	6.8	μC	I _S =72A

Notes:

④ - Pulse Width ≤ 300 μ s; Duty cycle ≤ 2%.

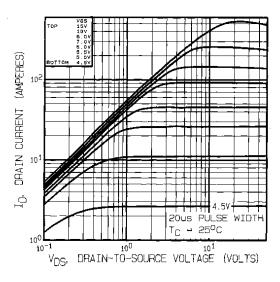


Fig 1. Typical Output Characteristics, $\rm T_{C}{=}25^{o}C$

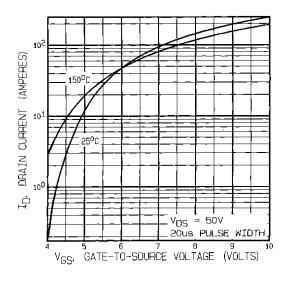


Fig 3. Typical Transfer Characteristics

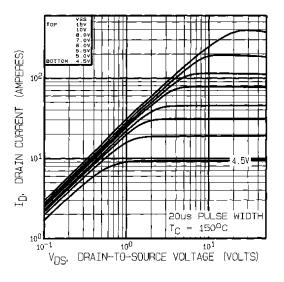


Fig 2. Typical Output Characteristics, $T_C=150^{\circ}C$

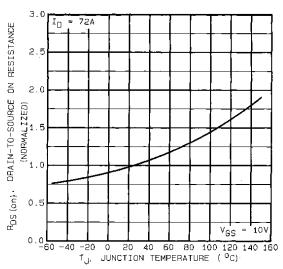


Fig 4. Normalized On-Resistance Vs. Temperature

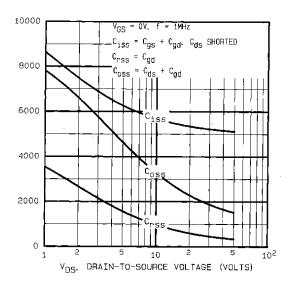


Fig 5. Typical Capacitance Vs. Drain-to-Source Voltage

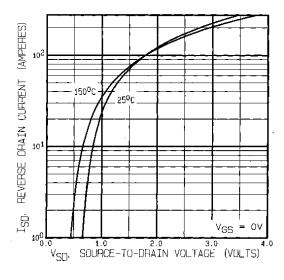


Fig 7. Typical Source-Drain Diode Forward Voltage

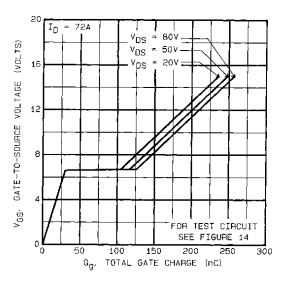


Fig 6. Typical Gate Charge Vs. Gate-to-Source Voltage

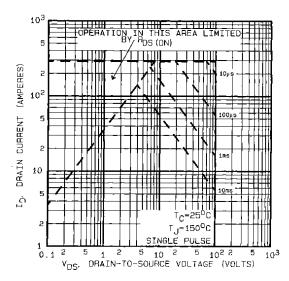


Fig 8. Maximum Safe Operating Area

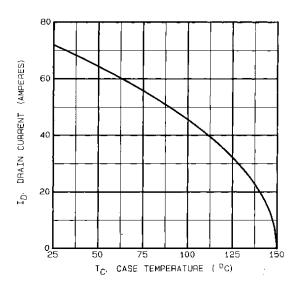


Fig 9. Maximum Drain Current Vs.
Case Temperature

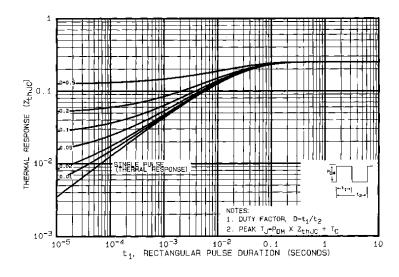


Fig 10. Maximum Effective Transient Thermal Impedance, Junction-to-Case

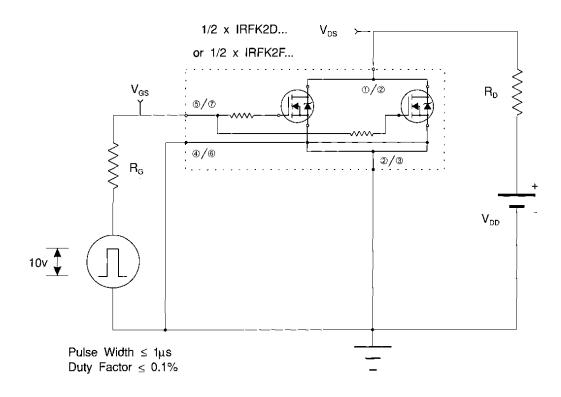


Fig 11a. Switching Time Test Circuit

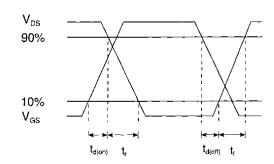
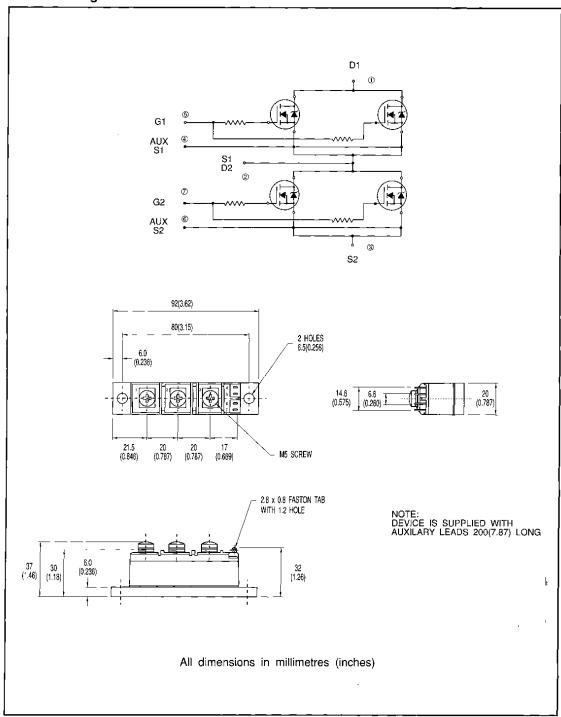


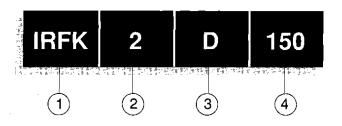
Fig 11b. Switching Time Waveforms

IRFK2D150,IRFK2J150

Circuit Configuration and Outline



Part Numbering



- HEX-pak Module.
- Number of arms of bridge.
- D Fast switching.
 - F Oscillation resistant for sensitive applications.
- Voltage code:-4.

054 - 60V

150 - 100V

250 - 200V

350 - 400V

450 - 500V

C50 - 600V

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