TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

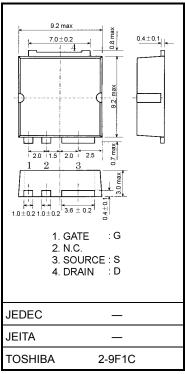
2SK3438

DC-DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON resistance: $RDS(ON) = 0.74 \Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 4.5 \text{ S} (typ.)$
- Low leakage current: $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement mode: $V_{th} = 3.0 \sim 5.0 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V _{DSS}	600	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)		V _{DGR}	600	V	
Gate-source voltage		V _{GSS}	±30	V	
Drain current	DC (Note 1)	ID	10		
	Pulse (Note 1)	I _{DP}	30	A	
Drain power dissipation (Tc = 25° C)		PD	80	W	
Single pulse avalanche energy (Note 2)		E _{AS}	252	mJ	
Avalanche current		I _{AR}	10	А	
Repetitive avalanche energy (Note 3)		E _{AR}	8	mJ	
Channel temperature		T _{ch}	150	°C	
Storage temperature range		T _{stg}	-55~150	°C	



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

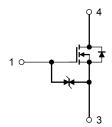
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.56	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$ (initial), L = 4.41 mH, R_G = 25 Ω , I_{AR} = 10 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Handle with care.



Unit: mm

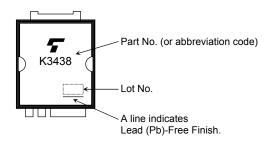
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS}=\pm 25~V,~V_{DS}=0~V$	_		±10	μΑ
Drain-source breakdown voltage		V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30		_	V
Drain cut-OFF current		I _{DSS}	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_		100	μA
Drain-source brea	akdown voltage	V (BR) DSS	$I_D=10\ mA,\ V_{GS}=0\ V$	600		_	V
Gate threshold vo	oltage	V _{th}	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0		5.0	V
Drain-source ON	resistance	R _{DS (ON)}	$V_{GS}=10~V,~I_D=5~A$	_	0.74	1.0	Ω
Forward transfer	admittance	Y _{fs}	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		4.5	_	S
Input capacitance		C _{iss}	V_{DS} = 25 V, V_{GS} = 0 V, f = 1 MHz	_	1200	_	pF
Reverse transfer capacitance		C _{rss}		_	10	_	
Output capacitance		C _{oss}		_	130	_	
Switching time	Rise time	tr	$V_{GS}^{10 \text{ V}} \downarrow_{DD} = 5 \text{ A VOUT}$ $V_{GS}^{C} \downarrow_{DD} = 60 \Omega$ $V_{DD} \simeq 300 \text{ V}$ Duty $\leq 1\%, t_{W} = 10 \mu \text{s}$		13	_	- ns
	Turn-ON time	t _{on}			40		
	Fall time	tf			8		
	Turn-OFF time	t _{off}		—	50		
Total gate charge (gate-source plus gate-drain)		Qg			28		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$	—	16	—	
Gate-drain ("miller") charge		Q _{gd}		_	12	—	

Source-Drain Ratings and Characteristics (Ta = 25°C)

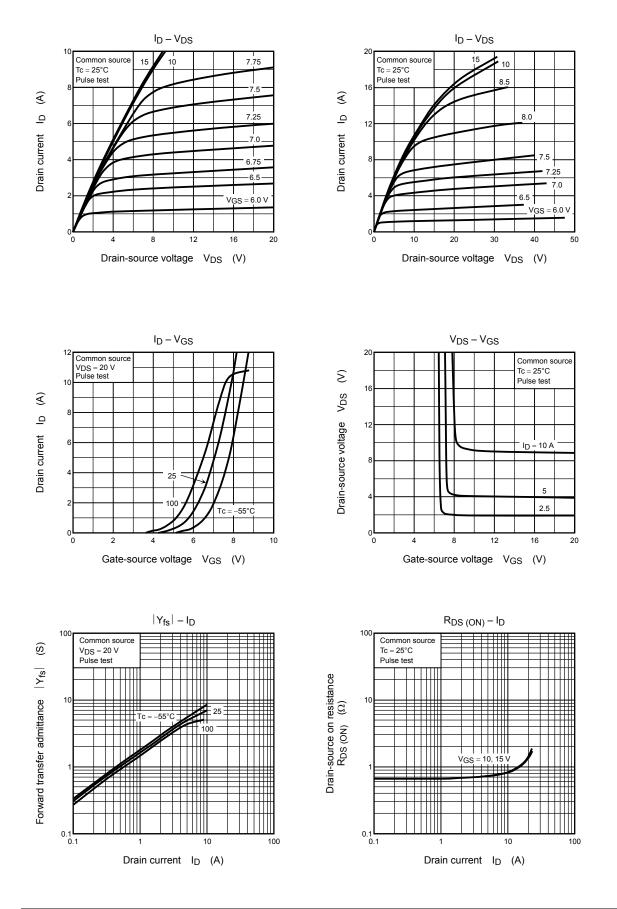
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I _{DR}	—	_	_	10	А
Pulse drain reverse current (Note 1)	I _{DRP}	_	_	_	30	А
Forward voltage (diode)	V _{DSF}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	1600	_	ns
Reverse recovery charge	Qrr	dI _{DR} /dt = 100 A/μs		17		μC

Marking

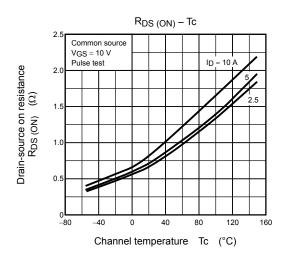


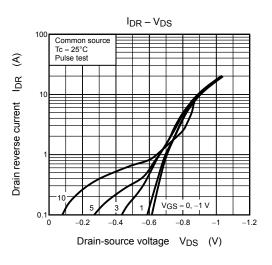
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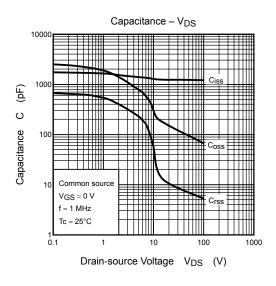
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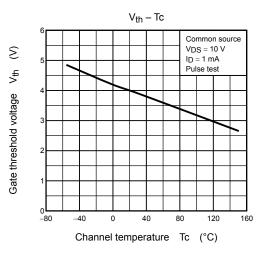


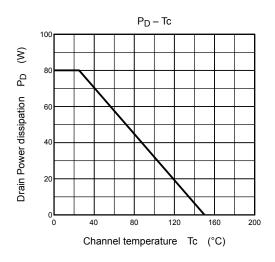
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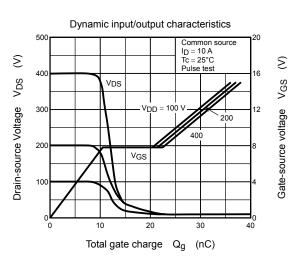


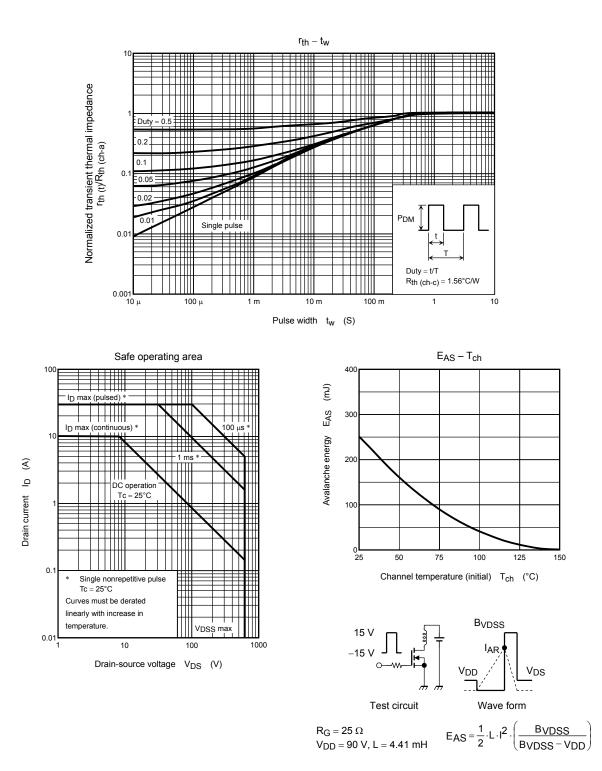












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