TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

# 2SK3437

## DC-DC Converter, Relay Drive and Motor Drive Applications

- Low drain-source ON resistance:  $RDS(ON) = 0.74 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 4.5 \text{ S} (typ.)$
- Low leakage current:  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 600 \ V)$
- Enhancement mode:  $V_{th} = 3.0 \sim 5.0 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	600	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		VDGR	600	V	
Gate-source voltage		V <sub>GSS</sub>	±30	V	
Drain current	DC (Note 1)	ID	10	A	
	Pulse (Note 1)	I <sub>DP</sub>	30		
Drain power dissipation (Tc = $25^{\circ}$ C)		PD	80	W	
Single pulse avalanche energy (Note 2)		E <sub>AS</sub>	252	mJ	
Avalanche current		I <sub>AR</sub>	10	А	
Repetitive avalanche energy (Note 3)		E <sub>AR</sub>	8	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature range		T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc.).

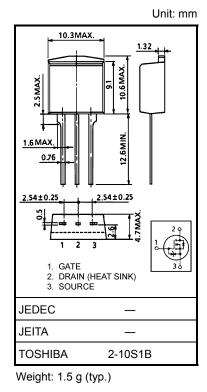
#### **Thermal Characteristics**

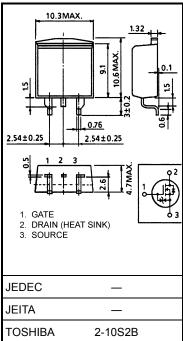
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	1.56	°C/W
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	83.3	°C/W

Note 1: Ensure that the channel temperature does not exceed 150°C.

- Note 2: V\_{DD} = 90 V, T\_{ch} = 25 ^{\circ}C (initial), L = 4.41 mH, R\_G = 25  $\Omega,$  I\_{AR} = 10 A
- Note 3: Repetitive rating: Pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)

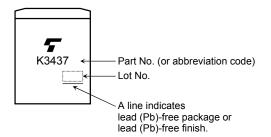
#### **Electrical Characteristics (Ta = 25°C)**

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I <sub>GSS</sub>	$V_{GS}=\pm 25~V,~V_{DS}=0~V$	_	—	±10	μΑ
Drain-source breakdown voltage		V (BR) GSS	$I_G=\pm 10~\mu A,~V_{DS}=0~V$	±30	—		V
Drain cut-OFF current		I <sub>DSS</sub>	$V_{DS} = 600 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_	_	V
Gate threshold vo	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	3.0	_	5.0	V
Drain-source ON	resistance	R <sub>DS (ON)</sub>	$V_{GS}=10~V,~I_D=5~A$	_	0.74	1.0	Ω
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 15 \text{ V}, \text{ I}_{D} = 5 \text{ A}$	2.0	4.5	_	S
Input capacitance		C <sub>iss</sub>	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	_	1200	_	pF
Reverse transfer capacitance		C <sub>rss</sub>		_	10		
Output capacitance		C <sub>oss</sub>			130		
Switching time	Rise time	tr	$V_{GS}^{10 \text{ V}} \downarrow_{D} = 5 \text{ A}  V_{OUT}$ $\downarrow_{QS}^{OV} \downarrow_{QS}^{OV} \downarrow_{QS}^{OV} \downarrow_{QS}^{OV}$ $\downarrow_{QS}^{OV} \downarrow_{QS}^{OV} \downarrow_{QS}^{OV} \downarrow_{QD}^{OV} \approx 300 \text{ V}$ $J_{Duty} \leq 1\%, t_{W} = 10 \mu\text{s}$	_	13	—	- ns
	Turn-ON time	t <sub>on</sub>			40		
	Fall time	tf			8	_	
	Turn-OFF time	t <sub>off</sub>			50		
Total gate charge (gate-source plus gate-drain)		Qg			28		nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \simeq 400 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 10 \text{ A}$		16	—	
Gate-drain ("miller") charge		Q <sub>gd</sub>	]	_	12	_	

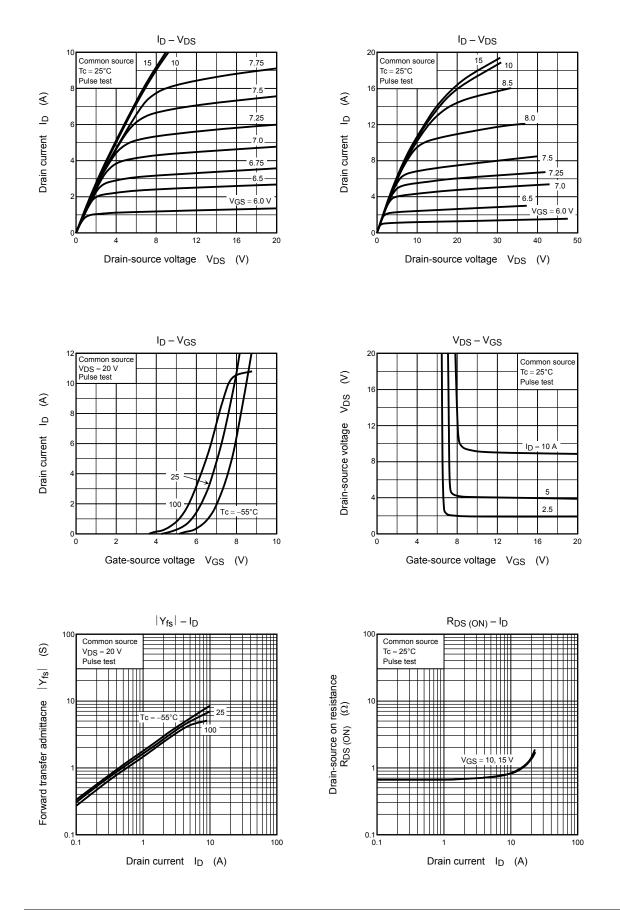
#### Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	IDR	—	_	_	10	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	30	А
Forward voltage (diode)	V <sub>DSF</sub>	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$	_	_	-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	_	1600	_	ns
Reverse recovery charge	Qrr	dI <sub>DR</sub> /dt = 100 A/µs		17	_	μC

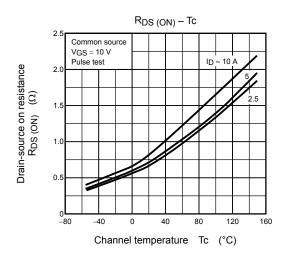
#### Marking

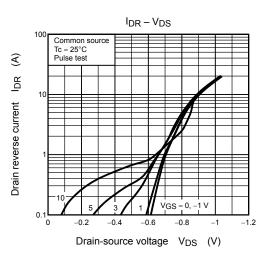


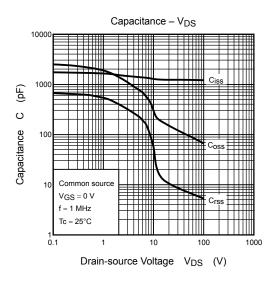
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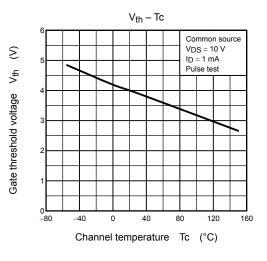


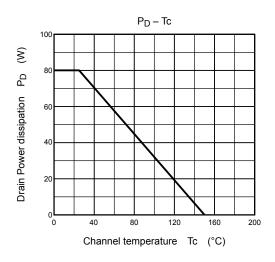
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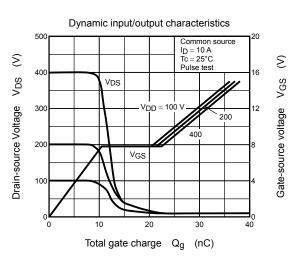


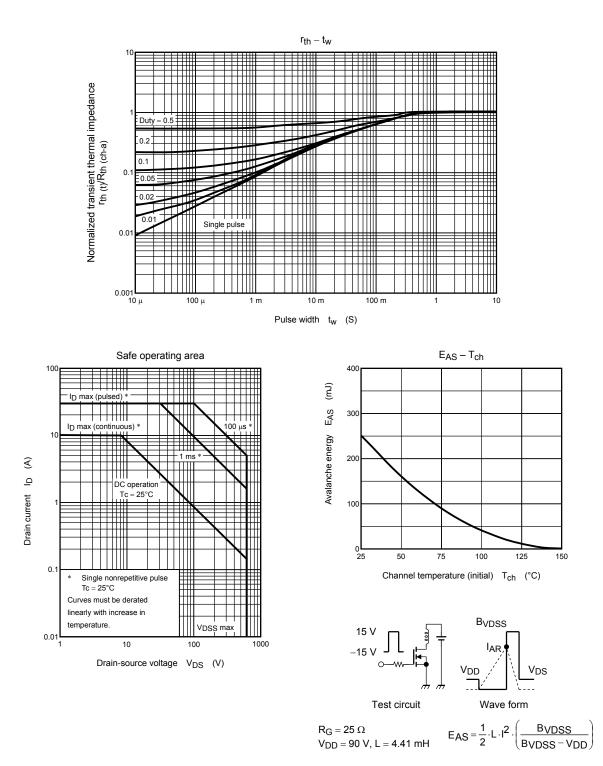












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