Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (π-MOSV)

2SK3310

Switching Regulator Applications

• Low drain-source ON resistance: RDS (ON) = 0.48Ω (typ.)

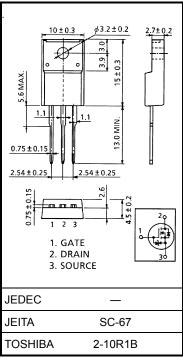
• High forward transfer admittance: $|Y_{fs}| = 4.3 \text{ S (typ.)}$

• Low leakage current: $IDSS = 100 \mu A (max) (VDS = 450 V)$

• Enhancement model: $V_{th} = 3.0 \sim 5.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics			Symbol	Rating	Unit	
Drain-source voltage			V_{DSS}	450	V	
Drain-gate voltage ($R_{GS} = 20 \text{ k}\Omega$)			V_{DGR}	450	V	
Gate-source voltage			V _{GSS}	±30	V	
Drain current	DC	(Note 1)	ΙD	10	Α	
	Pulse	(Note 1)	I _{DP}	40		
Drain power dissipation (Tc = 25°C)			P _D	40	W	
Single pulse avalanche energy (Note 2)			E _{AR}	222	mJ	
Avalanche current			I _{AR}	10	Α	
Repetitive avalanche energy (Note 3)			E _{AR}	4	mJ	
Channel temperature			T _{ch}	150	°C	
Storage temperature range			T _{stg}	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R _{th (ch-c)}	3.125	°C/W	
Thermal resistance, channel to ambient	R _{th (ch-a)}	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2: $V_{DD} = 90 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (initial), L = 3.7 mH, $R_G = 25 \Omega$, $I_{AR} = 10 \text{ A}$

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.



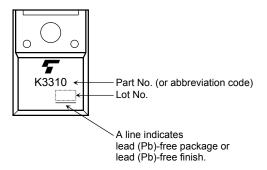
Electrical Characteristics (Ta = 25°C)

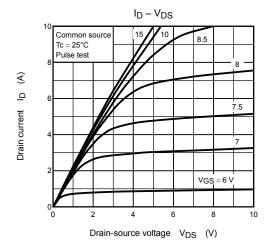
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0 \text{ V}$	_	_	±10	μΑ
Gate -source brea	kdown voltage	V (BR) GSS	$I_G = \pm 10 \ \mu A, \ V_{DS} = 0 \ V$	±30		_	V
Drain cut-off curre	nt	I _{DSS}	V _{DS} = 450 V, V _{GS} = 0 V	_	_	100	μА
Drain-source brea	kdown voltage	V (BR) DSS	$I_D = 10$ mA, $V_{GS} = 0$ V	450	_	_	V
Gate threshold voltage		V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON resistance		R _{DS} (ON)	$V_{GS} = 10 \text{ V}, I_D = 5 \text{ A}$	_	0.48	0.65	Ω
Forward transfer a	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 5 A	1.5	4.3	_	S
Input capacitance		C _{iss}		_	920	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	12	_	pF
Output capacitano	e	Coss		_	140		
Switching time	Rise time	t _r	10 V	_	25		ns
	Turn-on time	t _{on}	0 V		35	_	
	Fall time	t _f	0 \$		10	_	
	Turn-off time	t _{off}	Duty ≦ 1%, t _W = 10 μs	_	60		
Total gate charge		Qg		_	23		nC
Gate-source charge		Q _{gs}	$V_{DD} \simeq 360 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 10 \text{ A}$		9		
Gate-drain charge		Q _{gd}			14		

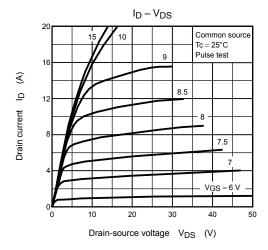
Source-Drain Ratings and Characteristics (Ta = 25°C)

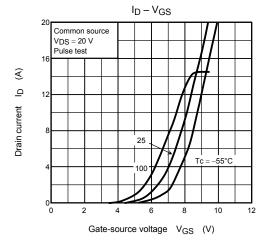
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I_{DR}	_		_	10	Α
Pulse drain reverse current (Note 1)	I _{DRP}	_			40	Α
Forward voltage (diode)	V_{DSF}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V}$			-1.7	V
Reverse recovery time	t _{rr}	$I_{DR} = 10 \text{ A}, V_{GS} = 0 \text{ V},$	-	280	_	ns
Reverse recovery charge	Q_{rr}	$dI_{DR}/dt = 100 \text{ A}/\mu\text{s}$	_	2.7	_	μС

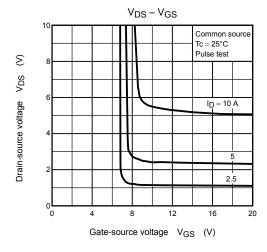
Marking

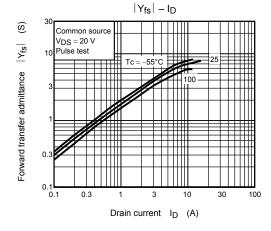


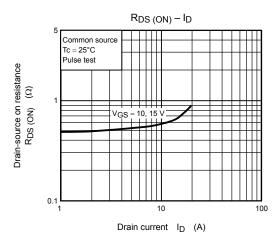




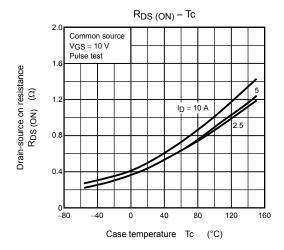


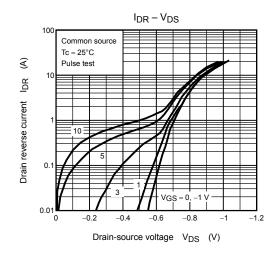


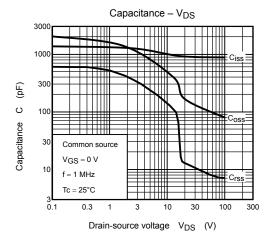


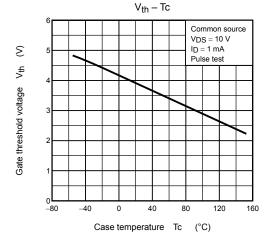


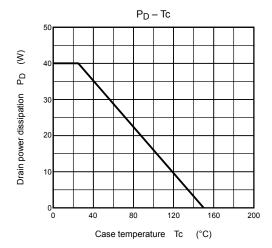
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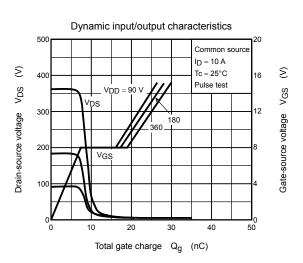




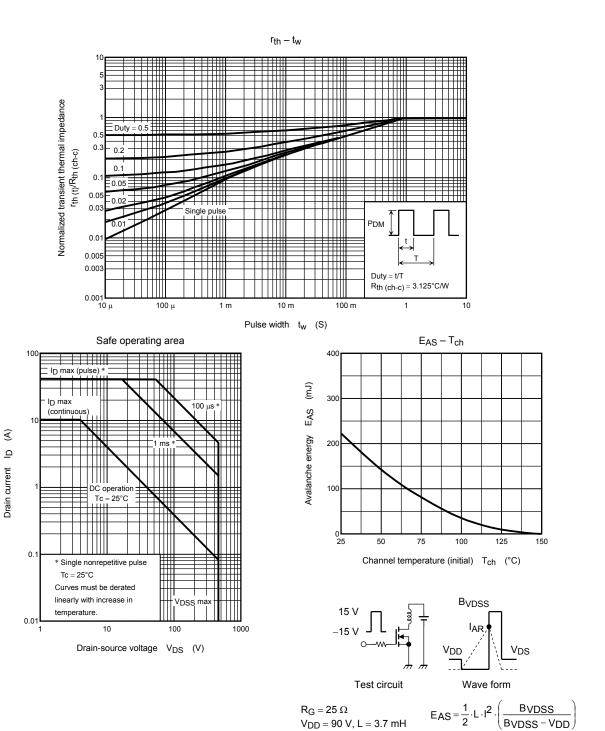








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