TOSHIBA

Discrete Semiconductors

Field Effect Transistor

Silicon N Channel MOS Type (r-MOS II.5)

High Speed, High Current Switching Applications

Features

- Low Drain-Source ON Resistance
- $R_{DS(ON)} = 1.0\Omega$ (Typ.) High Forward Transfer Admittance
- $|Y_{fs}| = 4.0S$ (Typ.)
- Low Leakage Current
- $I_{DSS} = 300 \mu A$ (Max.) ($V_{DS} = 0V$)
- Enhancement-Mode
 - $V_{th} = 1.5 \sim 3.5V$ ($V_{DS} = 10V$, $I_D = 1mA$)

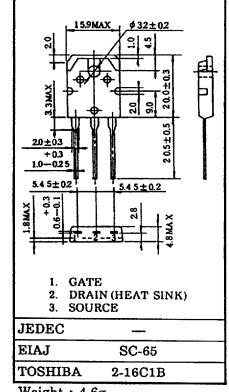
Absolute Maximum Ratings (Ta = 25C)

CHARACTERISTIC Drain-Source Voltage Drain-Gate Voltage (R _S = 20kΩ)		SYMBOL	RATING	UNIT V V	
		V _{DSS} V _{DGR}	800		
			800		
Gate-Source Voltage		Vass	±30	٧	
Drain Current	DC	b	9	A	
	Pulse	I _{DP}	27		
Drain Power Dissipation (Tc = 25°C)		PD	150	W	
Channel Temperature		եր	150	°C	
Storage Temperature Range		J tg	-55 ~ 150	°C	

Thermal Characteristics

CHARACTERISTIC	SYMBOL	MAX.	UNIT
Thermal Resistance, Channel to Case	R(ch-c)	0.833	°C/W
Thermal Resistance, Channel to Ambient	fi(ch-a)	50	°C/W

This transister is an electrostatic sensitive device. Please handle with caution.



Industrial Applications

Weight: 4.6g

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Unit in mm

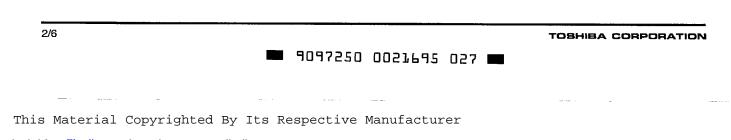
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Electrical Characteristics (Ta = 25C)

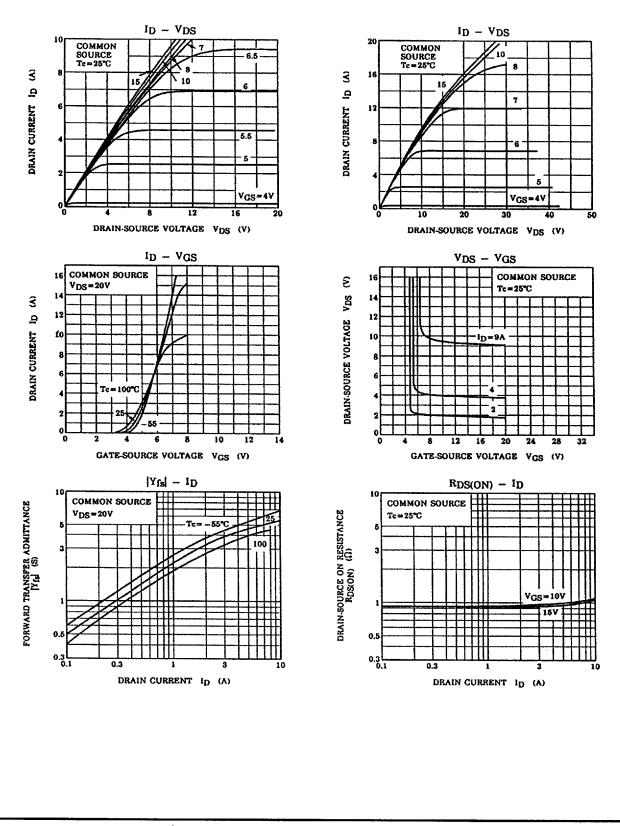
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Gate Leakage Current		6ss -	$V_{GS} = \pm 30V, V_{DS} = 0V$	-	-	±100	nA
Drain Cut-off Current		IDSS	$V_{DS} = 800V, V_{GS} = 0V$	-	-	100	μA
Drain-Source Breakdown Voltage		(HR) DSS	$I_D = 10$ mA, $V_{GS} = 0V$	800	-	-	٧
Gate Threshold Voltage		Yn	V _{DS} = 10V, b = 1mA	1.5	-	3.5	٧
Drain-Source ON	Resistance	Pos (ON)	V _{GS} = 10V, I _D = 4A		1.0	1.2	Ω
Forward Transfer	Admittance	IY _{1s} I	$V_{DS} = 15V, b = 4A$	2.0	40	-	S
Input Capacitance Reverse Transfer Capacitance Output Capacitance		G _{ISS}		-	1150	-	рF
		Ç _{ss}	V _{DS} = 25V, V _{GS} = 0V, f = 1MHz	-	135	-	
		Coss		-	210	-	
Switching Time Turn-on T Fall Time	Rise Time	tr		-	35	-	
	Turn-on Time	bn	$I_{D}=4A$	-	55	-	ПS
	Fall Time	ŧ		-	25	-	
	Turn-off Time	\$If	$V_{DD} = 400V$ $V_{IN} : t_r, t_f < 5ns,$ $Duty \le 1\%, t_W = 10\mu s$	-	100	-	
Total Gate Charge (Gate-Source Plus Gate-Drain) Gate-Source Charge		Qg	$V_{DD} = 400V, V_{GS} = -10V,$	-	85	-	
		Q _{ps}	I _D = 9A	-	40	-	nC
Gate-Drain ("Mill	Gate-Drain ("Miller") Charge]	-	45	-	

Source-Drain Diode Ratings and Characteristics (Ta = 25C)

CHARACTERISTICS	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Continuous Drain Reverse Current	dr	_	-	-	9	A
Pulse Drain Reverse Current	ØRP	_	-	-	27	A
Diode Forward Voltage	YOSF	$I_{DR} = 9A, V_{GS} = 0V$	-	-	-2.0	٧
Reverse Recovery Time	ŧ	$I_{DR} = 9A, V_{GS} = 0V$	-	300	-	ns
Reverse Recovered Charge	Q.	dl _{DR} / _{dl} = 100A/µs	-	26	-	μC



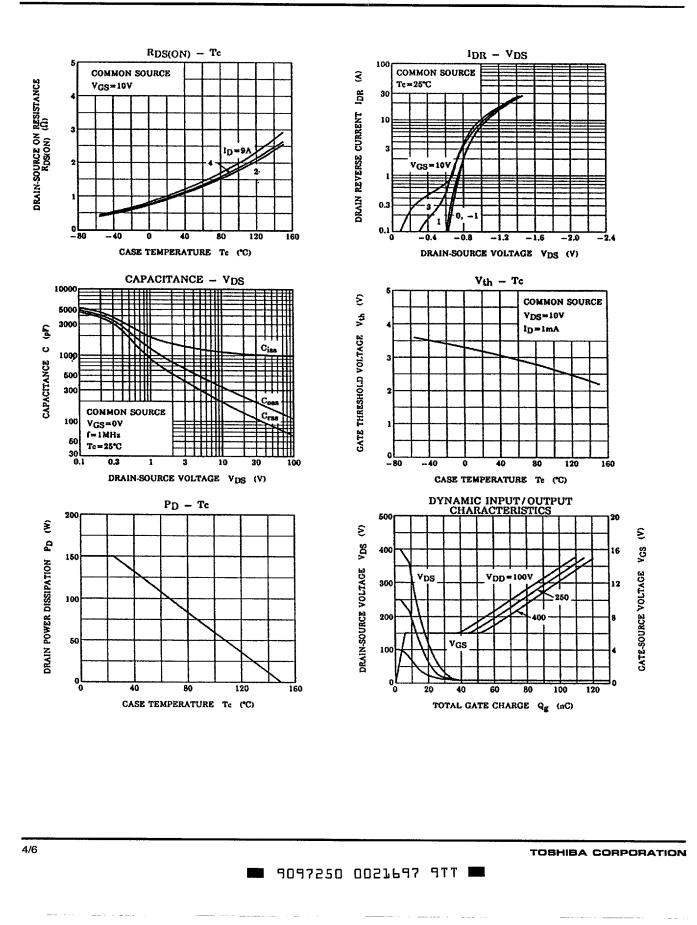
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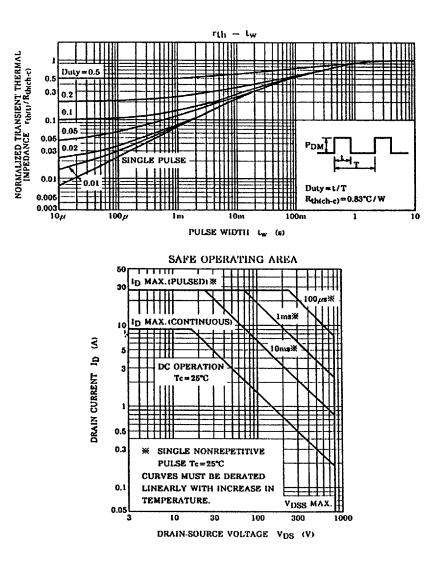
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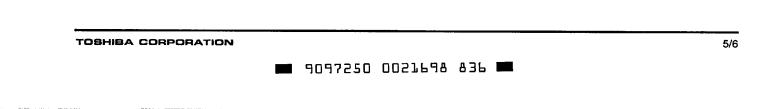
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