TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (L<sup>2</sup>-π-MOSV)

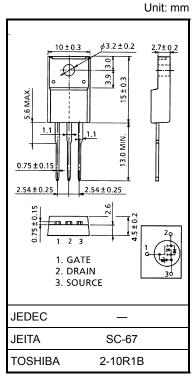
# 2SK2882

Chopper Regulator, DC-DC Converter and Motor Drive Applications

- 4-V gate drive
- Low drain-source ON resistance:  $RDS(ON) = 0.08 \Omega$  (typ.)
- High forward transfer admittance:  $|Y_{fs}| = 17 \text{ S}$  (typ.)
- Low leakage current:  $I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 150 \ V)$
- Enhancement mode:  $V_{th} = 0.8 \sim 2.0 \text{ V} (V_{DS} = 10 \text{ V}, I_D = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	150	V	
Drain-gate voltage ( $R_{GS} = 20 \text{ k}\Omega$ )		V <sub>DGR</sub>	150	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	ID	18	٨	
	Pulse (Note 1)	I <sub>DP</sub>	54	A	
Drain power dissipation	n (Tc = 25°C)	PD	45	W	
Single pulse avalanche	e energy (Note 2)	E <sub>AS</sub>	176	mJ	
Avalanche current		I <sub>AR</sub>	18	А	
Repetitive avalanche e	nergy (Note 3)	E <sub>AR</sub>	4.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature ra	ange	T <sub>stg</sub>	-55~150	°C	



Weight: 1.9 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

Characteristics	Symbol	Max	Unit	
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	2.78	°C/W	
Thermal resistance, channel to ambient	R <sub>th (ch-a)</sub>	62.5	°C/W	

Note 1: Ensure that the channel temperature does not exceed 150°C.

Note 2:  $V_{DD} = 50 \text{ V}, \text{ T}_{ch} = 25^{\circ}\text{C}$  (initial), L = 0.8 mH, R<sub>G</sub> = 25  $\Omega$ , I<sub>AR</sub> = 18 A

Note 3: Repetitive rating: pulse width limited by maximum junction temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.

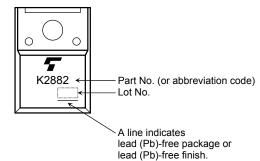
## **Electrical Characteristics (Ta = 25°C)**

Char	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I <sub>GSS</sub>	$V_{GS} = \pm 16 \text{ V}, V_{DS} = 0 \text{ V}$		—	±10	μA
Drain cut-off curr	ent	I <sub>DSS</sub>	$V_{DS} = 150 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	_	_	100	μΑ
Drain-source bre	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	150	_	_	V
Gate threshold ve	oltage	V <sub>th</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 1 \text{ mA}$	0.8	_	2.0	V
Drain-source ON resistance		RDC (ON)	$V_{GS} = 4 V, I_D = 9 A$		0.09	0.18	Ω
		R <sub>DS</sub> (ON)	$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$		0.08	0.12	
Forward transfer	admittance	Y <sub>fs</sub>	$V_{DS} = 10 \text{ V}, \text{ I}_{D} = 9 \text{ A}$	10	17		S
Input capacitance		C <sub>iss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		1380		pF
Reverse transfer capacitance		C <sub>rss</sub>		_	200	_	pF
Output capacitance		C <sub>oss</sub>		_	610	_	pF
Switching time	Rise time	tr	$V_{GS}$ $0 \vee $ $C_{GS}$ $0 \vee $ $C_{G}$ $C_{G}$ $V_{D}$ V $V_{D}$ V $V_{D}$ V V V V V V V V		12	_	- ns
	Turn-on time	t <sub>on</sub>			24		
	Fall time	t <sub>f</sub>		_	56		
	Turn-off time	t <sub>off</sub>	Duty $\leq$ 1%, $t_W^{}=$ 10 $\mu s$	_	130		
Total gate charge (gate-source plus gate-drain)		Qg		—	57		nC
Gate-source charge		Q <sub>gs</sub>	$V_{DD} \simeq 120 \text{ V}, \text{ V}_{GS} = 10 \text{ V}, \text{ I}_{D} = 18 \text{ A}$	_	43	_	nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			14	_	nC

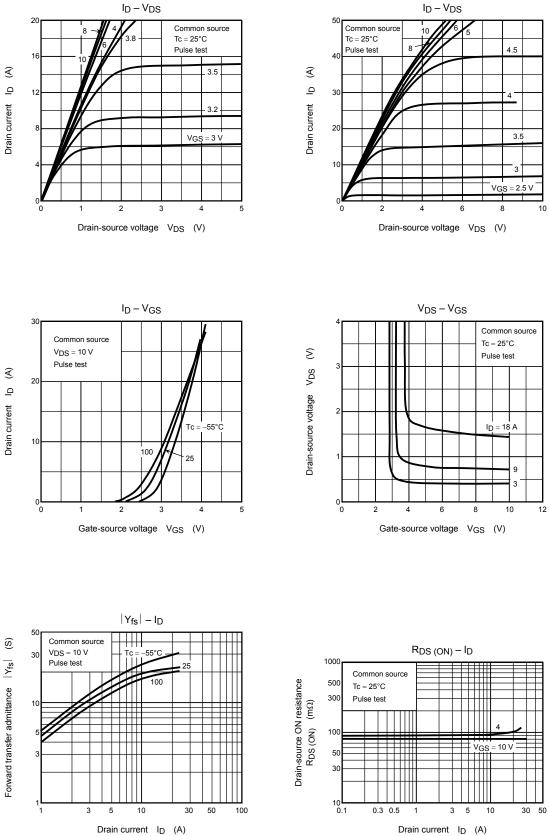
## Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	_	_	_	18	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	_	_		54	А
Forward voltage (diode)	VDSF	$I_{DR}=18~A,~V_{GS}=0~V$			-1.7	V
Reverse recovery time	t <sub>rr</sub>	$I_{DR} = 18 \text{ A}, \text{ V}_{GS} = 0 \text{ V}$		185	—	ns
Reverse recovery charge	Q <sub>rr</sub>	dI <sub>DR</sub> /dt = 100 A/µs		1.3	_	μC

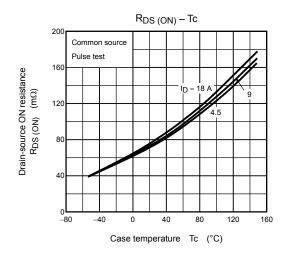
## Marking

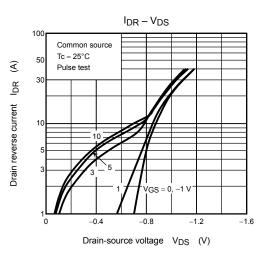


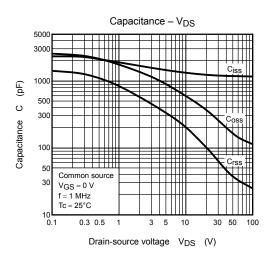
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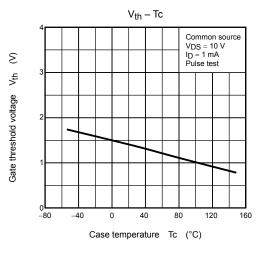


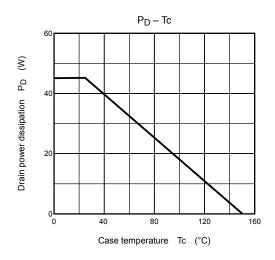
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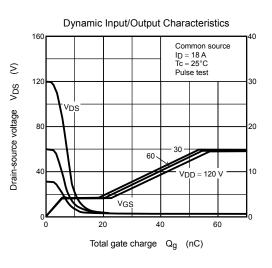


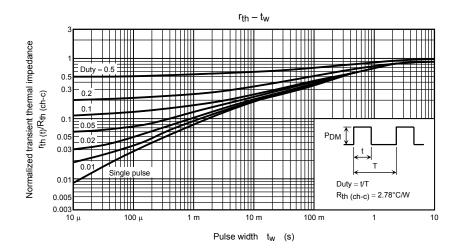


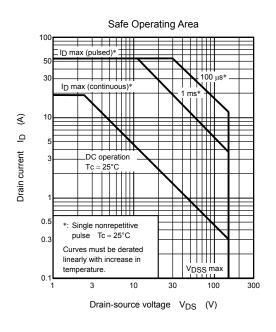


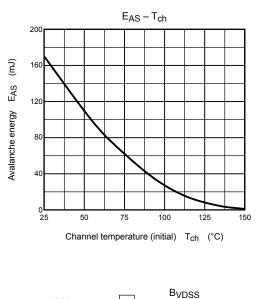


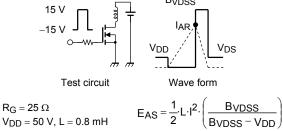












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