TOSHIBA Field Effect Transistor Silicon N Channel MOS Type ( $L^2-\pi$ -MOSV)

# 2SK2311

Chopper Regulator, DC–DC Converter and Switching Regulator Applications

- 4-V gate drive
- Low drain-source ON resistance  $: RDS(ON) = 36 \text{ m}\Omega \text{ (typ.)}$
- High forward transfer admittance  $: |Y_{fs}| = 16 \text{ S (typ.)}$
- Low leakage current  $: I_{DSS} = 100 \ \mu A \ (max) \ (V_{DS} = 60 \ V)$
- Enhancement mode  $: V_{th} = 0.8 \sim 2.0 \text{ V} (V_{DS} = 10 \text{ V}, \text{I}_{D} = 1 \text{ mA})$

#### Absolute Maximum Ratings (Ta = 25°C)

Characteri	stics	Symbol	Rating	Unit	
Drain-source voltage		V <sub>DSS</sub>	60	V	
Drain-gate voltage (R	<sub>GS</sub> = 20 kΩ)	V <sub>DGR</sub>	60	V	
Gate-source voltage		V <sub>GSS</sub>	±20	V	
Drain current	DC (Note 1)	ID	25	А	
	Pulse (Note 1)	I <sub>DP</sub>	100	A	
Drain power dissipatio	n (Tc = 25°C)	PD	40	W	
Single pulse avalanche	e energy (Note 2)	EAS	156	mJ	
Avalanche current		I <sub>AR</sub>	25	А	
Repetitive avalanche e	energy (Note 3)	E <sub>AR</sub>	3.5	mJ	
Channel temperature		T <sub>ch</sub>	150	°C	
Storage temperature r	ange	T <sub>stg</sub>	-55~150	°C	

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/Derating Concept and Methods) and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

#### **Thermal Characteristics**

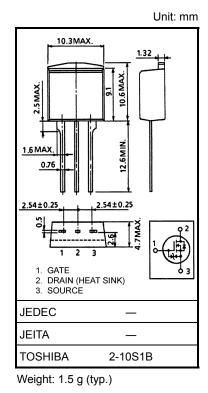
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R <sub>th (ch-c)</sub>	3.125	°C / W
Thermal resistance, channel to ambient	R <sub>th (ch−a)</sub>	83.3	°C / W

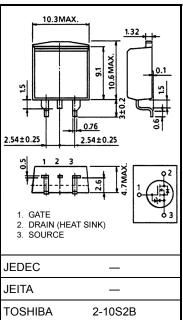


Note 2:  $V_{DD}$  = 25 V,  $T_{ch}$  = 25°C (initial), L = 339 µH,  $R_G$  = 25  $\Omega$ ,  $I_{AR}$  = 25 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic-sensitive device. Please handle with caution.





Weight: 1.5 g (typ.)

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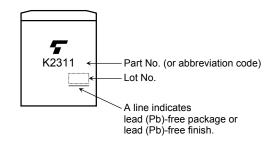
### **Electrical Characteristics (Ta = 25°C)**

Charao	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cu	urrent	I <sub>GSS</sub>	V <sub>GS</sub> = ±16 V, V <sub>DS</sub> = 0 V	_	_	±10	μA
Drain cut-off cu	rrent	I <sub>DSS</sub>	V <sub>DS</sub> = 60 V, V <sub>GS</sub> = 0 V	_	_	100	μA
Drain-source br	eakdown voltage	V (BR) DSS	I <sub>D</sub> = 10 mA, V <sub>GS</sub> = 0 V	60	_	_	V
Gate threshold	voltage	V <sub>th</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 1 mA	0.8	_	2.0	V
Drain-source ON resistance		R <sub>DS (ON)</sub>	V <sub>GS</sub> = 4 V, I <sub>D</sub> = 12 A	_	57	80	
			V <sub>GS</sub> = 10 V, I <sub>D</sub> = 12 A		36	46	mΩ
Forward transfe	r admittance	Y <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 12 A	10	16	_	S
Input capacitant	ce	C <sub>iss</sub>			1000	_	pF
Reverse transfe	r capacitance	C <sub>rss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz		200	_	
Output capacitance		Coss			550	—	
Switching time	Rise time	tr	$V_{GS} \stackrel{10 \text{ V}}{_{0 \text{ V}}} \int I_{D} = 12 \text{ A}$ $V_{GS} \stackrel{10 \text{ V}}{_{0 \text{ V}}} \int I_{D} = 12 \text{ A}$ $R_{L} = 2.5 \Omega$ $V_{DD} = 30 \text{ V}$	_	20	_	- ns
	Turn-on time	t <sub>on</sub>		_	30	_	
	Fall time	t <sub>f</sub>		_	55	_	
	Turn-off time	toff	Duty $\leq 1\%$ , t <sub>w</sub> = 10 $\mu$ s	_	130	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	38	_	
Gate-source charge		Q <sub>gs</sub>	V <sub>DD</sub> ≈ 48 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 25 A		25		nC
Gate-drain ("miller") charge		Q <sub>gd</sub>			13	_	

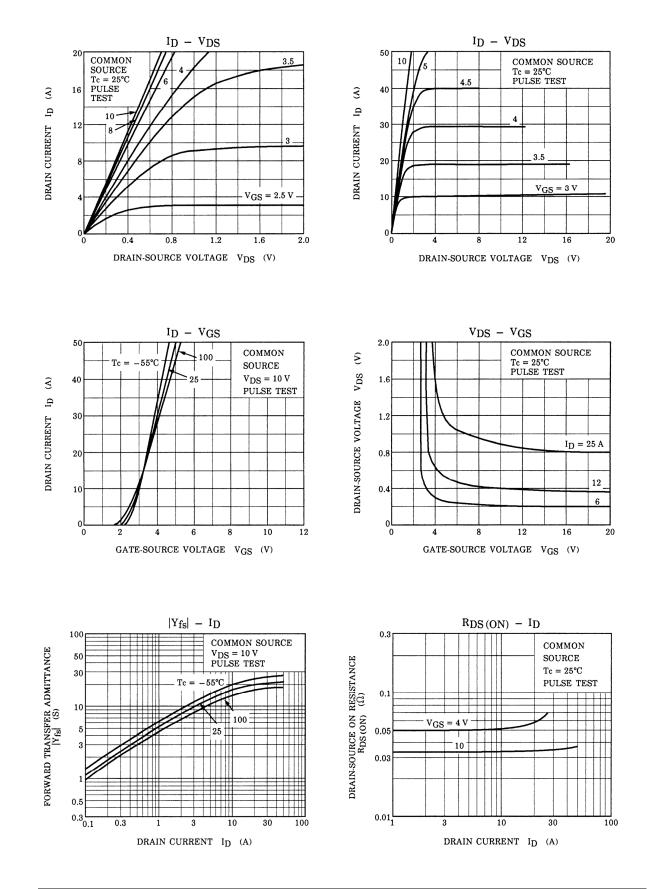
#### Source–Drain Ratings and Characteristics (Ta = 25°C)

Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note 1)	I <sub>DR</sub>	—	_	_	25	А
Pulse drain reverse current (Note 1)	I <sub>DRP</sub>	—	_	_	100	А
Forward voltage (diode)	V <sub>DSF</sub>	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V			-1.8	V
Reverse recovery time	t <sub>rr</sub>	I <sub>DR</sub> = 25 A, V <sub>GS</sub> = 0 V		50	_	ns
Reverse recovery charge	Q <sub>rr</sub>	dl <sub>DR</sub> / dt = 50 Å / μs		35	_	μC

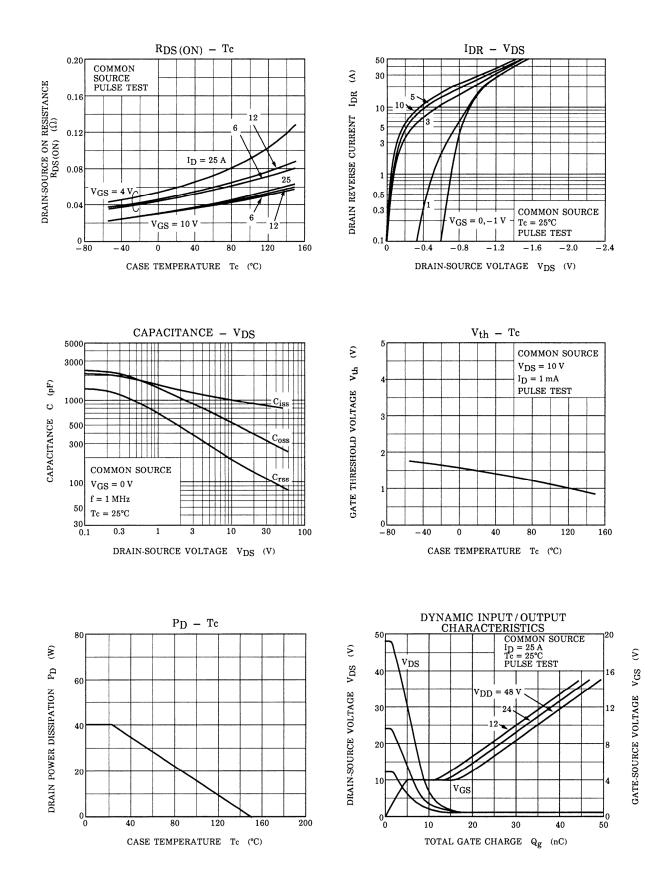
### Marking

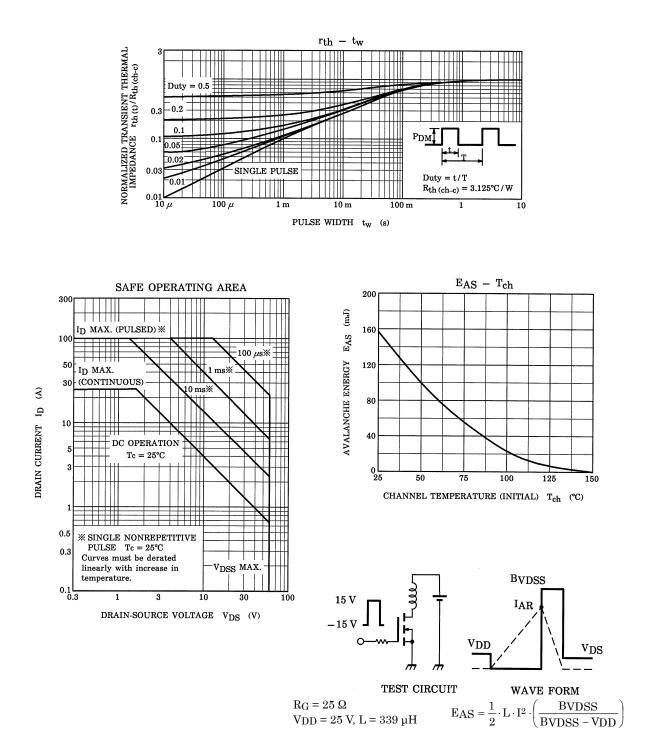


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