

No.3775A

N-Channel MOS Silicon FET

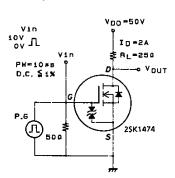
Very High-Speed Switching Applications

## Features

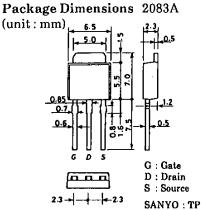
- · Low ON resistance.
- · Very high-speed switching.
- · Low-voltage drive.

Absolute Maximum Ratings at Ta = 25°C				unit		
Drain to Source Voltage	$V_{DSS}$	•		100	V	
Gate to Source Voltage	V <sub>GSS</sub>			±15	v	
Drain Current(DC)	I <sub>D</sub>		_	4	À	
Drain Current(Pulse)	I <sub>DP</sub>	$PW \le 10 \mu s$ , duty cycle $\le 1\%$		16	Α	
Allowable Power Dissipation	P <sub>D</sub>	Tc=25°C		20	w	
Channel Temperature	Tch	-		150	°C	
Storage Temperature	Tstg		-55 to +		°Č	
Storage Temperature	1306		-0000	100	O	
Electrical Characteristics at Ta = 25°C			min	typ	max	unit
D-S Breakdown Voltage	$V_{(BR)DSS}$	$I_D = 1 \text{ mA, } V_{GS} = 0$	100			v
G-S Breakdown Voltage		$I_G = \pm 100 \mu A, V_{DS} = 0$	$\pm 15$			v
Zero Gate Voltage	I <sub>DSS</sub>	$V_{DS} = 100 V, V_{GS} = 0$			100	$\mu A$
Drain Current						•
Gate to Source Leakage Current	$I_{GSS}$	$V_{GS} = \pm 12V, V_{DS} = 0$			$\pm 10$	$\mu$ A
Cutoff Voltage	V <sub>GS(off)</sub>	$V_{DS} = 10V$ , $l_D = 1mA$	1.0		2.0	· v
Forward Transfer Admittance	$ \mathbf{y}_{fs} $	$V_{DS} = 10 V_{ID} = 2A$	2.5	4		s
Static Drain to Source	R <sub>DS(on)</sub>	$I_{D} = 2A, V_{GS} = 10V$		0.3	0.4	Ω
on State Resistance	R <sub>DS(on)</sub>	$I_D = 2A, V_{GS} = 4V$		0.4	0.55	Ω
Input Capacitance	Ciss	$V_{DS} = 20V, f = 1MHz$		380		рF
Output Capacitance	Coss	$V_{DS} = 20V, f = 1MHz$		80		рF
Reverse Transfer Capacitance	Crss	$V_{DS} = 20V, f = 1MHz$		15		рF
Turn-ON Delay Time	$t_{d(on)}$	See specified Test Circuit.		10		ns
Rise Time	$t_r$	• "		13		ns
Turn-OFF Delay Time	t <sub>d(off)</sub>	"		70		ns
Fall Time	tf	"		30		ns
Diode Forward Voltage	$\dot{v}_{ ext{SD}}$	$I_S = 4A, V_{GS} = 0$		1.0	1.5	v

## Switching Time Test Circuit



## Package Dimensions 2083A

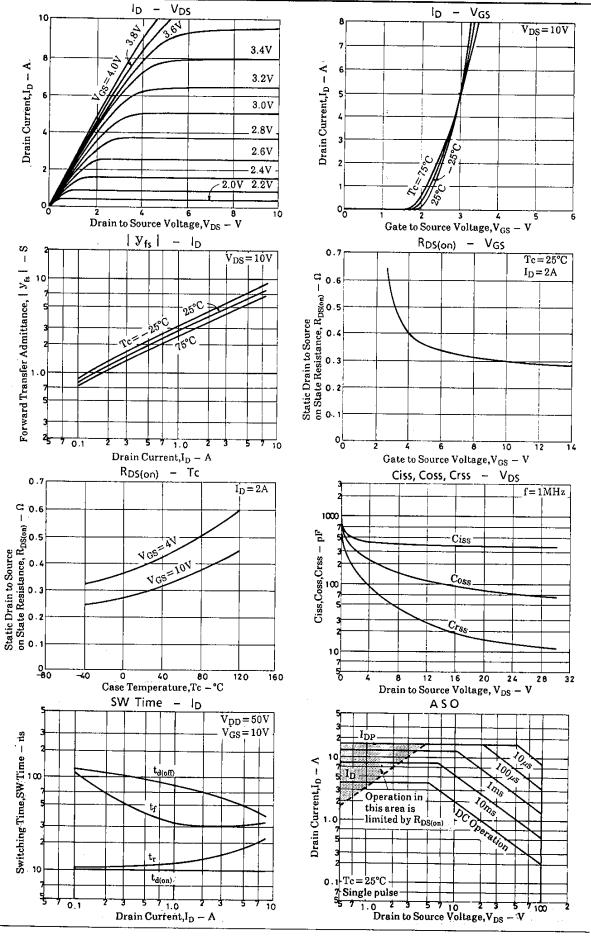


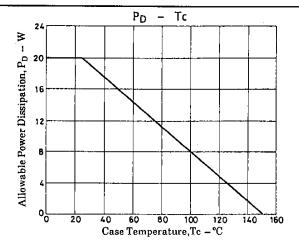
Package Dimensions 2092A (unit: mm)

G : Gate D : Drain S : Source

SANYO: TP-FA

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