# 2SK1315(L)(S), 2SK1316(L)(S)

## Silicon N-Channel MOS FET

# **HITACHI**

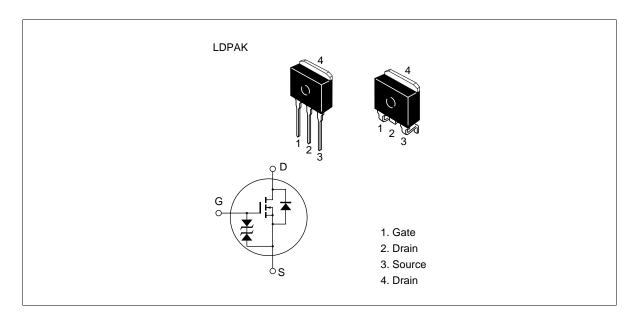
### **Application**

High speed power switching

#### **Features**

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator, DC-DC converter and motor driver

### **Outline**





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### **Absolute Maximum Ratings** $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1315	V <sub>DSS</sub>	450	V
	2SK1316		500	
Gate to source voltage		$V_{GSS}$	±30	V
Drain current		I <sub>D</sub>	8	А
Drain peak current		l <sub>D(pulse)</sub> *1	32	A
Body to drain diode reverse	e drain current	I <sub>DR</sub>	8	Α
Channel dissipation		Pch*2	60	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

Notes: 1. PW  $\leq$  10  $\mu$ s, duty cycle  $\leq$  1%

2. Value at  $T_c = 25^{\circ}C$ 

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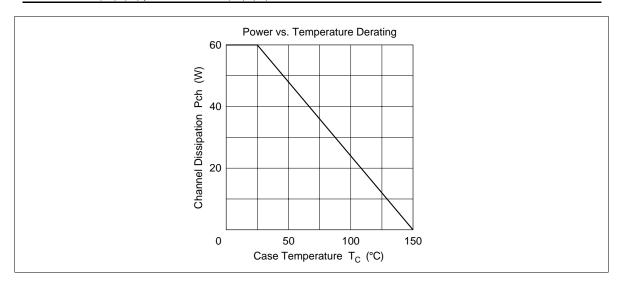
### **Electrical Characteristics** ( $Ta = 25^{\circ}C$ )

Item		Symbol	Min	Тур	Max	Unit	Test conditions
Drain to source	2SK1315	$V_{(BR)DSS}$	450	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
breakdown voltage	2SK1316	-	500	=			
Gate to source breakdown voltage		$V_{(BR)GSS}$	±30	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
Gate to source leak c	urrent	I <sub>GSS</sub>	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
Zero gate voltage	2SK1315	I <sub>DSS</sub>	_	_	250	μΑ	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
drain current	2SK1316	-					$V_{DS} = 400 \text{ V}, V_{GS} = 0$
Gate to source cutoff	voltage	$V_{GS(off)}$	2.0	_	3.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
Static Drain to source	2SK1315	R <sub>DS(on)</sub>	_	0.55	0.7	Ω	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
on state resistance	2SK1316	-	_	0.60	8.0	-	
Forward transfer adm	ittance	yfs	4.5	7.5	_	S	$I_D = 4 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Input capacitance		Ciss	_	1150	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Output capacitance		Coss	_	340	_	pF	f = 1 MHz
Reverse transfer capacitance		Crss	_	55	_	pF	
Turn-on delay time		$t_{d(on)}$	_	17	_	ns	$I_D = 4 \text{ A}, V_{GS} = 10 \text{ V},$
Rise time		t <sub>r</sub>	_	55	_	ns	$R_L = 7.5 \Omega$
Turn-off delay time		$t_{d(off)}$	_	100	_	ns	
Fall time		t <sub>f</sub>	_	45	_	ns	-
Body to drain diode for voltage	orward	$V_{DF}$	_	0.9	_	V	$I_F = 8 \text{ A}, V_{GS} = 0$
Body to drain diode re recovery time	everse	t <sub>rr</sub>	_	350	_	ns	$I_F = 8 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$

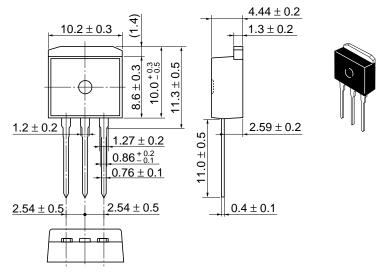
Note: 1. Pulse test

See characteristic curves of 2SK1159, 2SK1160.

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### Unit: mm



Hitachi Code	LDPAK (L)			
JEDEC				
EIAJ				
Weight (reference value)	1.4 g			

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