

1200V thinQ![™] SiC Schottky Diode

Features:

- Revolutionary Semiconductor Material -Silicon Carbide
- Switching Behaviour Benchmark
- No Reverse Recovery / No Forward Recovery
- Temperature Independent Switching Behaviour
- Qualified According to JEDEC¹⁾ Based on Target Applications

Applications:

- Motor Drives / Solar Inverters
- High Voltage CCM PFC
- Switch Mode Power Supplies
- High Voltage Multipliers



Chip Type	V _{BR}	IF	Die Size	Package
IDC08S120	1200V	7.5A	2.012 x 2.012 mm ²	sawn on foil

MECHANICAL PARAMETERS

Raster size	2.012 x 2.012				
Anode pad size	1.476 x 1.476	mm ²			
Area total / active	4.05 / 3]			
Thickness	362	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	901 pcs				
Passivation frontside	Photoimide				
Pad metal	3200 nm Al				
Backside metal	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	Electrically conductive glue or solder				
Wire bond	AI, ≤ 350µm				
Reject ink dot size	Ø ≥ 0.3 mm				
Recommended storage environment	torage environment Store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				



ELECTRICAL PARAMETERS

Maximum Ratings

Parameter	Symbol	Condition	Value	Unit	
Repetitive peak reverse voltage	V _{RRM}	<i>T</i> j=25 ℃	1200	V	
DC blocking voltage	V _{DC}		1200	v	
Continuous forward current, limited by T _{jmax}	I _F		7.5		
Surge non repetitive forward current,	,	$T_C = 25^{\circ}C, t_P = 10 ms$	39		
sine halfwave	I _{F,SM}	$T_C = 150^{\circ}C, t_P = 10 ms$	33	- A	
Repetitive peak forward current, limited by thermal resistance R _{th}	I _{F,RM}	$T_C = 100^{\circ}C, T_j = 150^{\circ}C, D = 0.1$	32		
Non-repetitive peak forward current	I _{F,max}	$T_C = 25^{\circ}C, t_P = 10\mu s$	160		
i ² t value	$\int i^2 dt$	$T_C = 25^{\circ}C, t_P = 10 ms$	7	– A ² s	
	J ^{i ai}	$T_C = 150 ^{\circ}C, t_P = 10 ms$	5		
Operating junction and storage temperature range	T_{j} , T_{stg}		-55+175	°C	

Static Characteristics (tested on wafer)

Parameter	Symbol	Conditions		Unit		
		Conditions	min.	Тур.	max.	Unit
Reverse current	I _R	V _R =1200V, T _j =25°C		8	180	μΑ
Diode forward voltage	V _F	I _F =7.5A, T _j =25°C		1.6	1.8	V

Static Characteristics (not subject to production test - verified by design / characterization)

Parameter	Symbol	Conditions		Unit		
		Conditions	min.	Тур.	max.	Onic
Reverse current	I _R	V _R =1200V, T _j =150°C		30	1000	μA
Diode forward voltage	V _F	I _F =7.5A, T _j =150°C		2.5	3	V



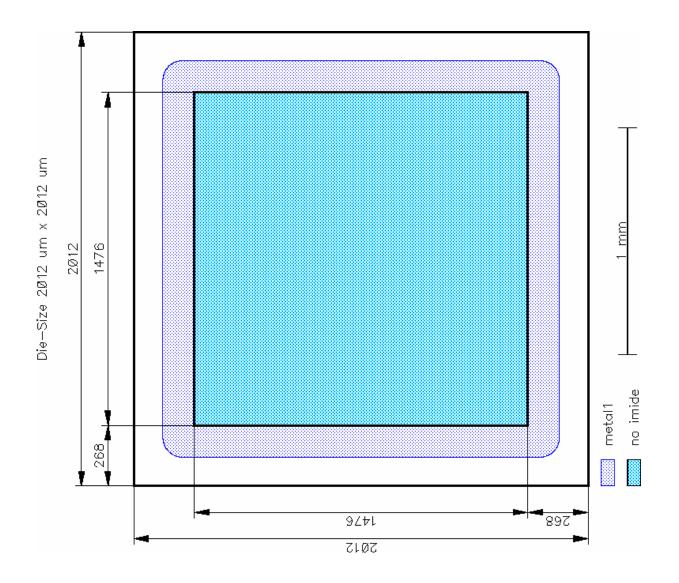
Parameter	Symbol	Conditions		Value			Unit
	Cymbol			min.	Тур.	max.	onn
Total capacitive charge ³⁾	Q _c	$I_F <= I_{F,max}$	T _j =150 °C		27		nC
Switching time ²⁾	t _c	<i>di/dt=200A/ms</i> <i>V_R=1200V</i>	T _j =150 °C			<10	ns
			$V_R = 1 V$		380		
Total capacitance	С	f=1MHz	V _R =300V		30		pF
			V _R =600V		27		

Dynamic Characteristics (not subject to production test - verified by design / characterization)

 $^{1)}$ J-STD20 and JESD22 $^{2)}$ t_c is the time constant for the capacitive displacement current waveform (independent from T_j, I_{LOAD} and di/dt), different from t_{rr}, which is dependent on T_j, I_{LOAD}, di/dt. No reverse recovery time constant t_{rr} due to absence of minority carrier inject. $^{3)}$ Only capacitive charge occurring, guaranteed by design (independent from T_j, I_{LOAD} and di/dt).



CHIP DRAWING





FURTHER ELECTRICAL CHARACTERISTICS

This chip data sheet refers to the device data sheet

INFINEON TECHNOLOGIES

IDH08S120

Description:

AQL 0,65 for visual inspection according to failure catalogue

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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