

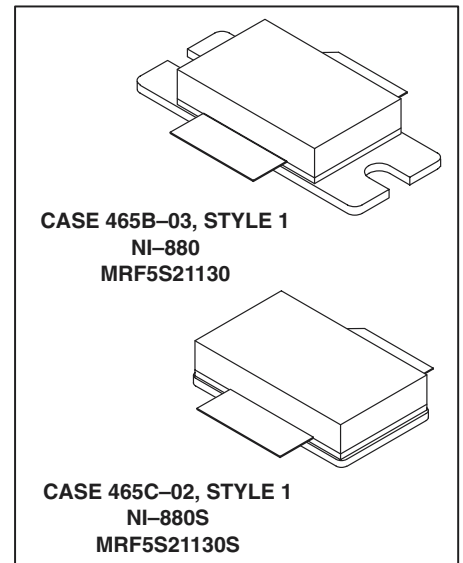
The RF MOSFET Line
RF Power Field Effect Transistors
N-Channel Enhancement-Mode Lateral MOSFETs

Designed for W-CDMA base station applications at frequencies from 2110 to 2170 MHz. Suitable for TDMA, CDMA and multicarrier amplifier applications. To be used in Class AB for PCN-PCS/cellular radio and WLL applications.

- Typical 2-carrier W-CDMA Performance for $V_{DD} = 28$ Volts, $I_{DQ} = 1200$ mA, $f_1 = 2135$ MHz, $f_2 = 2145$ MHz, Channel Bandwidth = 3.84 MHz, Adjacent Channels Measured over 3.84 MHz BW @ $f_1 -5$ MHz and $f_2 +5$ MHz, Distortion Products Measured over a 3.84 MHz BW @ $f_1 -10$ MHz and $f_2 +10$ MHz, Peak/Avg. = 8.5 dB @ 0.01% Probability on CCDF.
Output Power — 28 Watts Avg.
Power Gain — 13.5 dB
Efficiency — 26%
IM3 — -37 dBc
ACPR — -39 dBc
- Internally Matched, Controlled Q, for Ease of Use
- High Gain, High Efficiency and High Linearity
- Integrated ESD Protection
- Designed for Maximum Gain and Insertion Phase Flatness
- Capable of Handling 10:1 VSWR, @ 28 Vdc, 2140 MHz, 92 Watts CW Output Power
- Excellent Thermal Stability
- Characterized with Series Equivalent Large-Signal Impedance Parameters
- Qualified Up to a Maximum of 32 V_{DD} Operation
- Available in Tape and Reel. R3 Suffix = 250 Units per 56 mm, 13 inch Reel.

MRF5S21130
MRF5S21130R3
MRF5S21130S
MRF5S21130SR3

2170 MHz, 28 W AVG.,
2 x W-CDMA, 28 V
LATERAL N-CHANNEL
RF POWER MOSFETs



MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Drain-Source Voltage	V_{DSS}	65	Vdc
Gate-Source Voltage	V_{GS}	-0.5, +15	Vdc
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	315 2	Watts W/°C
Storage Temperature Range	T_{stg}	-65 to +150	°C
Operating Junction Temperature	T_J	200	°C
CW Operation	CW	92	Watts

NOTE - **CAUTION** - MOS devices are susceptible to damage from electrostatic charge. Reasonable precautions in handling and packaging MOS devices should be observed.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case Case Temperature 80°C, 92 W CW Case Temperature 80°C, 28 W CW	$R_{\theta JC}$	0.56 0.56	°C/W

ELECTRICAL CHARACTERISTICS ($T_C = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
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OFF CHARACTERISTICS

Zero Gate Voltage Drain Leakage Current ($V_{DS} = 65\text{ Vdc}$, $V_{GS} = 0\text{ Vdc}$)	I_{DSS}	—	—	10	μAdc
Zero Gate Voltage Drain Leakage Current ($V_{DS} = 28\text{ Vdc}$, $V_{GS} = 0\text{ Vdc}$)	I_{DSS}	—	—	1	μAdc
Gate–Source Leakage Current ($V_{GS} = 5\text{ Vdc}$, $V_{DS} = 0\text{ Vdc}$)	I_{GSS}	—	—	1	μAdc

ON CHARACTERISTICS

Gate Threshold Voltage ($V_{DS} = 10\text{ Vdc}$, $I_D = 300\ \mu\text{Adc}$)	$V_{GS(th)}$	2.5	2.7	3.5	Vdc
Gate Quiescent Voltage ($V_{DS} = 28\text{ Vdc}$, $I_D = 1200\text{ mAdc}$)	$V_{GS(Q)}$	—	3.7	—	Vdc
Drain–Source On–Voltage ($V_{GS} = 10\text{ Vdc}$, $I_D = 3\text{ Adc}$)	$V_{DS(on)}$	—	0.26	0.3	Vdc
Forward Transconductance ($V_{DS} = 10\text{ Vdc}$, $I_D = 3\text{ Adc}$)	g_{fs}	—	7.5	—	S

DYNAMIC CHARACTERISTICS (1)

Reverse Transfer Capacitance ($V_{DS} = 28\text{ Vdc} \pm 30\text{ mV(rms)ac}$ @ 1 MHz, $V_{GS} = 0\text{ Vdc}$)	C_{rss}	—	2.6	—	pF
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FUNCTIONAL TESTS (In Motorola Test Fixture, 50 ohm system) 2–carrier W–CDMA, 3.84 MHz Channel Bandwidth Carriers, ACPR and IM3 measured in 3.84 MHz Bandwidth. Peak/Avg. = 8.5 dB @ 0.01% Probability on CCDF.

Common–Source Amplifier Power Gain ($V_{DD} = 28\text{ Vdc}$, $P_{out} = 28\text{ W Avg.}$, $I_{DQ} = 1200\text{ mA}$, $f_1 = 2112.5\text{ MHz}$, $f_2 = 2122.5\text{ MHz}$ and $f_1 = 2157.5\text{ MHz}$, $f_2 = 2167.5\text{ MHz}$)	G_{ps}	12	13.5	—	dB
Drain Efficiency ($V_{DD} = 28\text{ Vdc}$, $P_{out} = 28\text{ W Avg.}$, $I_{DQ} = 1200\text{ mA}$, $f_1 = 2112.5\text{ MHz}$, $f_2 = 2122.5\text{ MHz}$ and $f_1 = 2157.5\text{ MHz}$, $f_2 = 2167.5\text{ MHz}$)	η	24	26	—	%
Third Order Intermodulation Distortion ($V_{DD} = 28\text{ Vdc}$, $P_{out} = 28\text{ W Avg.}$, $I_{DQ} = 1200\text{ mA}$, $f_1 = 2112.5\text{ MHz}$, $f_2 = 2122.5\text{ MHz}$ and $f_1 = 2157.5\text{ MHz}$, $f_2 = 2167.5\text{ MHz}$; IM3 measured over 3.84 MHz BW at $f_1 - 10\text{ MHz}$ and $f_2 + 10\text{ MHz}$ referenced to carrier channel power.)	IM3	—	–37	–35	dBc
Adjacent Channel Power Ratio ($V_{DD} = 28\text{ Vdc}$, $P_{out} = 28\text{ W Avg.}$, $I_{DQ} = 1200\text{ mA}$, $f_1 = 2112.5\text{ MHz}$, $f_2 = 2122.5\text{ MHz}$ and $f_1 = 2157.5\text{ MHz}$, $f_2 = 2167.5\text{ MHz}$; ACPR measured over 3.84 MHz at $f_1 - 5\text{ MHz}$ and $f_2 + 5\text{ MHz}$.)	ACPR	—	–39	–37	dBc
Input Return Loss ($V_{DD} = 28\text{ Vdc}$, $P_{out} = 28\text{ W Avg.}$, $I_{DQ} = 1200\text{ mA}$, $f_1 = 2112.5\text{ MHz}$, $f_2 = 2122.5\text{ MHz}$ and $f_1 = 2157.5\text{ MHz}$, $f_2 = 2167.5\text{ MHz}$)	IRL	—	–12	–9	dB

(1) Part is internally matched both on input and output.

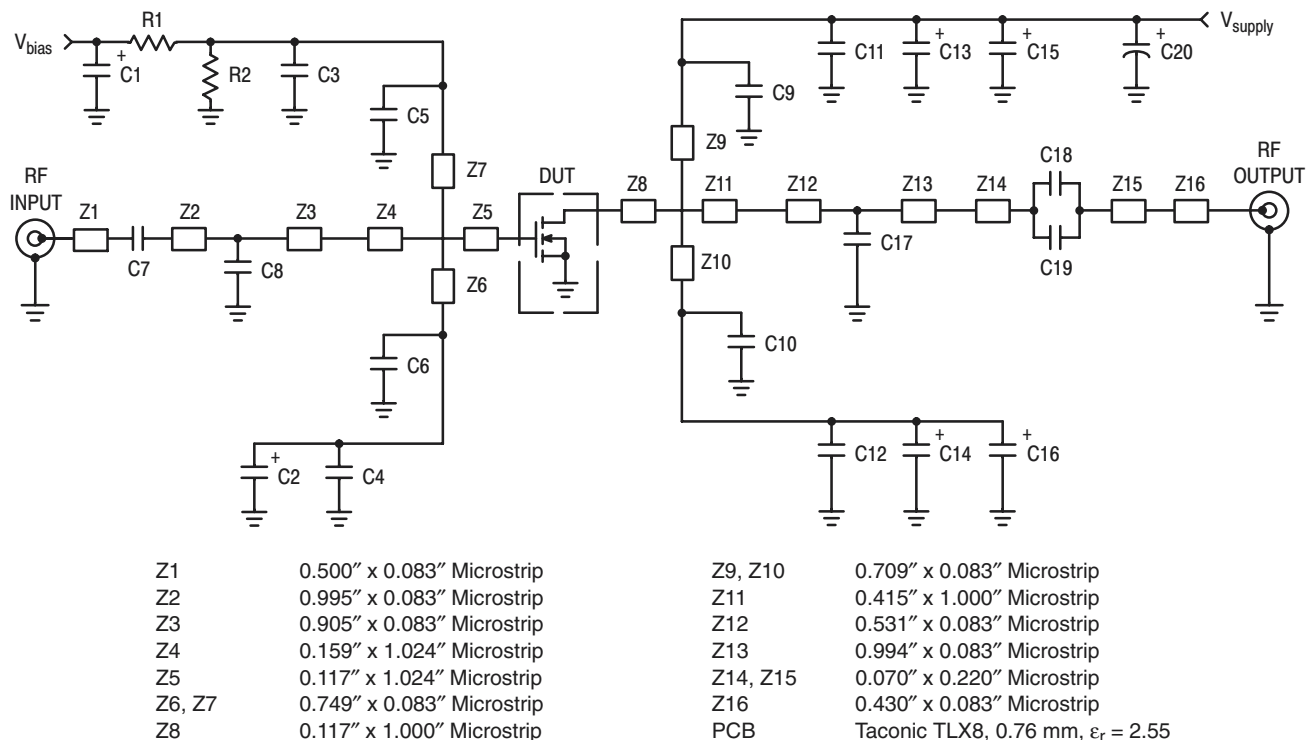


Figure 1. MRF5S21130 Test Circuit Schematic

Table 1. MRF5S21130 Test Circuit Component Designations and Values

Part	Description	Value, P/N or DWG	Manufacturer
C1, C2, C13, C14, C15, C16	10 μ F, 35 V Tantalum Capacitors	293D1106X9035D	Vishay–Sprague
C3, C4, C11, C12	220 nF Chip Capacitors (1812)	1812Y224KXA	Vishay–Vitramon
C5, C6, C7, C9, C10, C18, C19	6.8 pF 100B Chip Capacitors	100B6R8CW	ATC
C8	0.1 pF 100B Chip Capacitor	100B0R1BW	ATC
C17	0.5 pF 100B Chip Capacitor	100B0R5BW	ATC
C20	220 μ F, 63 V Electrolytic Capacitor, Radial	13668221	Philips
R1, R2	1 k Ω , 1/4 W Chip Resistors		

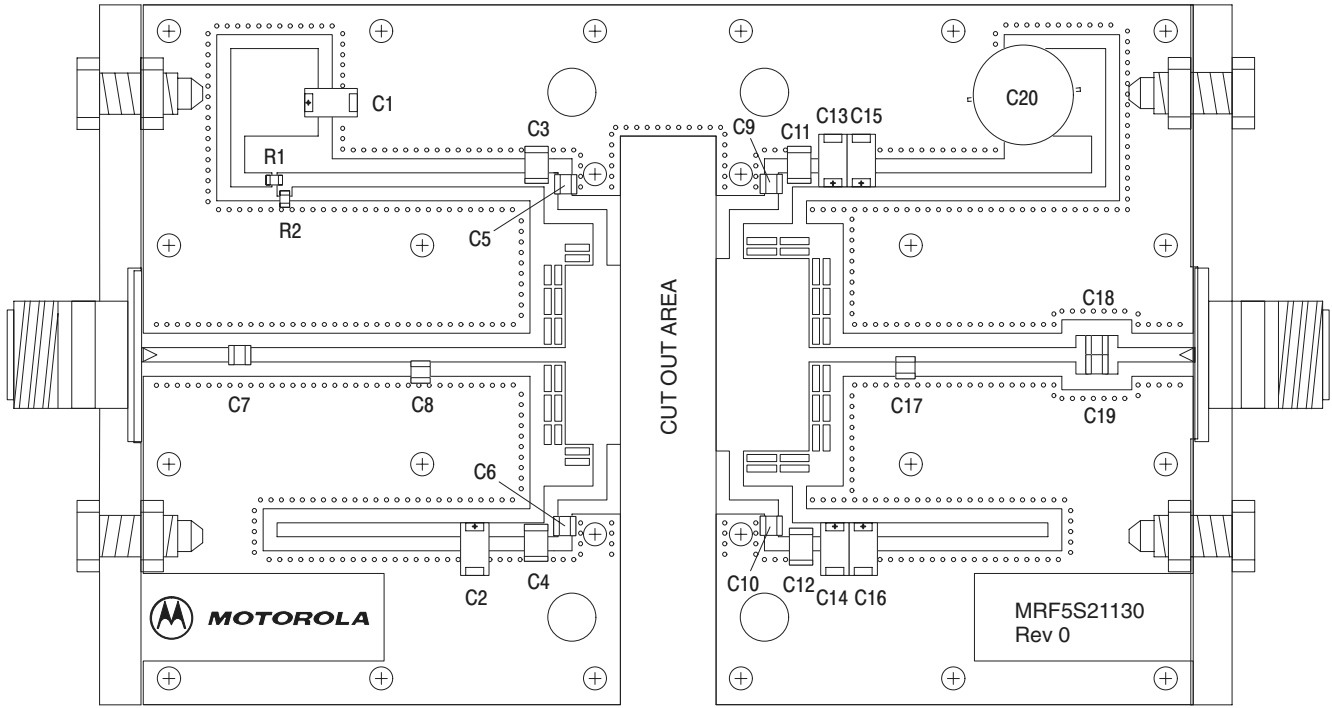


Figure 2. MRF5S21130 Test Circuit Component Layout

TYPICAL CHARACTERISTICS

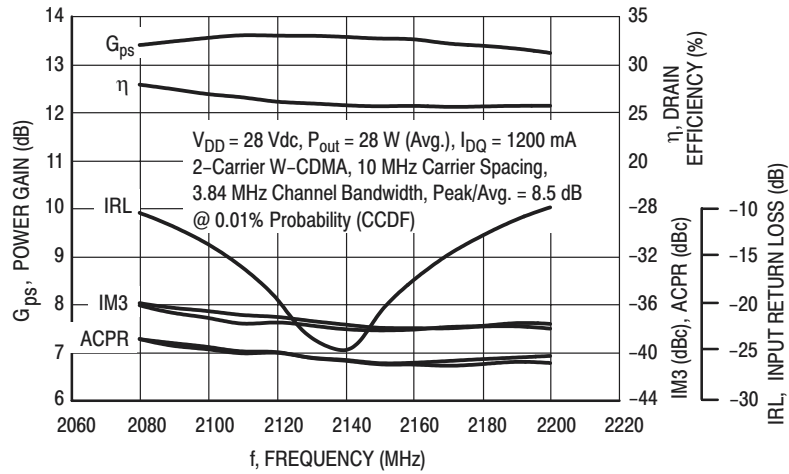


Figure 3. 2-Carrier W-CDMA Broadband Performance

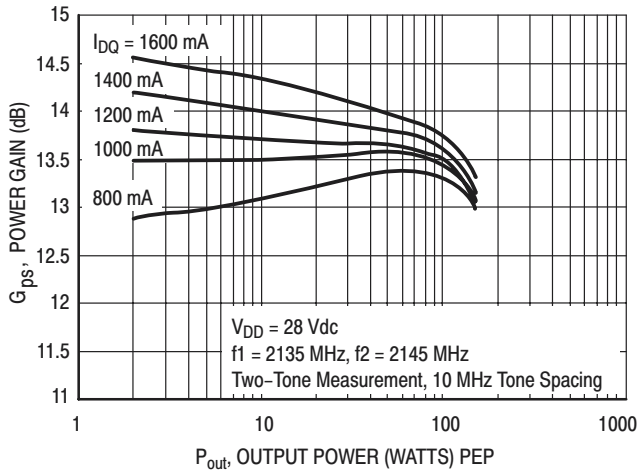


Figure 4. Two-Tone Power Gain versus Output Power

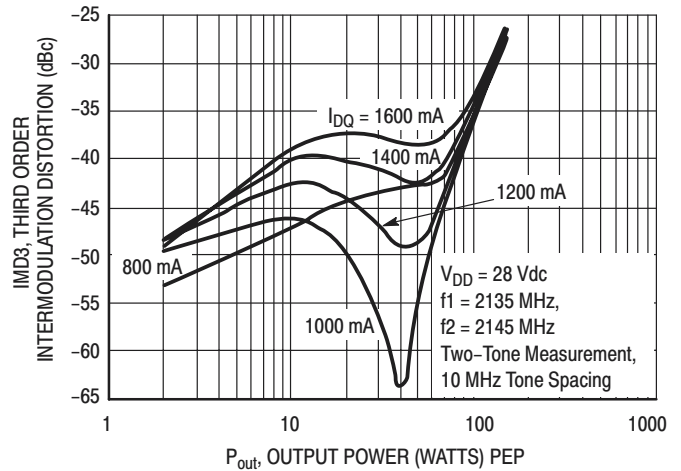


Figure 5. Third Order Intermodulation Distortion versus Output Power

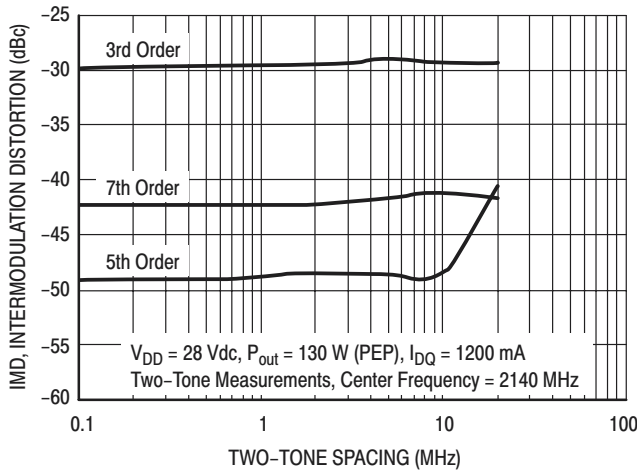


Figure 6. Intermodulation Distortion Products versus Tone Spacing

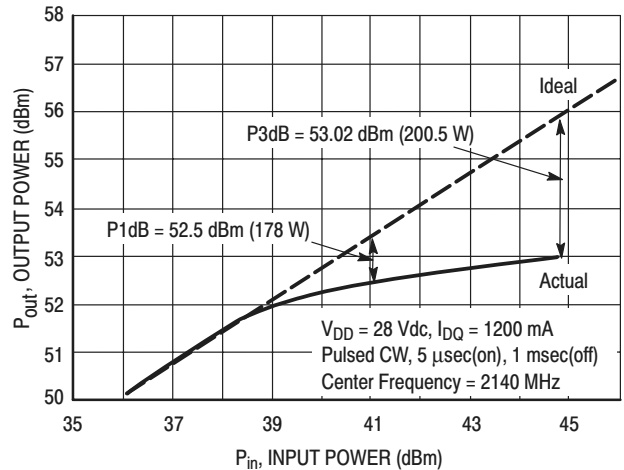


Figure 7. Pulse CW Output Power versus Input Power

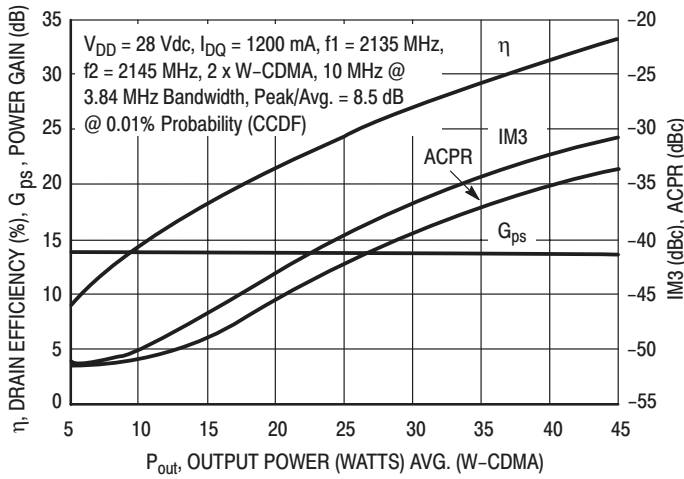


Figure 8. 2-Carrier W-CDMA ACPR, IM3, Power Gain and Drain Efficiency versus Output Power

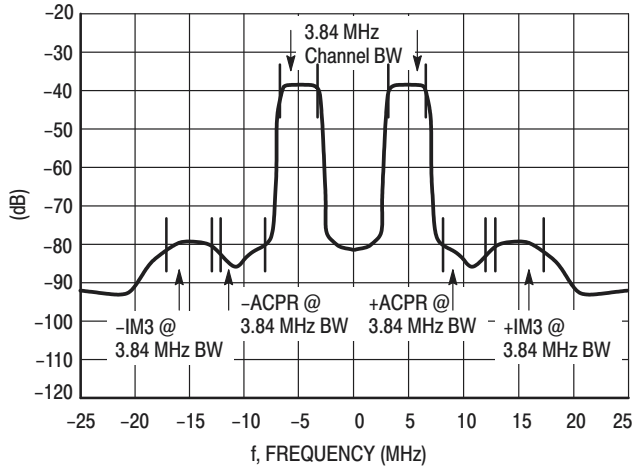


Figure 9. 2-Carrier W-CDMA Spectrum

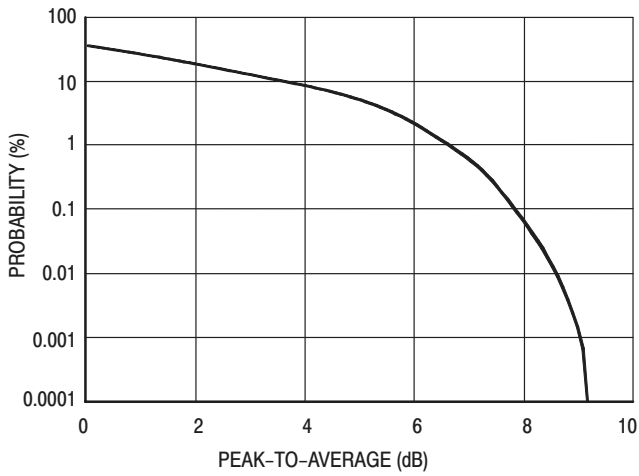
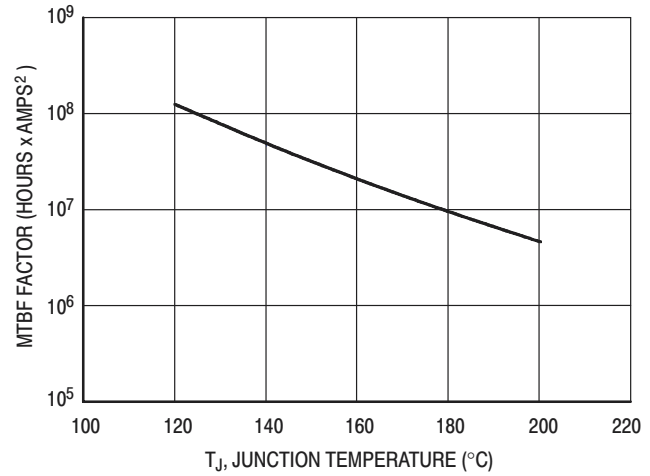
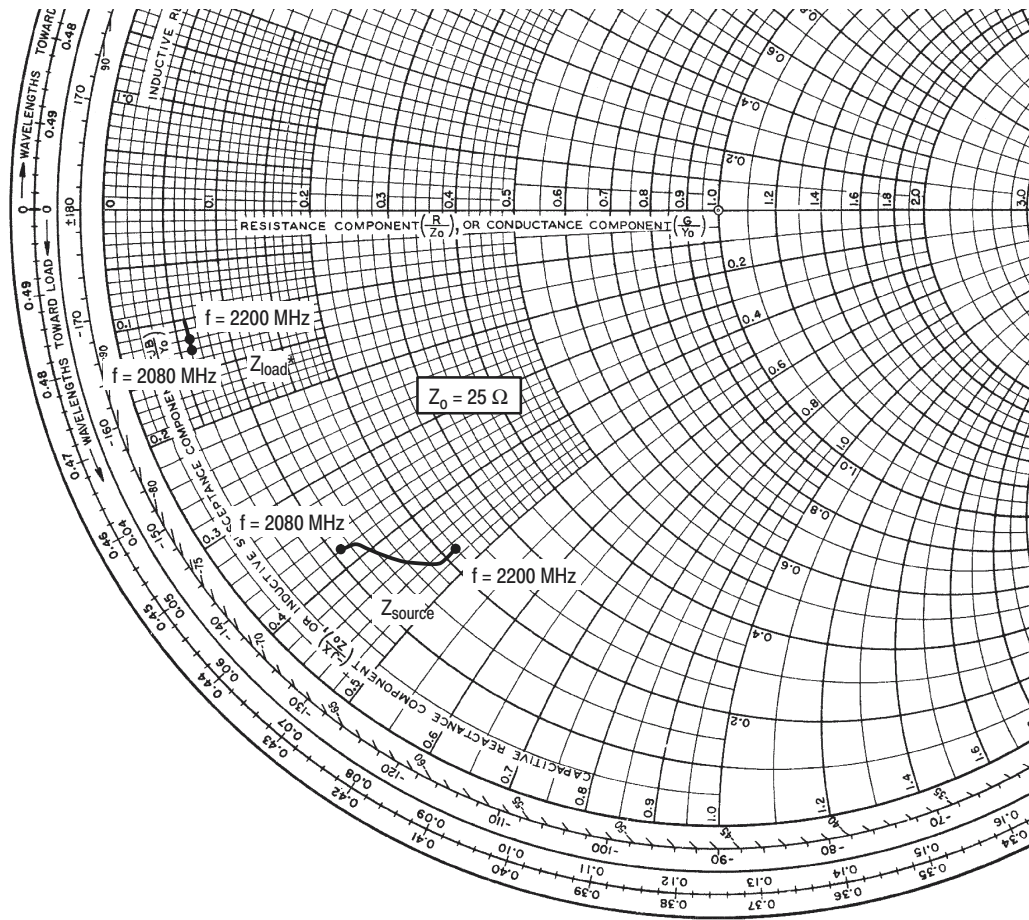


Figure 10. CCDF W-CDMA 3GPP, Test Model 1, 64 DPCH, 67% Clipping, Single Carrier Test Signal



This above graph displays calculated MTBF in hours x ampere² drain current. Life tests at elevated temperatures have correlated to better than $\pm 10\%$ of the theoretical prediction for metal failure. Divide MTBF factor by I_D^2 for MTBF in a particular application.

Figure 11. MTBF Factor versus Junction Temperature



$V_{DD} = 28 \text{ Vdc}$, $I_{DQ} = 1200 \text{ mA}$, $P_{out} = 28 \text{ W Avg.}$

f MHz	Z_{source} Ω	Z_{load} Ω
2080	$2.87 - j9.49$	$1.51 - j2.97$
2110	$3.13 - j9.86$	$1.52 - j2.54$
2140	$4.05 - j10.90$	$1.59 - j2.68$
2170	$4.80 - j11.75$	$1.62 - j2.70$
2200	$5.55 - j11.87$	$1.54 - j3.13$

Z_{source} = Test circuit impedance as measured from gate to ground.

Z_{load} = Test circuit impedance as measured from drain to ground.

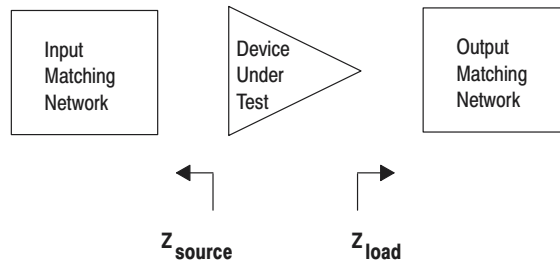


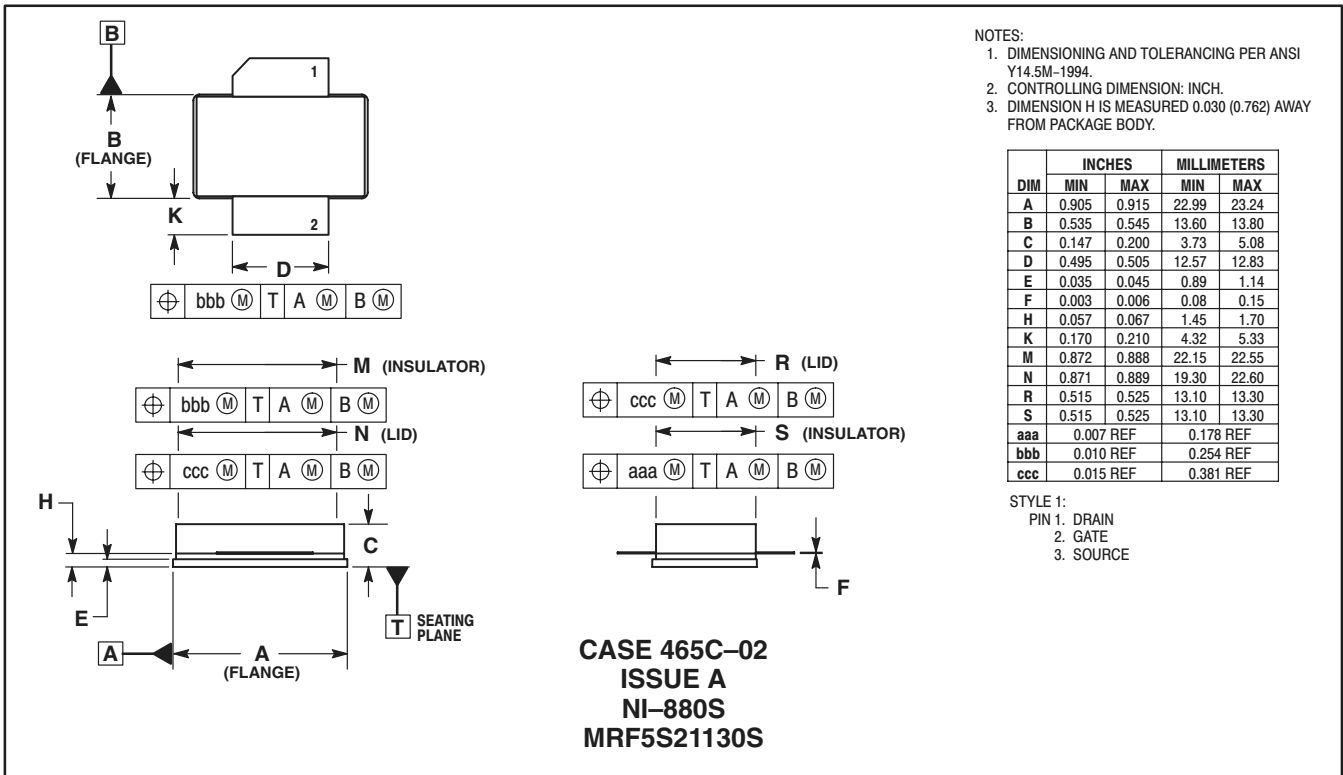
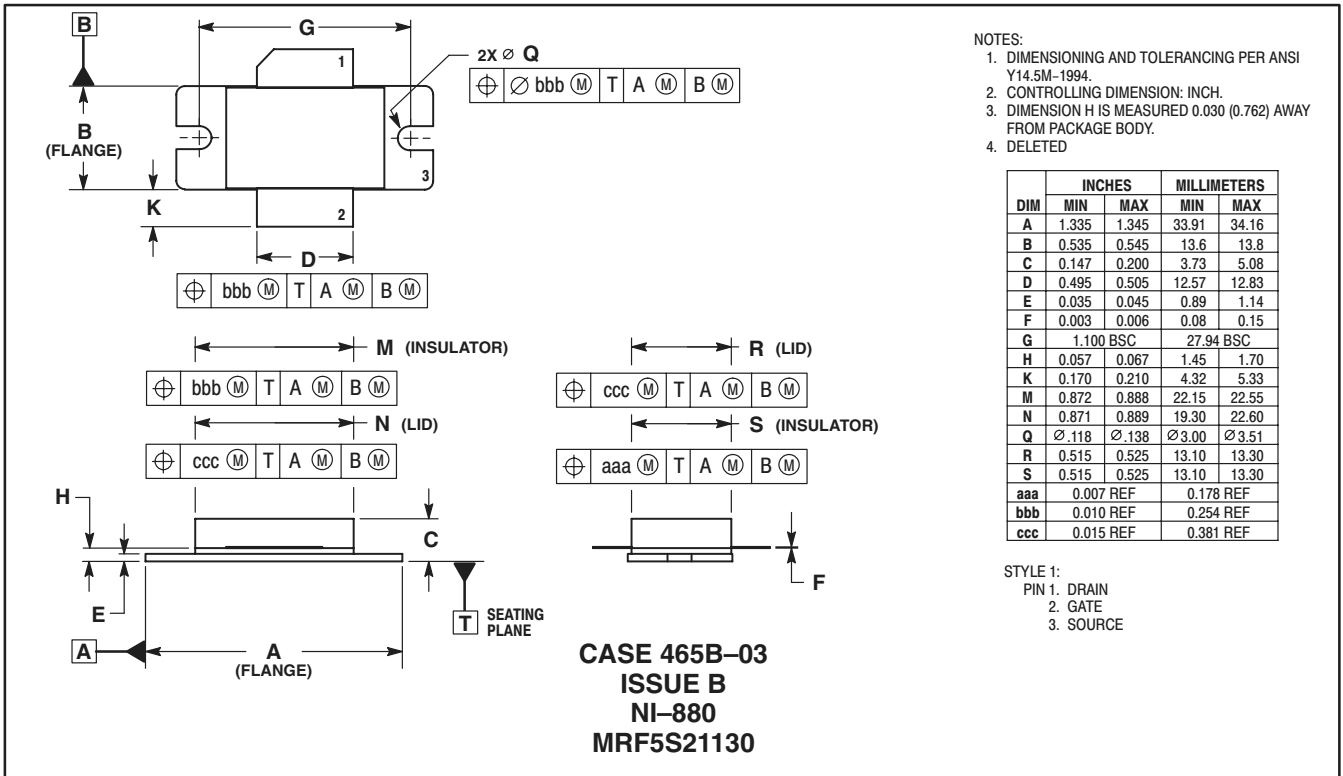
Figure 12. Series Equivalent Input and Output Impedance

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PACKAGE DIMENSIONS



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