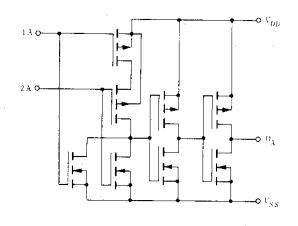
HD14001B

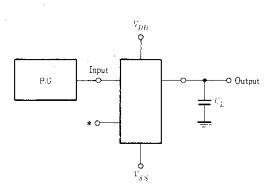
Quadruple 2-input NOR Gate

- FEATURES
- Quiescent Current = 0.5nA typ/pkg @5V
- Noise Immunity = 45% of V_{DD} typ
- Capable of Driving One Low-power Schottky TTL Load Over the Rated Temperature Range
- Pin-for Pin Replacements for CD4001B and MC14001B Series

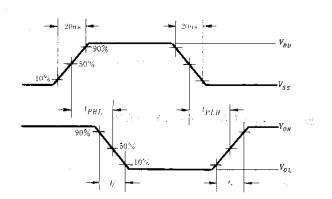
CIRCUIT SCHEMATIC (1/4)



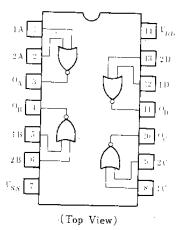
SWITCHING TIME TEST CIRCUIT



 * All Unused inputs of OR, NOR gates must be connected to Vss



PIN ARRANGEMENT



Characteristic	Symbol		'Test Conditions	-40°C			25°C			85°C	
onartecteristic	Symbol	$\mathcal{V}_{DD}(\mathbf{V})$	DD(V)		max	min	typ	max	min	max	Unit
Output Voltage		5.0	$V_{in} = V_{DD}$	-	0.05		0	0.05	-	0.05	v
	Vol	10		_	0.05	_	0	0.05	-	0.05	
		15		-	0.05	-	0	0.05	_	0.05	
		5.0	$V_{in} = 0$	4.95	_	4.95	5.0	-	4.95	—	v
	Von	10		9.95	- -	9.95	10	-	9.95	_	
		15		14.95	_	14.95	15	_	14.95		
Input Voltage		5.0	$V_{out} = 4.5 \mathrm{V}$	-	1.5	-	2.25	1.5	-	1.5	v
	VIL	10	$V_{out} = 9.0 \mathrm{V}$		3.0	—	4.50	3.0	-	3.0	
		15	$V_{out} = 13.5 \mathrm{V}$	-	4.0	-	6.75	4.0		4.0	
Input Contego		5.0	$V_{out} = 0.5 \mathrm{V}$	3.5	_	3.5	2.75	-	3.5		v
	V_{IH}	10	$V_{out} = 1.0 \mathrm{V}$	7.0	-	7.0	5.50		7.0		
		15	$V_{out} = 1.5 V$	11.0		11.0	8.25	_	11.0		
		5.0	$V_{OH} = 2.5 \mathrm{V}$	-2.5	—	-2.1	-4.2	-	-1.7	-	mA
	Іон	5.0	$V_{OH} = 4.6V$	-0.52	_	-0.44	-0.88	-	-0.36	-	
Output Drive Current	100	10	$V_{OH} = 9.5 \mathrm{V}$	-1.3		-1.1	-2.25	· _	-0.9	-	
		15	$V_{OH} = 13.5 V$	-3.6	—	-3.0	-8.8	_]	-2.4		
		5.0	$V_{OL} = 0.4 \mathrm{V}$	0.52	-	0.44	0.88		0.36	_	mA
	. Iol	10	$V_{OL} = 0.5 V$	1.3		1.1	2.25	:	0.9		
		15	$V_{OL} = 1.5 V$	3.6	-	3.0	8.8	-	2.4	-	
Input Current	- Iin	15		-	± 0.3	-	±0.00001	± 0.3	-	± 1.0	μA
Input Capacitance	Cin		$V_{in} = 0$	—	_		5.0	7.5	-	-	pF
Quiescent Current		5.0	Zero Signal, per Package	-	1.0	—	0.0005	1.0	-	7.5	μA
	IDD	10		—	2.0	-	0.0010	2.0		15.0	
		15		-	4.0		0.0015	4.0		30.0	
		5.0	Dynamic $+I_{DD}$, $C_L = 50 \text{pF}$ per Gate, $f = 1 \text{kHz}$	—	_	-	0.3	-	_	-	μA
Total Supply Current	Ir	10		-	-	_	0.6		_	_	
	1	15		—	_	_	0.9	_		_	

ELECTRICAL CHARACTERISTICS

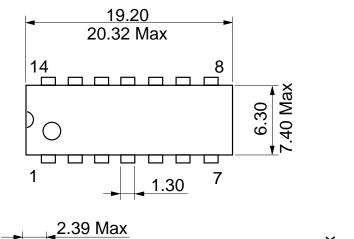
* To calculate total supply current at frequency other than 1kHz. (a) $V_{DD} = 5.0V I_T = (0.3\mu A/kHz)f + I_{DD}/4$ (b) $V_{DD} = 10V I_T = (0.6\mu A/kHz)f + I_{DD}/4$ (c) $V_{DD} = 15V I_T = (0.9\mu A/kHz)f + I_{DD}/4$

SWITCHING CHARACTERISTICS $(C_L = 50 \text{ pF}, Ta = 25^{\circ}\text{C})$

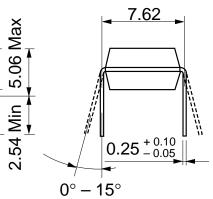
Characterístic	Symbol	$V_{DD}(\mathbf{V})$	min	typ	max	Unit
		5.0	-	100	200	ns
Outpùt Rise Time	t+	10	_	50	100	
		15	_	40	80	
Output Fall Time		5.0	-	100	200	ns
	tj	10	_	50	100	
		15	_	40	80	
		5.0	_	125	250	ns`
	t _{PLH}	10	-	50	100	
Propagation Dalay Time	i	15	-	40	80	
Propagation Delay Time		5.0		125	250	1
	t _{PHL}	10	-	50	100	ns
		15	_	40	80	1

HITACHI

Unit: mm



 0.48 ± 0.10



0.51 Min

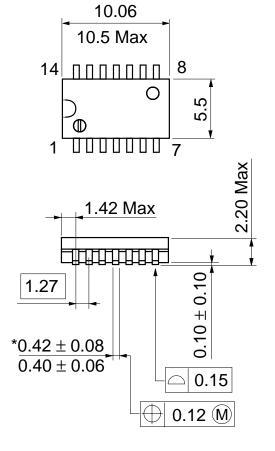
RANK

Hitachi Code	DP-14
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.97 g

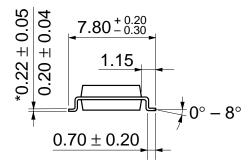
 2.54 ± 0.25

Unit: mm





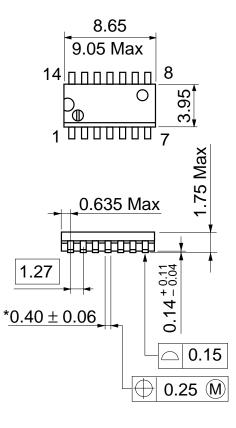
*Dimension including the plating thickness Base material dimension



Hitachi Code	FP-14DA
JEDEC	
EIAJ	Conforms
Weight (reference value)	0.23 g

Unit: mm





Hitachi Code	FP-14DN
JEDEC	Conforms
EIAJ	Conforms
Weight (reference value)	0.13 g

*Pd plating

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