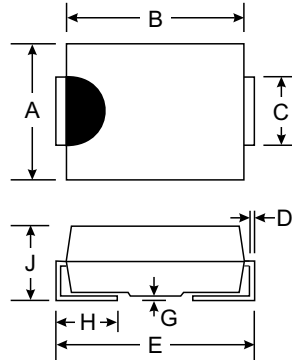


Features

- Schottky Barrier Chip
- Guard Ring Die Construction for Transient Protection
- Ideally Suited for Automatic Assembly
- Low Power Loss, High Efficiency
- Surge Overload Rating to 70A Peak
- For Use in Low Voltage, High Frequency Inverters, Free Wheeling, and Polarity Protection Application
- Plastic Material - UL Flammability Classification 94V-0



SMA		
Dim	Min	Max
A	2.29	2.92
B	4.00	4.60
C	1.27	1.63
D	0.15	0.31
E	4.80	5.59
G	0.10	0.20
H	0.76	1.52
J	2.01	2.62
All Dimensions in mm		

Mechanical Data

- Case: Molded Plastic
- Terminals: Solder Plated Terminal - Solderable per MIL-STD-202, Method 208
- Polarity: Cathode Band or Cathode Notch
- Approx. Weight: 0.064 grams
- Mounting Position: Any
- Marking: B34AN

Maximum Ratings @ T_A = 25°C unless otherwise specified

Single phase, half wave, 60Hz, resistive or inductive load unless otherwise noted.
For capacitive load, derate current by 20%.

Characteristic	Symbol	B340AN	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage	V _{RRM} V _{RWM} V _R	40	V
RMS Reverse Voltage	V _{R(RMS)}	28	V
Average Rectified Output Current (Note 3)	I _o	3.0	A
Non-Repetitive Peak Forward Surge Current, single sine-wave superimposed on rated load, 60Hz	I _{FSM}	70	A
Typical Junction Capacitance (Note 2)	C _j	300	pF
Typical Thermal Resistance, Junction to Terminal (Note 1)	R _{θJT}	25	K/W
Operating and Storage Temperature Range	T _{j, TSTG}	-40 to +125	°C

Electrical Characteristics @ T_A = 25°C unless otherwise specified

Characteristic	Symbol	Typ	Max	Unit	Conditions
Forward Voltage	V _{FM}	0.425 0.485	0.500 0.500	V	I _F = 1.5A I _F = 3.0A
Peak Reverse Current	I _{RM}	0.01	1.00	mA	V _R = 40V

- Notes:
1. Thermal Resistance: Junction to terminal, unit mounted on alumina board.
 2. Measured at 1.0 MHz and applied reverse voltage of 4.0V DC.
 3. When mounted on alumina board (82x30x1.00mm), 180° half sine wave.

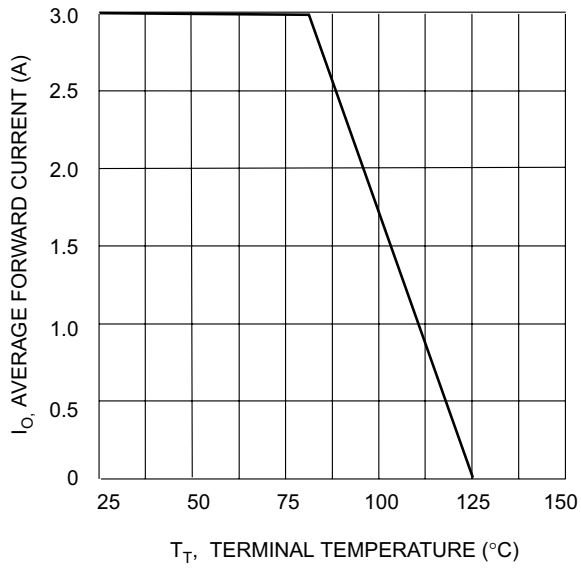


Fig. 1 Forward Current Derating Curve

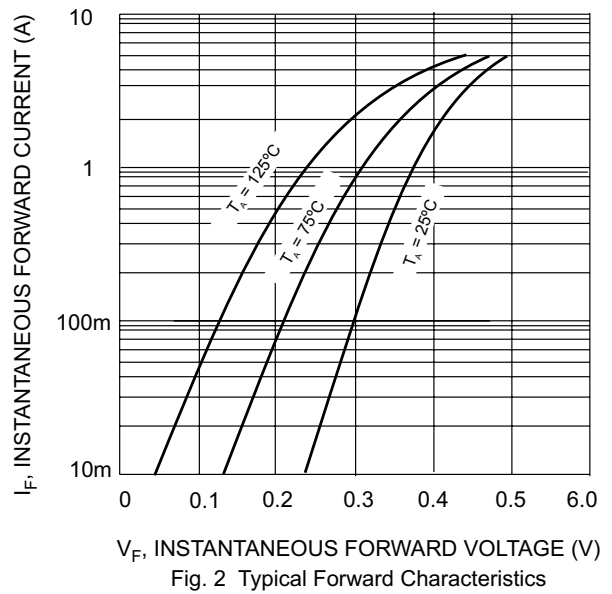


Fig. 2 Typical Forward Characteristics

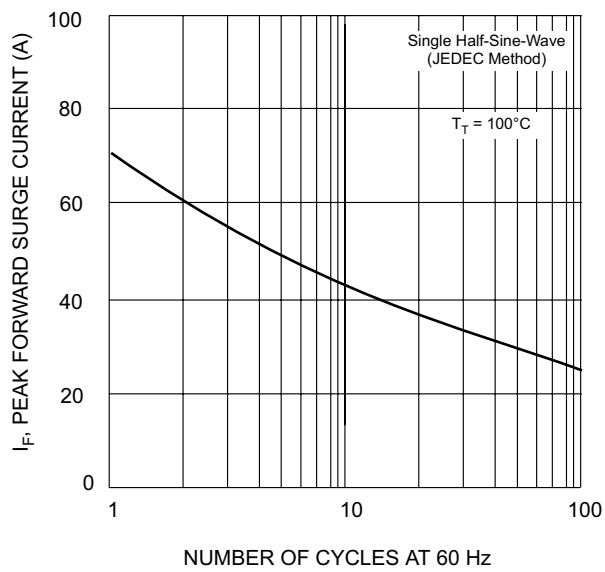


Fig. 3 Max Non-Repetitive Peak Fwd Surge Current

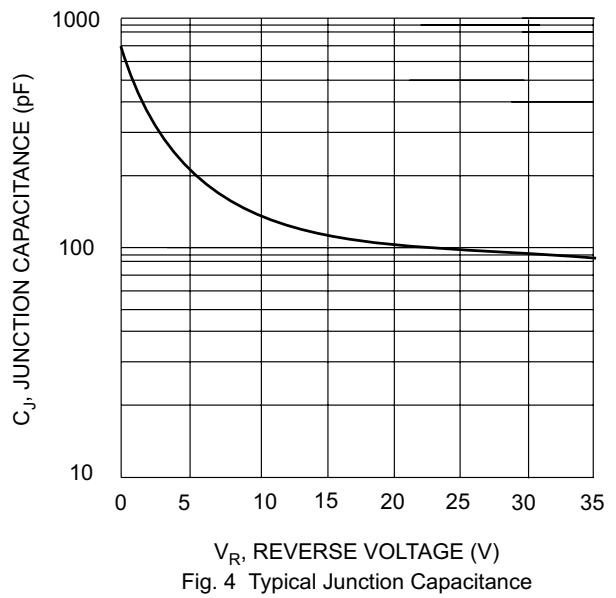


Fig. 4 Typical Junction Capacitance

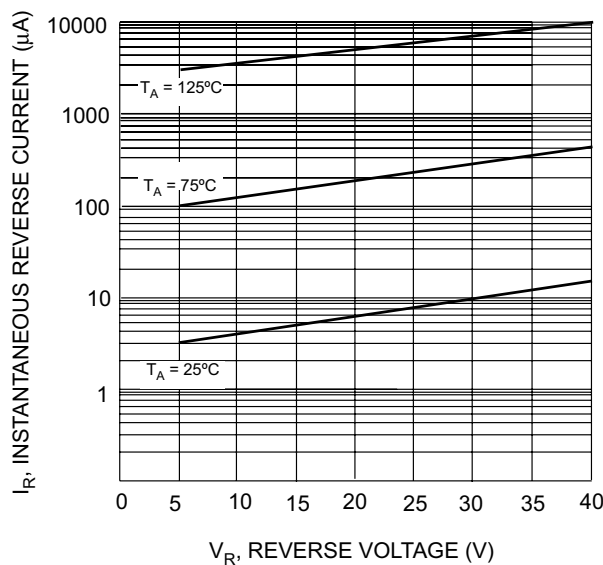


Fig. 5 Typical Reverse Characteristics