

Data Sheet B7706, Pb-Free





B7706

Low-Loss Filter for Mobile Communication

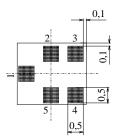
942,5 MHz

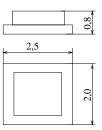
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Features

Chip Sized SAW Package QCS5H

- Low-loss RF filter for mobile telephone EGSM system, receive path
- Usable passband 35 MHz
- Unbalanced to balanced operation
- Excellent symmetry between balanced ports
- Impedance transformation from 50 Ω to 200 Ω
- Suitable for GPRS class 1 to 12
- Ceramic Package for Surface Mounted Technology (SMT)
- Pb-Free





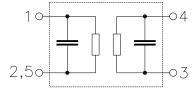
Terminals

■ Ni, gold-plated

Dimensions in mm, approx. weight 0,015 g

Pin configuration

1	Input, unbalanced
3, 4	Output, balanced
2.5	Case ground



Туре	Ordering code	Marking and Package	Packing
		according to	according to
B7706	B39941-B7706-K910	C61157-A7-A139	F61074-V8189-Z000

Electrostatic Sensitive Device (ESD)

Maximum ratings

Operable temperature range	T	- 40 / + 85	°C	
Storage temperature range	T_{stg}	- 40 / + 85	°C	
DC voltage	$V_{\rm DC}$	3	V	
ESD voltage	V* _{ESD}	100*	V	Machine Model, 10 pulses
Input power at	P_{IN}	15	dBm	peak power of GSM signal,
GSM850, GSM900,	***			duty cycle 4:8
GSM1800 and GSM1900				
Tx bands				

^{* -} acc. to JESD22-A115A (Machine Model), 10 negative & 10 positive pulses



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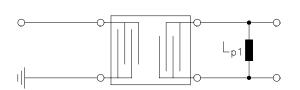
Characteristics

 $T = 25 + -2^{\circ}C$ Operating temperature: Terminating source impedance:

 $\begin{array}{ll} Z_{\rm S} & = 50~\Omega \\ Z_{\rm L} & = 200~\Omega & {\rm including~matching~network} \end{array}$ Terminating load impedance:

	min.	typ.	max.	
Center frequency f _C	; –	942,5	_	MHz
Maximum insertion attenuation α.	max			
925,0 960,0 MHz		2,6	3,2	dB
Amplitude ripple (p-p)	α			
925,0 960,0 MHz	_	1,3	1,9	dB
Output phase balance $(\phi(S_{31})-\phi(S_{21})+180^{\circ})$				
925,0 960,0 MHz	-4	0	4	degree
Output amplitude balance ($ S_{31}/S_{21} $)				
925,0 960,0 MHz	-0,3	0	0,3	dB
Input VSWR				
925,0 960,0 MHz		1,8	2,3	
Output VSWR		4.0	0.0	
925,0 960,0 MHz	_	1,8	2,3	
Attenuation α				
0,0 880,0 MHz	50	60	_	dB
880,0 905,0 MHz	30	40	_	dB
905,0 915,0 MHz	20	27	_	dB
980,01050,0 MHz	22	24	_	dB
1050,06000,0 MHz	50	65	_	dB

Test matching network



 $L_{p1} = 100 \text{ nH}$ (20% tolerance, Q = 30)



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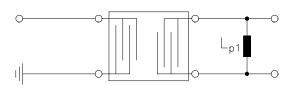
 $T = -10 \text{ to } +80 \text{ }^{\circ}\text{C}$ Operating temperature range:

Terminating source impedance:

 $Z_{\rm S} = 50~\Omega$ $Z_{\rm L} = 200~\Omega$ including matching network Terminating load impedance:

		min.	typ.	max.	
Center frequency	$f_{\mathbb{C}}$	_	942,5	_	MHz
Maximum insertion attenuation 925,0 960,0 MHz	α_{max}	_	2,7	3,5	dB
Amplitude ripple (p-p) 925,0 960,0 MHz	Δα	_	1,4	2,2	dB
Output phase balance $(\phi(S_{31})-\phi(S_{21})+180^{\circ})$ 925,0 960,0 MHz		-4	0	4	degree
Output amplitude balance ($ S_{31}/S_{21} $) 925,0 960,0 MHz		-0,3	0	0,3	dB
Input VSWR 925,0 960,0 MHz		_	1,8	2,3	
Output VSWR 925,0 960,0 MHz		_	1,8	2,3	
Attenuation	α				
0,0 880,0 MHz		50	60	_	dB
880,0 905,0 MHz		30	40	_	dB
905,0 915,0 MHz		20	27	_	dB
980,01050,0 MHz 1050,06000,0 MHz		22 50	23 65	_	dB dB

Test matching network



 $L_{p1} = 100 \text{ nH}$ (20% tolerance, Q = 30)



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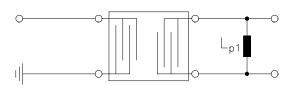
 $T = -30 \text{ to } +85 \text{ }^{\circ}\text{C}$ Operating temperature range:

Terminating source impedance:

 $Z_{\rm S} = 50~\Omega$ $Z_{\rm L} = 200~\Omega$ including matching network Terminating load impedance:

		min.	typ.	max.	
Center frequency	f _C	_	942,5	_	MHz
Maximum insertion attenuation					
925,0 960,0 MHz		_	2,8	3,6	dB
Amplitude ripple (p-p)	Δα				
925,0 960,0 MHz		_	1,5	2,3	dB
Output phase balance $(\phi(S_{31}) - \phi(S_{21}) + 180^{\circ})$					
925,0 960,0 MHz		-10	0	10	degree
Output amplitude balance ($ S_{31}/S_{21} $)			_		
925,0 960,0 MHz		-1	0	1	dB
Input VSWR					
925,0 960,0 MHz		_	2,0	_	
Output VSWR					
925,0 960,0 MHz		_	2,0	_	
	α				
0,0 880,0 MHz		50	60	_	dB
880,0 905,0 MHz		30	40	_	dB
905,0 915,0 MHz		16	20	_	dB
980,01050,0 MHz		20	22	_	dB
1050,06000,0 MHz		50	65	_	dB

Test matching network



 $L_{p1} = 100 \text{ nH}$ (20% tolerance, Q = 30)



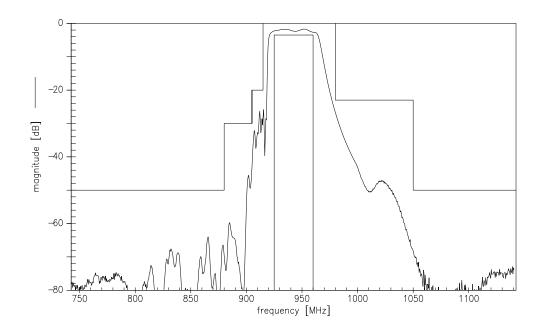
SAW Components

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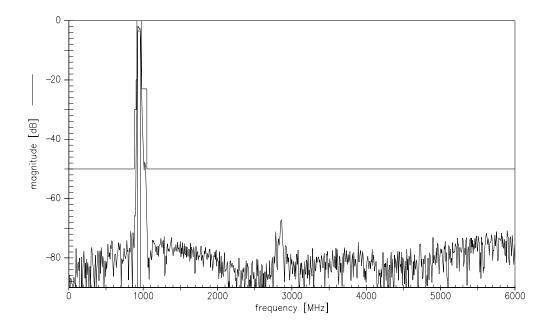
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Transfer function



Transfer function (wideband)

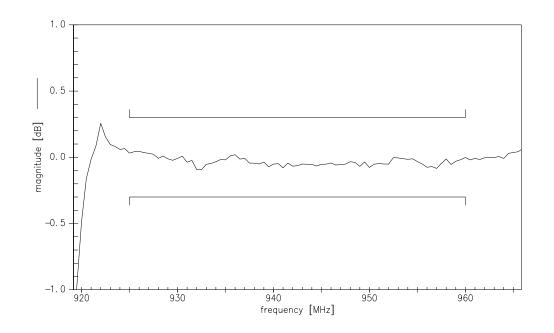


Jan 20, 2005

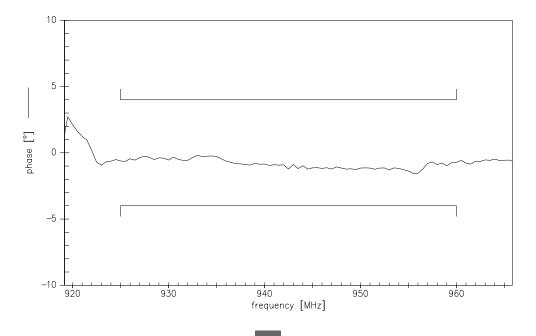




Output amplitude balance ($|S_{31}/S_{21}|$)



Output phase balance $\,(\varphi(S_{31})\!\!-\!\!\varphi(S_{21})\!\!+\!180^\circ)$



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