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## **CGR-0218Z**

# PUSH-PULL 5MHz to 210MHz HIGH LINEARITY INGAP HBT AMPLIFIER

Package: SOIC-8



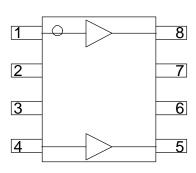


#### **Product Description**

RFMD's CGR-0218Z is a high performance InGaP HBT MMIC Amplifier designed with the InGaP process technology for excellent reliability. A Darlington configuration is utilized for broadband performance. The heterojunction increases breakdown voltage and minimizes leakage current between junctions. The CGR-0218Z contains two amplifiers for use in wideband push-pull CATV amplifiers requiring excellent second order performance. The second and third order non-linearities are greatly improved in the push-pull configuration.

# Optimum Technology Matching® Applied GaAs HBT GaAs MESFET InGaP HBT SiGe BiCMOS Si BiCMOS SiGe HBT GaAs pHEMT Si CMOS Si BJT GaN HEMT InP HBT RF MEMS LDMOS

#### **Amplifier Configuration**



#### **Features**

- 5V Single Supply
- Excellent Linearity
- Two Amplifiers in Each SOIC-8 Package Simplify Push-Pull PC Board Layout
- SOIC-8 Package
- Available in Lead-Free, RoHS Compliant Packaging

#### **Applications**

- CATV Head Ends
- CATV Line Drivers
- DOCSIS Cable Modems

| Parameter                                      | Specification |      |      | Unit  | Condition  |  |
|--|---------------|------|------|-------|--|--|
| r araineter                                    | Min.          | Тур. | Max. | Ullit | Condition  |  |
| Small Signal Gain                              |               | 17.3 |      | dB    | 5MHz to 210MHz   |  |
| Gain Flatness                                  |               | ±0.2 |      | dB    | 5MHz to 210MHz   |  |
| OIP <sub>3</sub>                               |               | 42   |      | dBm   | 5MHz to 210MHz, Tone Spacing=1MHz, P <sub>OUT</sub> per tone=+6dBm |  |
| P1dB   |               | 23   |      | dBm   | 5 MHz to 210 MHz   |  |
| Input Return Loss                              |               | 22   |      | dB    | 5MHz to 210MHz   |  |
| Output Return Loss                             |               | 22   |      | dB    | 5MHz to 210MHz   |  |
| Noise Figure, Balun Insertion Loss<br>Included |               | 4.0  |      | dB    | 5 MHz to 210 MHz   |  |
| CSO  |               | 80   |      | dBc   | 7 Ch, Flat Tilt, +50dBmV   |  |
| СТВ  |               | 67   |      | dBc   | 7 Ch, Flat Tilt, +50dBmV   |  |
| XMOD   |               | 66   |      | dBc   | 7 Ch, Flat Tilt, +50dBmV   |  |
| Device Operating Voltage                       |               | 5.0  |      | V     |  |  |
| Device Operating Current                       |               | 217  |      | mA    | 5V V <sub>CC</sub>   |  |
| Thermal Resistance (Junction to Lead)          |               | 30   |      | °C/W  | Junction to case slug  |  |

Test Conditions:  $V_{CC}=5V$ ,  $I_D=217$  mA Typ.,  $T_L=25$  °C,  $Z_S=Z_L=75\Omega$ , Push Pull Application Circuit

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DS091015

# **CGR-0218Z**



#### **Absolute Maximum Ratings**

| Parameter                              | Rating     | Unit |
|--|------------|------|
| Max Device Current (I <sub>D</sub> )   | 300        | mA   |
| Max Device Voltage (V <sub>D</sub> )   | 6.0        | V    |
| Max RF Input Power                     | 18         | dBm  |
| Max Junction Temp (T <sub>J</sub> )    | 150        | °C   |
| Operating Temp Range (T <sub>L</sub> ) | -40 to +85 | °C   |
| Max Storage Temp                       | 150        | °C   |
| Min Storage Temp                       | -40        | °C   |

Operation of this device beyond any one of these limits may cause permanent damage. For reliable continuous operation, the device voltage and current must not exceed the maximum operating values specified in the table on page one. Bias Conditions should also satisfy the following expression:  $I_DV_D \! < \! (T_J \! - \! T_L) / R_{TH}, j \! - \! l \text{ and } T_L \! = \! T_{LEAD}$ 

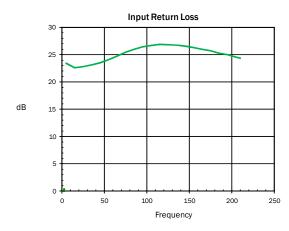


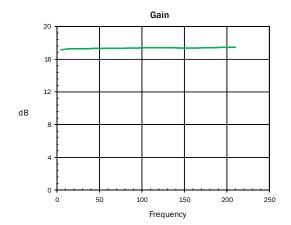
Caution! ESD sensitive device.

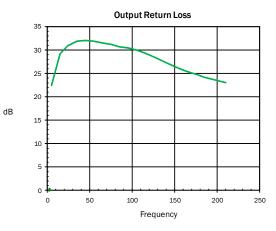
Exceeding any one or a combination of the Absolute Maximum Rating conditions may cause permanent damage to the device. Extended application of Absolute Maximum Rating conditions to the device may reduce device reliability. Specified typical performance or functional operation of the device under Absolute Maximum Rating conditions is not implied.

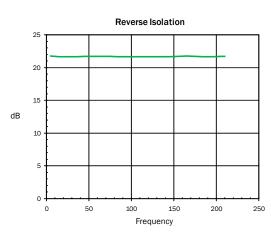
RoHS status based on EUDirective 2002/95/EC (at time of this document revision).

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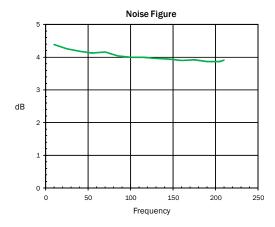


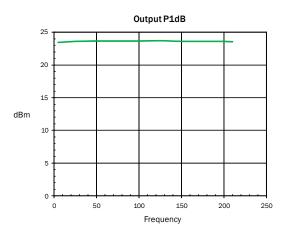


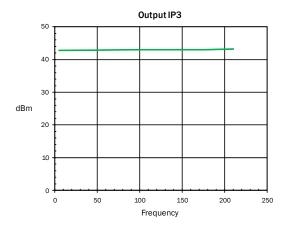


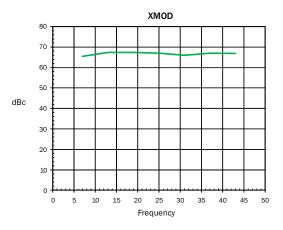


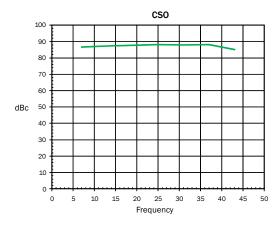
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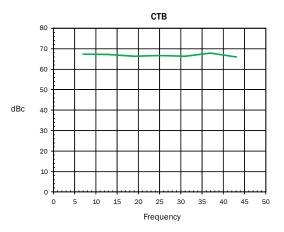






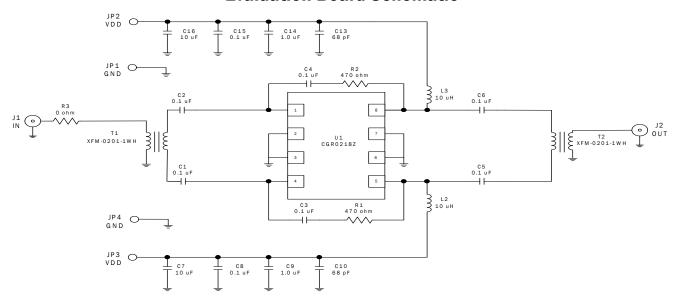




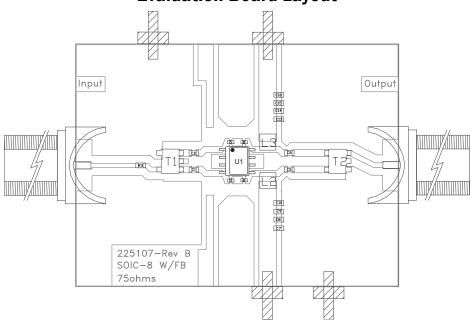




#### **Evaluation Board Schematic**



#### **Evaluation Board Layout**



| Value                | QTY | Location                        |  |  |
|----------------------|-----|---------------------------------|--|--|
| 68pF                 | 2   | C10, C13                        |  |  |
| 0.1uF                | 8   | C1, C2, C3, C4, C5, C6, C8, C15 |  |  |
| 1.0 uF               | 2   | C9, C14                         |  |  |
| 10uF                 | 2   | C7, C10                         |  |  |
| ΟΩ                   | 1   | R3                              |  |  |
| 470Ω                 | 2   | R1, R2                          |  |  |
| 10 uH                | 2   | L2, L3                          |  |  |
| RFMD™ XFM-0201-1WH 2 |     | T1, T2                          |  |  |



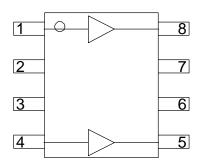


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| Pin           | Function   | Description  |  |  |  |
|---------------|------------|--|--|--|--|
| 1             | RF IN      | RF input pin. External DC blocking capacitor is required.  |  |  |  |
| 2, 3,<br>6, 7 | GND        | Connection to ground. Use via holes for best performance to reduce lead inductance as close to ground leads as possible.   |  |  |  |
| 4             | RF IN      | Same as pin 1.   |  |  |  |
| 5             | RF OUT/VCC | RF output and bias pin (open collector).   |  |  |  |
| 8             | RF OUT/VCC | Same as pin 5.   |  |  |  |
| EPAD          | GND        | Exposed area on the bottom side of the package must be soldered to the ground plane of the board for optimum thermal and RF performance. Several vias should be located under the EPAD as shown in the recommended land pattern. |  |  |  |

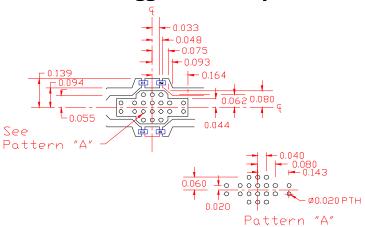
#### **Pin Out**

#### **Amplifier Configuration**



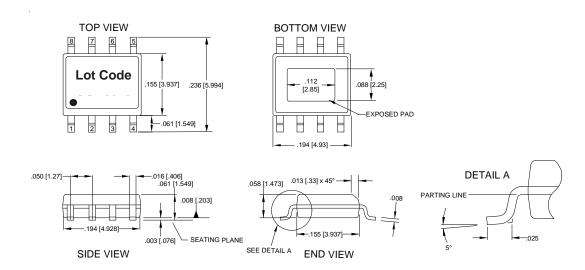


#### **Suggested Pad Layout**



# Package Drawing and Marking Package Type: SOIC-8

Dimensions in inches (millimeters)
Refer to drawing posted at www.rfmd.com for tolerances.







### **Ordering Information**

| Part Number     | Description                       | Reel Size (in.) | Devices/Reel |
|-----------------|-----------------------------------|-----------------|--------------|
| CGR0218ZSB      | 5pcs Sample Bag                   | NA              | NA           |
| CGR0218ZSQ      | 25pcs Sample Bag                  | NA              | NA           |
| CGR0218ZSR      | Lead Free, RoHS Compliant         | 7               | 100          |
| CGR0218ZTR7     | Lead Free, RoHS Compliant         | 7               | 750          |
| CGR0218ZTR13    | Lead Free, RoHS Compliant         | 13              | 2500         |
| CGR0218PCBA-410 | 5 MHz to 210 MHz Evaluation Board | NA              | NA           |

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