

Description

- Dual chip digital transistor

Features

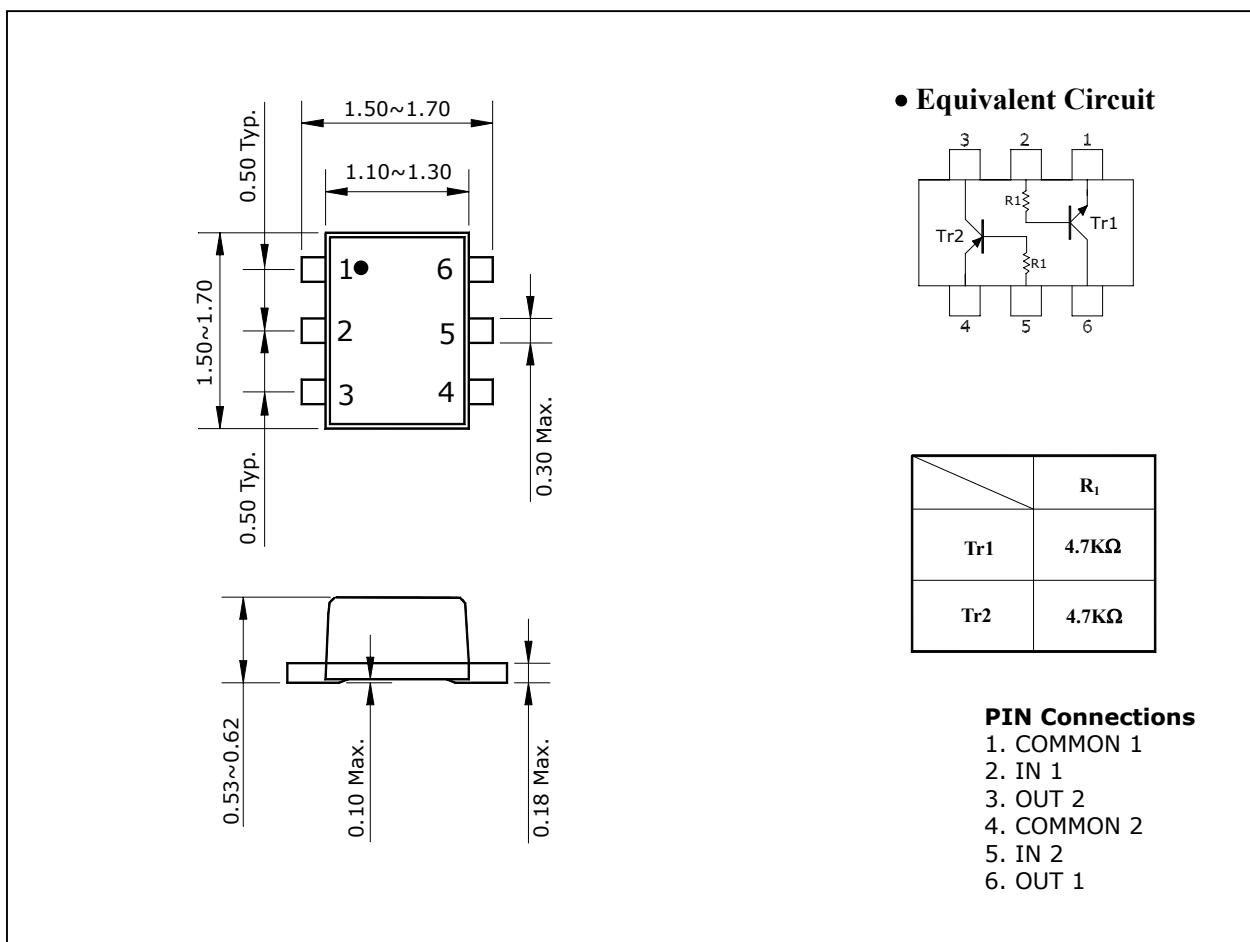
- Both SRC1210 chip and SRA2210 chip in SOT-563F package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

Ordering Information

Type NO.	Marking	Package Code
SUR540EF	SJ	SOT-563F

Outline Dimensions

unit : mm



Absolute maximum ratings (Tr1, Tr2) Ta=25°C

Characteristic	Symbol	Rating		Unit
		Tr1	Tr2	
Output voltage	V _O	50	-50	
Input voltage	V _I	20,-5	-20,5	V
Output current	I _O	100	-100	mA
Power dissipation	P _D *	150		mW
Junction temperature	T _J	150		°C
Storage temperature range	T _{stg}	-55 ~ 150		°C

*: Total rating

Electrical Characteristics [Tr1]

(Ta=25°C)

Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V _O =50V, V _I =0	-	-	500	nA
DC current gain	G _I	V _O =5V, I _O =10mA	120	-	-	-
Output voltage	V _{O(ON)}	I _O =10mA, I _I =0.5mA	-	0.1	0.3	V
Input voltage (ON)	V _{I(ON)}	V _O =0.2V, I _O =5mA	-	0.8	1.2	V
Input voltage (OFF)	V _{I(OFF)}	V _O =5V, I _O =0.1mA	0.3	0.55	-	V
Transition frequency	f _T *	V _O =10V, I _O =5mA, f=1MHz	-	200	-	MHz
Input current	I _I	V _I =5V, I _O =0	-	-	1.8	mA
Input resistor (Input to base)	R _I	-	3.3	4.7	6.1	KΩ

*: Characteristic of transistor only

Electrical Characteristics [Tr2]

(Ta=25°C)

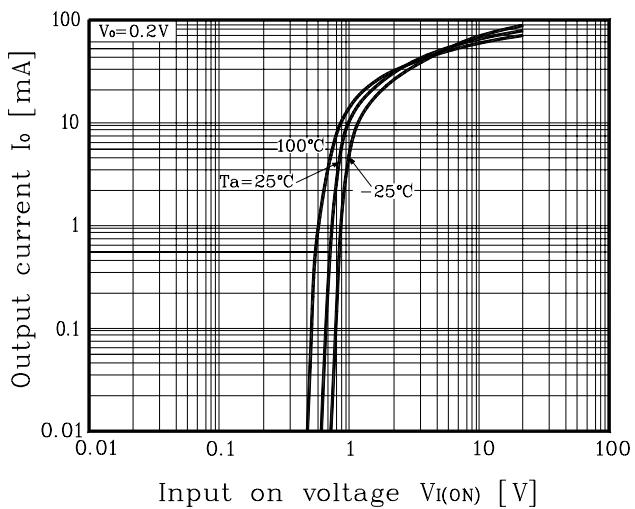
Characteristic	Symbol	Test Condition	Min.	Typ.	Max.	Unit
Output cut-off current	I _{O(OFF)}	V _O =-50V, V _I =0	-	-	-500	nA
DC current gain	G _I	V _O =-5V, I _O =-10mA	120	-	-	-
Output voltage	V _{O(ON)}	I _O =-10mA, I _I =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	V _{I(ON)}	V _O =-0.2V, I _O =-5mA	-	-0.8	-1.2	V
Input voltage (OFF)	V _{I(OFF)}	V _O =-5V, I _O =-0.1mA	-0.3	-0.55	-	V
Transition frequency	f _T *	V _O =-10V, I _O =-5mA, f=1MHz	-	200	-	MHz
Input current	I _I	V _I =-5V, I _O =0	-	-	-1.8	mA
Input resistor (Input to base)	R _I	-	3.3	4.7	6.1	KΩ

*: Characteristic of transistor only

Electrical Characteristic Curves

[Tr1]

Fig. 1 $I_O - V_{I(ON)}$



[Tr2]

Fig. 1 $I_O - V_{I(ON)}$

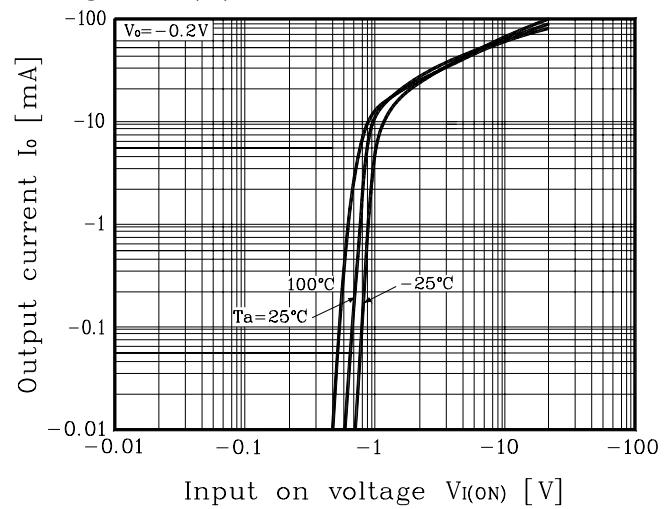


Fig. 2 $I_O - V_{I(OFF)}$

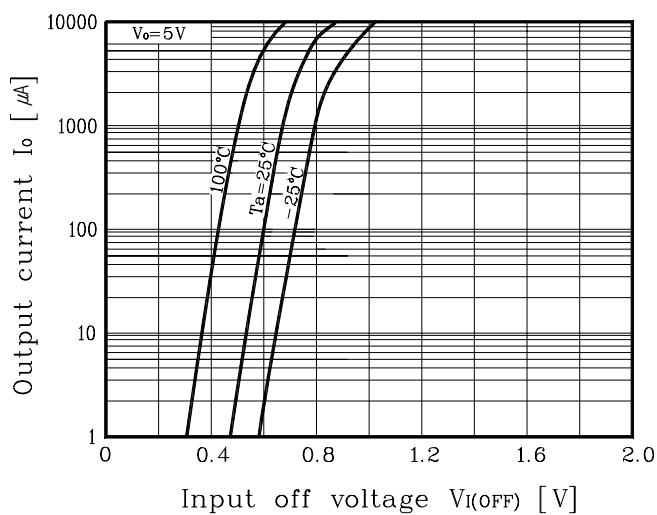


Fig. 2 $I_O - V_{I(OFF)}$

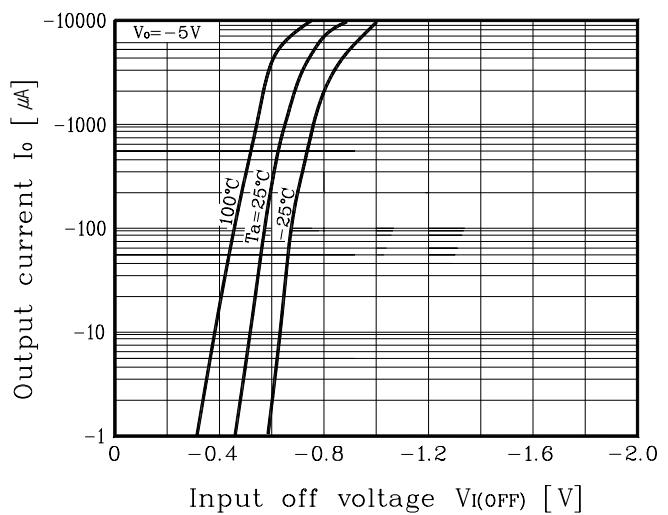


Fig. 3 $G_I - I_O$

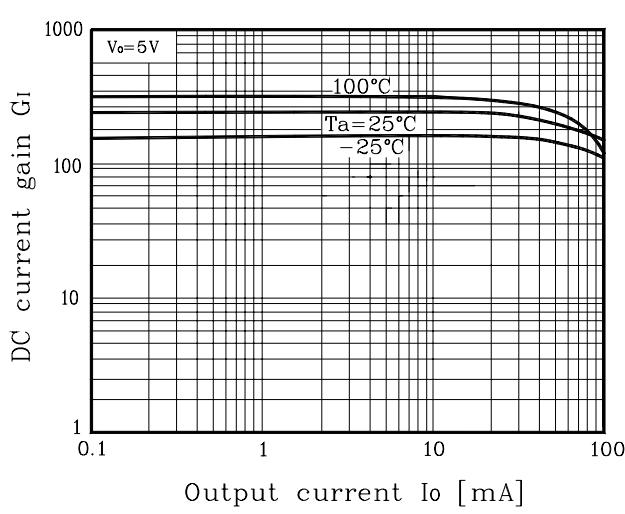
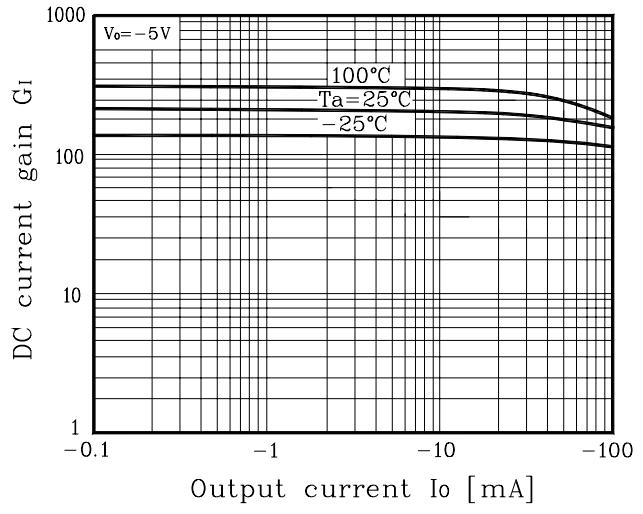


Fig. 3 $G_I - I_O$



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