

# SUR532H

#### Epitaxial planar PNP silicon transistor

## **Descriptions**

• Dual chip digital transistor

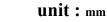
#### **Features**

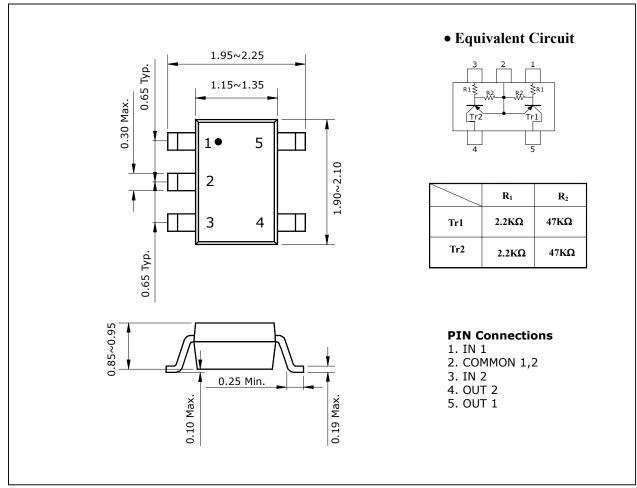
- Two SRA2205 chips in SOT-353 package
- Simplify circuit design
- Reduce a quantity of parts and manufacturing process

### **Ordering Information**

Type NO.	Marking	Package Code
SUR532H	32H	SOT-353

### **Outline Dimensions**





Absolute Maximum Ratings [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	Rating	Unit
Output voltage	Vo	-50	V
Input voltage	V <sub>I</sub>	-15, 5	V
Output current	$I_{O}$	-100	mA
Power dissipation	P <sub>D</sub> **	200	mW
Junction temperature	T <sub>J</sub>	150	°C
Storage temperature range	$T_{stg}$	-55 ~ 150	°C

<sup>\*:</sup> Total rating

## **Electrical Characteristics** [Tr1,Tr2]

(Ta=25°C)

Characteristic	Symbol	<b>Test Condition</b>	Min.	Тур.	Max.	Unit
Output cut-off current	$I_{O(OFF)}$	$V_0 = -50V, V_I = 0$	-	-	-500	nA
DC current gain	$G_{\mathrm{I}}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-10mA	80	200	-	-
Output voltage	$V_{O(ON)}$	$I_O$ =-10mA, $I_I$ =-0.5mA	-	-0.1	-0.3	V
Input voltage (ON)	$V_{I(ON)}$	$V_0 = -0.2V$ , $I_0 = -5mA$	-	-	-1.1	V
Input voltage (OFF)	$V_{I(OFF)}$	V <sub>O</sub> =-5V, I <sub>O</sub> =-0.1mA	-0.5	-	-	V
Transition frequency	$f_T^*$	$V_O=-10V$ , $I_O=-5$ mA, $f=1$ MHz	-	200	-	MHz
Input current	$I_{I}$	$V_{I}$ =-5 $V$ , $I_{O}$ =0	-	-	-3.6	mA
Input resistor (Input to base)	$R_1$	-	1.54	2.2	2.86	<b>K</b> Ω
Input resistor (Base to common)	R <sub>2</sub>	-	33	47	61	<b>K</b> Ω

<sup>\* :</sup> Characteristic of transistor only

# **Electrical Characteristic Curves** [Tr1,Tr2]

Fig. 1  $I_0$  -  $V_{I(ON)}$ -100  $V_0$  - 0.2V  $V_0$  - 0.2

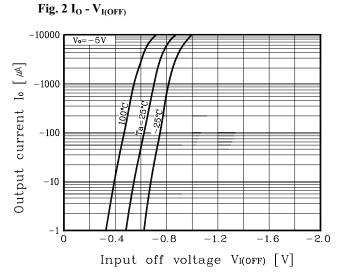


Fig. 3 G<sub>I</sub>- I<sub>O</sub>

1000
V<sub>o</sub>=-5V

Ta=25°C

Ta=25°C

Output current Io [mA]

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