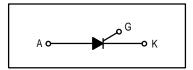
Silicon Controlled Rectifiers Reverse Blocking Triode Thyristors

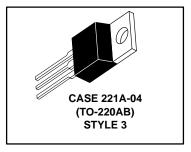
... designed primarily for half-wave ac control applications, such as motor controls, heating controls and power supplies; or wherever half-wave silicon gate-controlled, solid-state devices are needed.

- Glass Passivated Junctions with Center Gate Fire for Greater Parameter Uniformity
 and Stability
- Small, Rugged, Thermowatt Construction for Low Thermal Resistance, High Heat Dissipation and Durability
- Blocking Voltage to 800 Volts

S2800 Series

SCRs 10 AMPERES RMS 50 thru 800 VOLTS





Rating	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blocking Voltage ⁽¹⁾ (T _J = 25 to 100°C, Gate Open) F A B S2800 D M N	Vrrm Vdrm	50 100 200 400 600 800	Volts
Peak Non-repetitive Reverse Voltage and Non-Repetitive Off-State Voltage ⁽¹⁾ F A B S2800 D M N	Vrsm Vdsm	75 125 250 500 700 900	Volts
RMS Forward Current (All Conduction Angles)TC = 75°C	IT(RMS)	10	Amps
Peak Forward Surge Current (1 Cycle, Sine Wave, 60 Hz, $T_C = 80^{\circ}C$)	ITSM	100	Amps
Circuit Fusing Considerations (t = 8.3 ms)	l ² t	40	A ² s
Forward Peak Gate Power (t $\leq 10 \mu$ s)	PGM	16	Watts
Forward Average Gate Power	P _{G(AV)}	0.5	Watt
Operating Junction Temperature Range	Tj	-40 to +100	°C
Storage Temperature Range	T _{stg}	-40 to +150	°C

MAXIMUM RATINGS (T_{.1} = 25°C unless otherwise noted.)

1. V_{DRM} and V_{RRM} for all types can be applied on a continuous basis. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode. Blocking voltages shall not be tested with a constant current source such that the voltage ratings of the devices are exceeded.



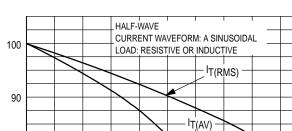
S2800 Series

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Case	R _{θJC}	2	°C/W

ELECTRICAL CHARACTERISTICS (T_C = 25°C unless otherwise noted.)

Characteristic	Symbol	Min	Тур	Max	Unit
Peak Forward or Reverse Blocking Current $(V_{AK} = Rated V_{DRM} \text{ or } V_{RRM}, Gate Open)$ $T_C = 25^{\circ}C$ $T_C = 100^{\circ}C$	IDRM, IRRM		_	10 2	μA mA
Instantaneous On-State Voltage, (I_{TM} = 30 A Peak, Pulse Width \leq 1 ms, Duty Cycle \leq 2%)	VT	-	1.7	2	Volts
Gate Trigger Current (Continuous dc) (V _D = 12 Vdc, R _L = 30 Ohms)	IGT	_	8	15	mA
Gate Trigger Voltage (Continuous dc) (V _D = 12 Vdc, R _L = 30 Ohms)	V _{GT}	-	0.9	1.5	Volts
Holding Current (Gate Open, V _D = 12 Vdc, I _T = 150 mA)	Ч	-	10	20	mA
Gate Controlled Turn-On Time $(V_D = Rated V_{DRM}, I_{TM} = 2 A, I_{GR} = 80 mA)$	tgt	-	1.6	_	μs
Circuit Commutated Turn-Off Time $(V_D = V_{DRM}, I_{TM} = 2 \text{ A}, \text{ Pulse Width} = 50 \mu\text{s},$ $dv/dt = 200 V/\mu\text{s}, di/dt = 10 A/\mu\text{s}, T_C = 75^{\circ}\text{C})$	tq	_	25	_	μs
Critical Rate-of-Rise of Off-State Voltage (V _D = Rated V _{DRM} , Exponential Rise, T _C = 100°C)	dv/dt	-	100	_	V/µs



4

6

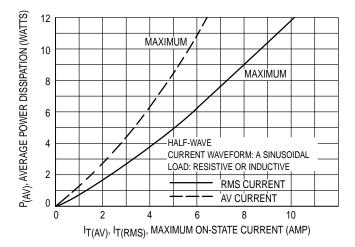
I_{T(AV)}, I_{T(RMS)}, ON-STATE CURRENT (AMPS)

8

10

FIGURE 1 – CURRENT DERATING

FIGURE 2 – POWER DISSIPATION



 $T_{\rm C}$, MAXIMUM ALLOWABLE CASE TEMPERATURE (°C)

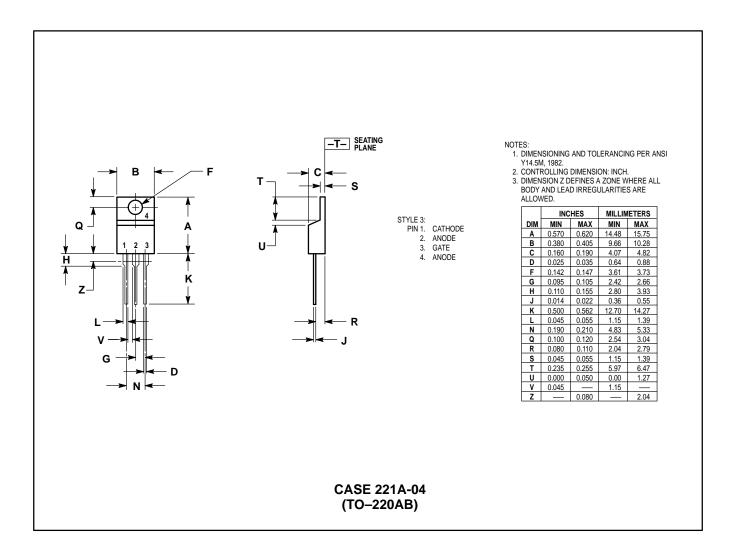
80

70 L 0

2

S2800 Series

PACKAGE DIMENSIONS



S2800 Series

Motorola reserves the right to make changes without further notice to any products herein. Motorola makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Motorola assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters can and do vary in different applications. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. Motorola does not convey any license under its patent rights nor the rights of others. Motorola products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Motorola product could create a situation where personal injury or death may occur. Should Buyer purchase or use Motorola products for any such unintended or unauthorized application, Buyer shall indemnify and hold Motorola and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death Motorola was negligent regarding the design or manufacture of the part. Motorola and "" are registered trademarks of Motorola, Inc. Notorola, Inc. is an Equal Opportunity/Affirmative Action Employer.

Literature Distribution Centers:

USA: Motorola Literature Distribution; P.O. Box 20912; Phoenix, Arizona 85036. EUROPE: Motorola Ltd.; European Literature Centre; 88 Tanners Drive, Blakelands, Milton Keynes, MK14 5BP, England. JAPAN: Nippon Motorola Ltd.; 4-32-1, Nishi-Gotanda, Shinagawa-ku, Tokyo 141, Japan. ASIA PACIFIC: Motorola Semiconductors H.K. Ltd.; Silicon Harbour Center, No. 2 Dai King Street, Tai Po Industrial Estate, Tai Po, N.T., Hong Kong.



 \Diamond

