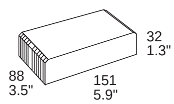
power-one™ Changing the Shape of Power



Input voltage up to 80 VDC
Single output of 5.1 - 36 VDC
No input to output isolation

- High efficiency up to 96%
- Wide input voltage range
- Low input to output differential voltage
- Very good dynamic properties
- Input undervoltage lock-out
- External output voltage adjustment and inhibit
- Two temperature ranges
- Continuous no-load and short-circuit proof
- No derating

Safety according to IEC/EN 60950

c UL us CE

Summary

The PSC series of positive switching regulators is designed as power supply modules for electronic systems. Their major advantages include a high level of efficiency that remains virtually constant over the entire input voltage range, high reliability, low ripple and excellent dynamic response.

Modules with input voltages up to 80 V are specially designed for secondary switched and battery driven applications. The case design allows operation at nominal load up to 71°C without additional cooling.

Model Selection and Key Data

Table 1: Type survey

Output voltage $V_{o\ nom}$ [V]	Output current $I_{o\ nom}$ [A]	Input voltage range V_i [V] [1]	Input voltage $V_{i\ nom}$ [V]	Efficiency [2]		Type designation	Options	Superseded old type (phased-out)
				η_{min} [%]	η_{typ} [%]			
5.1	12	7 - 40	20	82	83	PSC 5A12-7iR	-9, L, P, C, D	PSR 512-7
5.1	10	8 - 80	40	76	79	PSC 5A10-7iR		PSR 510-7
12	8	15 - 80	40	88	89	PSC 128-7iR		PSR 128-7
15	8	19 - 80	40	90	91	PSC 158-7iR		PSR 158-7
24	8	29 - 80	50	93	94	PSC 248-7iR		PSR 248-7
36	8	42 - 80	60	95	96	PSC 368-7iR		PSR 368-7

[1] See: *Electrical Input Data:* $\Delta V_{io\ min}$ (min. differential voltage $V_i - V_o$).
[2] Efficiency at $V_{i\ nom}$ and $I_{o\ nom}$.

Non standard input/output configurations or special custom adaptions are available on request.

Table of Contents

Part Number Description

PSC 12 8 -7 L i R P C D

Positive switching regulator in case C03 .... PSC
Nominal output voltage in volt (5A for 5.1 V) .... 5A - 36
Nominal output current in ampere .... 8 - 12
Operational ambient temperature range T_A
    –25 to 71 °C .... -7
    –40 to 71 °C (option) .... -9
Input filter (option) L
Inhibit input i
Control input for output voltage adjustment [1] .... R
Potentiometer [1] (option) .... P
Thyristor crowbar (option) C
Input/output voltage monitor (option) D/D1

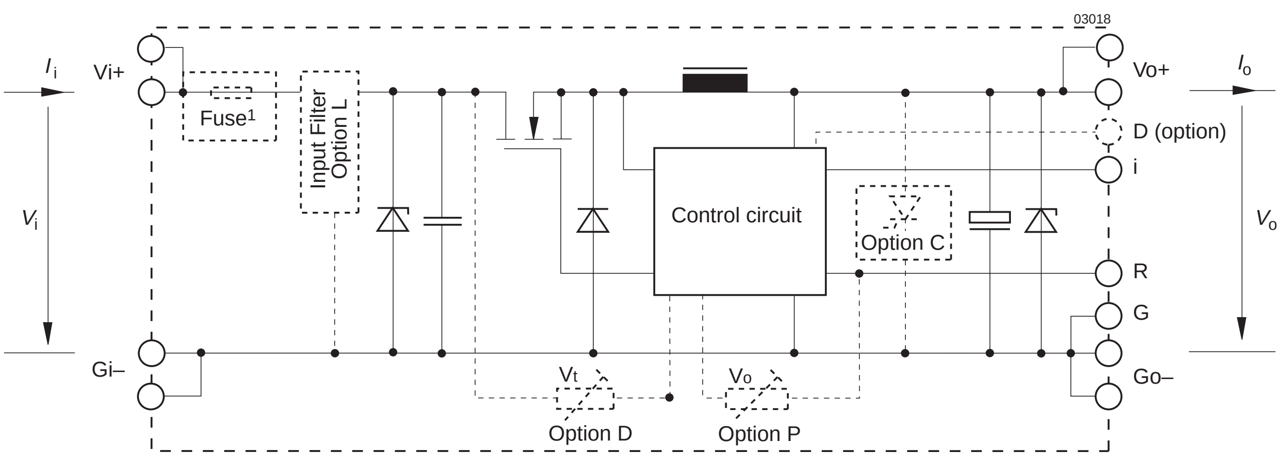
[1] Feature R excludes option P and vice versa.

Example: PSC 128-7LiPC = A positive switching regulator with a 12 V, 8 A output, ambient temperature range of –25 to 71 °C, input filter, inhibit input, output adjust potentiometer and thyristor crowbar.

Functional Description

The switching regulators are designed using the buck converter topology. See also: *Technical Information: Topologies.* The input is not electrically isolated from the output. During the on period of the switching transistor, current is transferred to the output and energy is stored in the output choke in the form of flux. During the off period, this energy forces the current to continue flowing through the output, to the load and back through the freewheeling diode. Regulation is accomplished by varying the on to off duty ratio of the power switch.

These regulators are ideal for a wide range of applications, where input to output isolation is not necessary, or where already provided by an external front end (e.g. a transformer with rectifier). To optimise customer's needs, additional options and accessories are available.



[1] Fuse fitted for option

Fig. 1
Block diagram

Electrical Input Data

General Conditions: T_A = 25°C, unless T_C is specified

Table 2a: Input data

Input			PSC 5A12			PSC 5A10			PSC 128			
Characteristics		Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
V_i	Operating input voltage	$I_o = 0$ - $I_{o\,nom}$ $T_{C\,min}$ - $T_{C\,max}$	7		40	8		80	15		80	VDC
$\Delta V_{io\,min}$	Min. diff. voltage $V_i - V_o$ [1]				1.9			2.9			3	
$V_{i\,o}$	Undervoltage lock-out			6.3			7.3			7.3		
$I_{i\,0}$	No load input current	$I_o = 0$, $V_{i\,min}$ - $V_{i\,max}$			45			40			35	mA
$I_{inr\,p}$	Peak value of inrush current	$V_{i\,nom}$ without option L		150			250			250		A
$t_{inr\,r}$	Rise time			5			5			5		µs
$t_{inr\,h}$	Time to half-value			40			40			40		
$I_{inr\,p}$	Peak value of inrush current	$V_{i\,nom}$ with option L		250			350			350		A
$t_{inr\,r}$	Rise time			25			25			25		µs
$t_{inr\,h}$	Time to half-value			125			125			125		
$u_{i\,RFI}$	Input RFI level, EN 55011/22 0.15 - 30 MHz	$V_{i\,nom}$, $I_{o\,nom}$ with option L [2]			B			B			B	

Table 2b: Input data

Input			PSC 158			PSC 248			PSC 368			
Characteristics		Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
V_i	Operating input voltage	$I_o = 0$ - $I_{o\,nom}$ $T_{C\,min}$ - $T_{C\,max}$	19		80	29		80	42		80	VDC
$\Delta V_{io\,min}$	Min. diff. voltage $V_i - V_o$ [1]				4			5			6 [1]	
$V_{i\,o}$	Undervoltage lock-out			7.3			12			19		
$I_{i\,0}$	No load input current	$I_o = 0$, $V_{i\,min}$ - $V_{i\,max}$			35			35			40	mA
$I_{inr\,p}$	Peak value of inrush current	$V_{i\,nom}$ without option L		250			250			250		A
$t_{inr\,r}$	Rise time			5			5			5		µs
$t_{inr\,h}$	Time to half-value			40			40			40		
$I_{inr\,p}$	Peak value of inrush current	$V_{i\,nom}$ with option L		350			350			350		A
$t_{inr\,r}$	Rise time			25			25			25		µs
$t_{inr\,h}$	Time to half-value			125			125			125		
$u_{i\,RFI}$	Input RFI level, EN 55011/22 0.15 - 30 MHz	$V_{i\,nom}$, $I_{o\,nom}$ with option L [2]			B			B			B	

[1] The minimum differential voltage $\Delta V_{io\,min}$ between input and output increases linearly by 0 to 1 V between T_A = 46°C and 71°C (T_C = 70°C and 95°C)
[2] With additional external input capacitor C_e = 120 µF/100 V (e.g. Nichicon PF series, or equivalent).

External Input Circuitry

The sum of the lengths of the supply lines to the source or to the nearest capacitor ≥100 µF (a + b) should not exceed 5 m unless option L is fitted. This option is recommended in order to prevent power line oscillations and reduce superimposed interference voltages. See also: *Technical Information: Application Notes*.

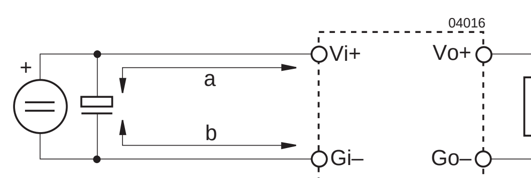


Fig. 2
Switching regulator with long supply lines.

power-one™ Changing the Shape of Power

Electrical Output Data

General Conditions:
– T_A = 25°C, unless T_C is specified
– With R or option P, output voltage $V_o = V_{o\,nom}$ at $I_{o\,nom}$

Table 3a: Output data

Output				PSC 5A12			PSC 5A10			PSC 128			
Characteristics			Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
V_o	Output voltage		$V_{i\,nom}$, $I_{o\,nom}$	5.07		5.13	5.07		5.13	11.93		12.07	V
I_o	Output current [1]		$V_{i\,min}$ - $V_{i\,max}$ $T_{C\,min}$ - $T_{C\,max}$	0		12.0	0		10.0	0		8.0	A
I_{oL}	Output current limitation response			12.0		15.6	10.0		13.0	8.0		10.4	
u_o	Output voltage noise	Switching freq.	$V_{i\,nom}$, $I_{o\,nom}$ IEC/EN 61204 [2] BW = 20 MHz		25	30		25	30		50	90	mV_{pp}
		Total			29	34		29	34		54	94	
$\Delta V_{o\,U}$	Static line regulation		$V_{i\,min}$ - $V_{i\,max}$, $I_{o\,nom}$		30	45		30	45		50	75	mV
$\Delta V_{o\,l}$	Static load regulation		$V_{i\,nom}$, I_o = 0 - $I_{o\,nom}$		20	25		20	25		30	35	
$u_{o\,d}$	Dynamic load regulation	Voltage deviat.	$V_{i\,nom}$ $I_{o\,nom} \leftrightarrow {}^1/_3\, I_{o\,nom}$ IEC/EN 61204 [2]		130			130			130		
t_d		Recovery time			40			50			60		µs
α_{Uo}	Temperature coefficient $\Delta V_o/\Delta T_C$ ($T_{C\,min}$ - $T_{C\,max}$)		$V_{i\,min}$ - $V_{i\,max}$ I_o = 0 - $I_{o\,nom}$			±1			±1			±2	mV/K
						±0.02			±0.02			±0.02	%/K

Table 3b: Output data

Output				PSC 158			PSC 248			PSC 368			
Characteristics			Conditions	min	typ	max	min	typ	max	min	typ	max	Unit
V_o	Output voltage		$V_{i\,nom}$, $I_{o\,nom}$	14.91		15.09	23.86		24.14	35.78		36.22	V
I_o	Output current [1]		$V_{i\,min}$ - $V_{i\,max}$ $T_{C\,min}$ - $T_{C\,max}$	0		8.0	0		8.0	0		8.0	A
I_{oL}	Output current limitation response			8.0		10.4	8.0		10.4	8.0		10.4	
u_o	Output voltage noise	Switching freq.	$V_{i\,nom}$, $I_{o\,nom}$ IEC/EN 61204 [2] BW = 20 MHz		50	90		55	150		80	190	mV_{pp}
		Total			54	94		60	155		85	195	
$\Delta V_{o\,U}$	Static line regulation		$V_{i\,min}$ - $V_{i\,max}$, $I_{o\,nom}$		70	100		150	220		200	270	mV
$\Delta V_{o\,l}$	Static load regulation		$V_{i\,nom}$, I_o = 0 - $I_{o\,nom}$		30	45		120	160		125	160	
$u_{o\,d}$	Dynamic load regulation	Voltage deviat.	$V_{i\,nom}$ $I_{o\,nom} \leftrightarrow {}^1/_3\, I_{o\,nom}$ IEC/EN 61204 [2]		130			150			220		
t_d		Recovery time			60			80			100		µs
α_{Uo}	Temperature coefficient $\Delta V_o/\Delta T_C$ ($T_{C\,min}$ - $T_{C\,max}$)		$V_{i\,min}$ - $V_{i\,max}$ I_o = 0 - $I_{o\,nom}$			±3			±5			±8	mV/K
						±0.02			±0.02			±0.02	%/K

[1] See also: *Thermal Considerations.*
[2] See: *Technical Information: Measuring and Testing.*

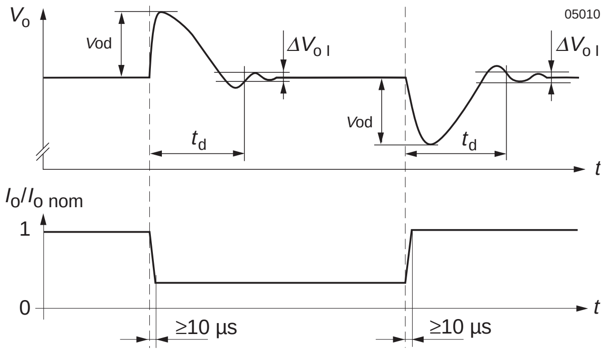


Fig. 3
Dynamic load regulation.

Thermal Considerations

When a switching regulator is located in free, quasi-stationary air (convection cooling) at a temperature T_A = 71°C and is operated at its nominal output current $I_{o\,nom}$, the case temperature T_C will be about 95°C after the warm-up phase, measured at the *Measuring point of case temperature* T_C (see: *Mechanical Data*).

Under practical operating conditions, the ambient temperature T_A may exceed 71°C, provided additional measures (heat sink, fan, etc.) are taken to ensure that the case temperature T_C does not exceed its maximum value of 95°C.

Example: Sufficient forced cooling allows $T_{A\,max}$ = 85°C. A simple check of the case temperature T_C ($T_C \leq$ 95°C) at full load ensures correct operation of the system.

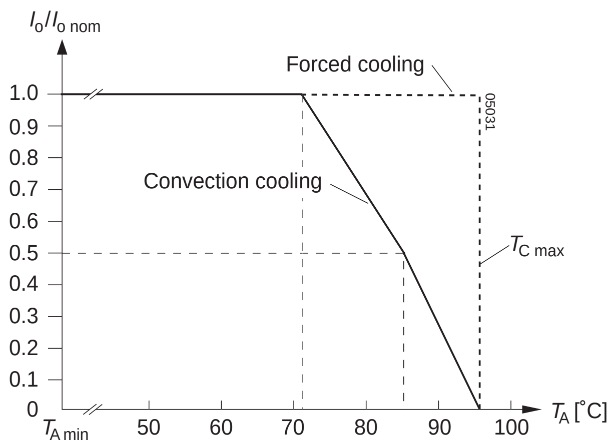


Fig. 4
Output current derating versus temperature

Output Protection

A voltage suppressor diode which in worst case conditions fails into a short circuit (or a thyristor crowbar, option C) protects the output against an internally generated overvoltage. Such an overvoltage could occur due to a failure of either the control circuit or the switching transistor. The output protection is not designed to withstand externally applied overvoltages. The user should ensure that systems with Power-One power supplies, in the event of a failure, do not result in an unsafe condition (fail-safe).

Parallel and Series Connection

Outputs of equal nominal voltages can be parallel-connected. However, the use of a single unit with higher output power, because of its power dissipation, is always a better solution.

In parallel-connected operation, one or several outputs may operate continuously at their current limit knee-point which will cause an increase of the heat generation. Consequently, the max. ambient temperature value should be reduced by 10 K.

Outputs can be series-connected with any other module. In series-connection the maximum output current is limited by the lowest current limitation. Electrically separated source voltages are needed for each module!

Short Circuit Behaviour

A constant current limitation circuit holds the output current almost constant whenever an overload or a short circuit is applied to the regulator's output. It acts self-protecting and recovers – in contrary to the fold back method – automatically after removal of the overload or short circuit condition.

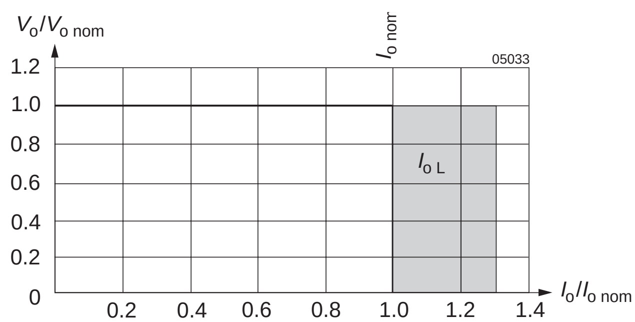


Fig. 5
Overload, short-circuit behaviour V_o versus I_o.

Auxiliary Functions

i Inhibit for Remote On and Off

Note: With open i-input, output is enabled (V_o = on)

The inhibit input allows the switching regulator output to be disabled via a control signal. In systems with several units, this feature can be used, for example, to control the activation sequence of the regulators by a logic signal (TTL, C-MOS, etc.). An output voltage overshoot will not occur when switching on or off.

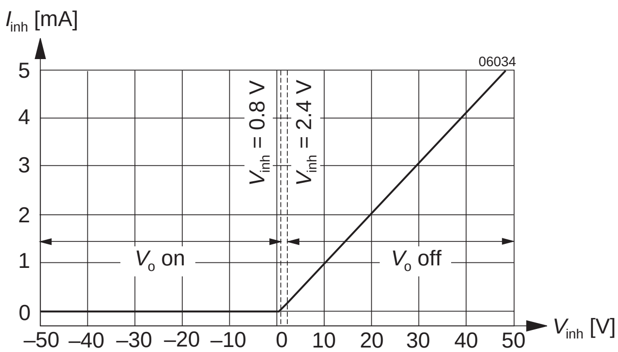


Fig. 6
Typical inhibit current I_{inh} versus inhibit voltage V_{inh}

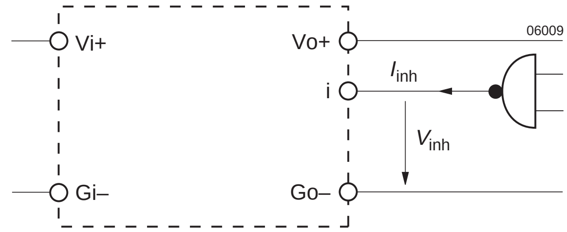


Fig. 7
Definition of I_{inh} and V_{inh}

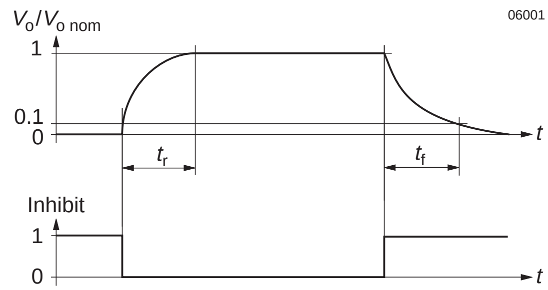


Fig. 8
Output response as a function of inhibit signal

Table 4: Inhibit characteristics

Characteristics			Conditions	min	typ	max	Unit
V_{inh}	Inhibit input voltage to keep regulator output voltage -	V_o = on	$V_{i\,min}$ - $V_{i\,max}$ $T_{C\,min}$ - $T_{C\,max}$	–50		+0.8	VDC
		V_o = off		+2.4		+50	
t_r	Switch-on time after inhibit command		$V_i = V_{i\,nom}$ $R_L = V_{o\,nom} / I_{o\,nom}$		5		ms
t_f	Switch-off time after inhibit command				10		
$I_{i\,inh}$	Input current when inhibited		$V_i = V_{i\,nom}$		10		mA

R Control for Output Voltage Adjustment

Note: With open R input, $V_o \approx V_{o\,nom}$. R excludes option P. (For superseded PSR types $V_o \approx 1.08 \cdot V_{o\,nom}$)

The output voltage V_o can either be adjusted with an external voltage (V_{ext}) or with an external resistor (R_1 or R_2). The adjustment range is 0 - 108% of $V_{o\,nom}$. The minimum differential voltage $\Delta V_{io\,min}$ between input and output (see: *Electrical Input Data*) should be maintained. Undervoltage lockout = Minimum input voltage.

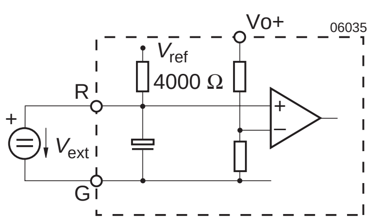


Fig. 9
Voltage adjustment with V_{ext} between R and G (Go-)

a) V_o = 0 - 108% $V_{o\,nom}$, using V_{ext} between R and G (Go–)

$$V_{ext} \approx 2.5\ \text{V} \cdot \frac{V_o}{V_{o\,nom}} \qquad V_o \approx V_{o\,nom} \cdot \frac{V_{ext}}{2.5\ \text{V}}$$

Caution: To prevent damage V_{ext} should not exceed 20 V, nor be negative and R_2 should never be less than 47 kΩ.

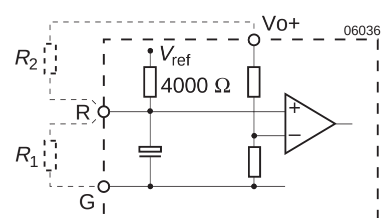


Fig. 10
Voltage adjustment with external resistor R_1 or R_2

b) $V_o = 0$ - 100% $V_{o\,nom}$, using R_1 between R and G (Go-):

$$R_1 \approx \frac{4000\ \Omega \cdot V_o}{V_{o\,nom} - V_o} \qquad V_o \approx \frac{V_{o\,nom} \cdot R_1}{R_1 + 4000\ \Omega}$$

c) $V_o = V_{o\,nom}$ - $V_{o\,max}$, using R_2 between R and Vo+:

$$V_{o\,max} = V_{o\,nom} + 8\%$$

$$R_2 \approx \frac{4000\ \Omega \cdot V_o \cdot (V_{o\,nom} - 2.5\ \text{V})}{2.5\ \text{V} \cdot (V_o - V_{o\,nom})}$$

$$V_o \approx \frac{V_{o\,nom} \cdot 2.5\ \text{V} \cdot R_2}{2.5\ \text{V} \cdot (R_2 + 4000\ \Omega) - V_{o\,nom} \cdot 4000\ \Omega}$$

LED Output Voltage Indicator

A yellow output indicator LED shines when the output voltage is higher than approx. 3 V.

Electromagnetic Compatibility (EMC)

Electromagnetic Immunity

General condition: Case not earthed.

Table 5: Immunity type tests

Phenomenon	Standard [1]	Class Level	Coupling mode [2]	Value applied	Waveform	Source Imped.	Test procedure	In oper.	Per-form. [3]
1 MHz burst disturbance	IEC 60255-22-1	III	i/o, i/c, o/c	2500 V_p	400 damped 1 MHz waves/s	200 Ω	2 s per coupling mode	yes	A [5]
			+i/–i, +o/–o	1000 V_p					
Voltage surge	IEC 60571-1		i/c, +i/–i	800 V_p	100 µs	100 Ω		yes	B
				1500 V_p	50 µs		1 pos. and 1 neg. voltage surge per coupling mode		
				3000 V_p	5 µs				
				4000 V_p	1 µs				
				7000 V_p	100 ns				
Electrostatic discharge	IEC/EN 61000-4-2	3	contact discharge to case	6000 V_p	1/50 ns	330 Ω	10 positive and 10 negative discharges	yes	A [4]
Electromagnetic field	IEC/EN 61000-4-3	2	antenna	3 V/m	AM 80% 1 kHz		80 - 1000 MHz	yes	A
Electrical fast transients/burst	IEC/EN 61000-4-4	3	i/c, +i/–i	2000 V_p	bursts of 5/50 ns 5 kHz rep. rate transients with 15 ms burst duration and a 300 ms period	50 Ω	60 s positive 60 s negative bursts per coupling mode	yes	A [4 5]
		4		4000 V_p					B [4 5]
Surge	IEC/EN 61000-4-5	3	i/c	2000 V_p	1.2/50 µs	12 Ω	5 pos. and 5 neg. surges per coupling mode	yes	B [4 5]
			+i/–i	1000 V_p		2 Ω			
Conducted disturbances	IEC/EN 61000-4-6	3	i, o, signal wires	140 dBµV (10 VAC)	AM 80% 1 kHz	150 Ω	0.15 - 80 MHz	yes	A

[1] For related and previous standards see: *Technical Information: Safety & EMC.* [2] i = input, o = output, c = case.
[3] A = Normal operation, no deviation from specifications, B = Normal operation, temporary deviation from specs possible.
[4] Option L neccessary. [5] With option C, manual reset might be necessary.

Electromagnetic Emission

For emission levels refer to: *Electrical Input Data*.

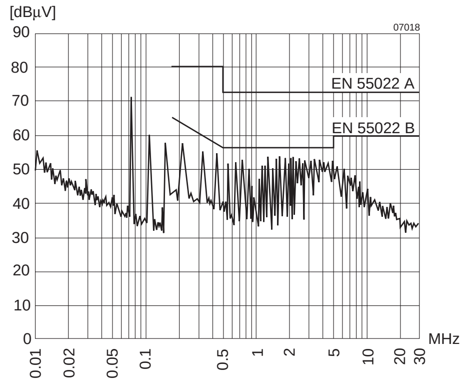


Fig. 11
Typical disturbance voltage (quasi-peak) at the input according to EN 55011/22 measured at $V_{i\ nom}$ and $I_{o\ nom}$.

Immunity to Environmental Conditions

Table 6: Mechanical stress

Test Method		Standard	Test Conditions		Status
Ca	Damp heat steady state	IEC/DIN 60068-2-3 MIL-STD-810D section 507.2	Temperature: Relative humidity: Duration:	$40^{\pm 2}$ °C $93^{+2/-3}$ % 56 days	Unit not operating
Ea	Shock (half-sinusoidal)	IEC/EN/DIN 60068-2-27 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	100 g_n = 981 m/s^2 6 ms 18 (3 each direction)	Unit operating
Eb	Bump (half-sinusoidal)	IEC/EN/DIN 60068-2-29 MIL-STD-810D section 516.3	Acceleration amplitude: Bump duration: Number of bumps:	40 g_n = 392 m/s^2 6 ms 6000 (1000 each direction)	Unit operating
Fc	Vibration (sinusoidal)	IEC/EN/DIN 60068-2-6 MIL-STD-810D section 514.3	Acceleration amplitude: Frequency (1 Oct/min): Test duration:	0.35 mm (10 - 60 Hz) 5 g_n = 49 m/s^2 (60 - 2000 Hz) 10 - 2000 Hz 7.5 h (2.5 h each axis)	Unit operating
Fda	Random vibration wide band Reproducibility high	IEC 60068-2-35 DIN 40046 part 23	Acceleration spectral density: Frequency band: Acceleration magnitude: Test duration:	0.05 g^2/Hz 20 - 500 Hz 4.9 g_{rms} 3 h (1 h each axis)	Unit operating
Kb	Salt mist, cyclic (sodium chloride NaCl solution)	IEC/EN/DIN 60068-2-52	Concentration: Duration: Storage: Storage duration: Number of cycles:	5% (30°C) 2 h per cycle 40°C, 93% rel. humidity 22 h per cycle 3	Unit not operating

Table 7: Temperature specifications, valid for air pressure of 800 - 1200 hPa (800 - 1200 mbar)

Temperature			Standard -7		Option -9		
Characteristics		Conditions	min	max	min	max	Unit
T_A	Ambient temperature	Operational [1]	–25	71	–40	71	°C
T_C	Case temperature		–25	95	–40	95	
T_S	Storage temperature	Non operational	–40	100	–55	100	

[1] See: *Thermal Considerations*

Table 8: MTBF and device hours

MTBF	Ground Benign	Ground Fixed		Ground Mobile	Device Hours [1]
MTBF acc. to MIL-HDBK-217F	T_C = 40°C	T_C = 40°C	T_C = 70°C	T_C = 50°C	
	660 000	143 000 h	81 000 h	68 000 h	2 800 000 h

[1] Statistical values, based on an average of 4300 working hours per year and in general field use

Mechanical Data

Dimensions in mm. Tolerances ±0.3 mm unless otherwise specified.

European Projection

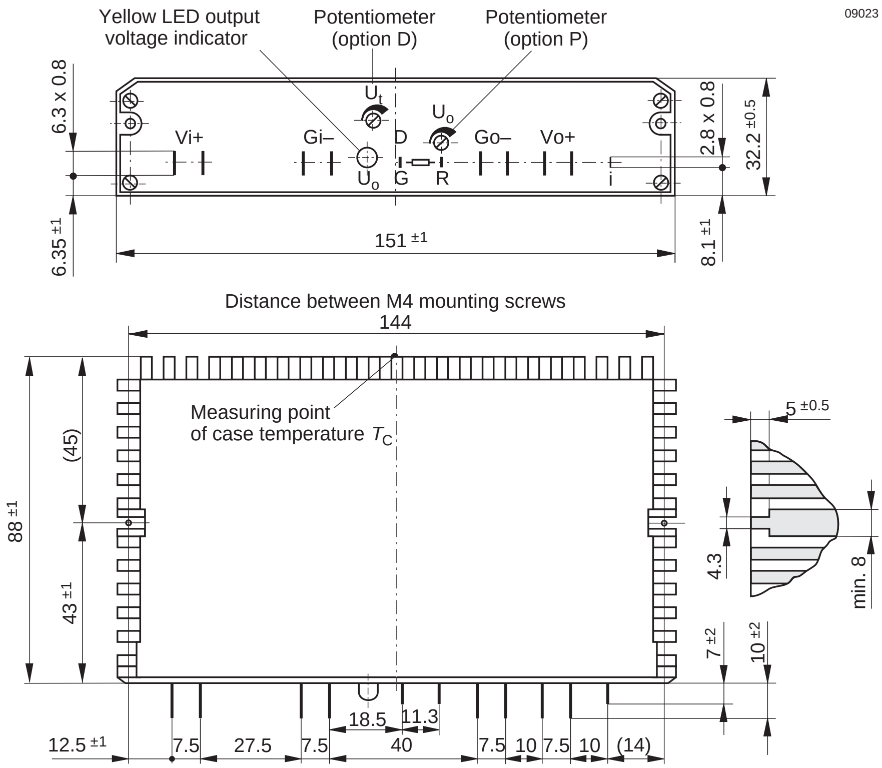


Fig. 12
Case C03, weight 440 g
Aluminium, black finish and self cooling

Safety and Installation Instructions

Installation Instruction

Installation of the switching regulators must strictly follow the national safety regulations in compliance with the enclosure, mounting, creepage, clearance, casualty, markings and segregation requirements of the end-use application.

Check for hazardous voltages before altering any connections. Connections can be made using fast-on or soldering technique.

The input and the output circuit are not separated, i.e. the negative path is internally interconnected!

The units should be connected to a secondary circuit.

Do not open the module.

Ensure that a unit failure (e.g. by an internal short-circuit) does not result in a hazardous condition. See also: *Safety of operator accessible output circuit.*

Cleaning Agents

In order to avoid possible damage, any penetration of cleaning fluids is to be prevented, since the power supplies are not hermetically sealed.

Protection Degree

The protection degree is IP 20.

Standards and Approvals

All switching regulators are UL recognized according to UL 1950, UL 1012 and EN 60950, UL recognized for Canada to CAN/CSA C22.2 No. 234-M90.

The units have been evaluated for:

- Building in,
- Operational insulation from input to output and input/output to case,
- The use in an overvoltage category II environment,
- The use in a pollution degree 2 environment.

The switching regulators are subject to manufacturing surveillance in accordance with the above mentioned UL and CSA and with ISO 9001 standards.

Isolation

Electric strength test voltage between input interconnected with output against case: 750 VDC, 1 s.

This test is performed in the factory as routine test in accordance with EN50116, IEC/EN 60950 and UL 1950 and should not be repeated in the field. Power-One will not honour any guarantee claims resulting from electric strength field tests.

Safety of Operator Accessible Output Circuit

If the output circuit of a switching regulator is operator-accessible, it shall be an SELV circuit according to IEC/EN 60950 related safety standards

The following table shows some possible installation configurations, compliance with which causes the output circuit of the switching regulator to be an SELV circuit according to IEC/EN 60950 up to a nominal output voltage of 30 V, or 48 V if option C is fitted.

However, it is the sole responsibility of the installer or user to assure the compliance with the relevant and applicable safety regulations.

More information is given in: *Technical Information: Safety & EMC*.

Table 9: Insulation concept leading to an SELV output circuit

Conditions	**Front end**			**Switching regulator**	**Result**
Supply voltage	Minimum required grade of isolation, to be provided by the AC-DC front end, including mains supplied battery charger	Maximum DC output voltage from the front end [1]	Minimum required safety status of the front end output circuit	Measures to achieve the specified safety status of the output circuit	Safety status of the switching regulator output circuit
Battery supply, considered as secondary circuit	Double or Reinforced	≤60 V	SELV circuit	None	SELV circuit
		>60 V	Earthed hazardous voltage secondary circuit [2]	Input fuse [3] non accessible case [5]	Earthed SELV circuit
			Unearthed hazardous voltage secondary circuit [5]	Input fuse [3] and unearthed, non accessible case [5]	Unearthed SELV circuit
			Hazardous voltage secondary circuit	Input fuse [3] and earthed output circuit [4] and non accessible case [5]	Earthed SELV circuit
Mains ≤250 V AC	Basic	≤60 V	Earthed SELV circuit [4]	None	
			ELV circuit	Input fuse [3] and earthed output circuit [4] and non accessible case [5]	
		>60 V	Hazardous voltage secondary circuit		
	Double or reinforced	≤60 V	SELV circuit	None	SELV circuit
		>60 V	Double or reinforced insulated unearthed hazardous voltage secondary circuit [5]	Input fuse [3] and unearthed and non accessible case [5]	Unearthed SELV circuit

[1] The front end output voltage should match the specified input voltage range of the switching regulator.

[2] The conductor to the Gi– terminal of the switching regulator has to be connected to earth by the installer according to the relevant safety standard, e.g. IEC/EN 60950.

[3] The installer shall provide an approved fuse (slow blow type with the lowest current rating suitable for the application, max. 12.5 A) in a non-earthed input conductor directly at the input of the switching regulator. If Vo+ is earthed, insert the fuse in the Gi- line. For UL's purpose, the fuse needs to be UL-listed. If option C is fitted, a suitable fuse is already built-in in the Vi+ line.

[4] The earth connection has to be provided by the installer according to the relevant safety standard, e.g. IEC/EN 60950.

[5] Has to be insulated from earth by double or reinforced insulation according to the relevant safety standard, based on the maximum output voltage from the front end.

Description of Options

-9 Extended Temperature Range

The operational ambient temperature range is extended to T_A = –40 to 71°C. (T_C = –40 to 95°C, T_S = –55 to 100°C.)

P Potentiometer

Option P excludes R function. The output voltage V_o can be adjusted with a screwdriver in the range from 0.92 - 1.08 of the nominal output voltage $V_{o\ nom}$.

However, the minimum differential voltage $\Delta V_{i\ o\ min}$ between input and output voltages as specified in: *Electrical Input Data* should be maintained.

L Input Filter

Option L is recommended to reduce superimposed interference voltages and to prevent oscillations, if input lines exceed approx. 5 m in total length. The fundamental wave (approx. 120 kHz) of the reduced interference voltage between Vi+ and Gi– has, with an input line inductance of 5 µH, a maximum magnitude of 4 mVAC. A reduction can be achieved by insertion of a capacitor across the input (e.g. plastic foil between Vi+ and Gi–).

The input impedance of the switching regulator at 120 kHz is about 3.5 Ω. The harmonics are small in comparison with the fundamental wave. See also: *Electrical Input Data: RFI*.

With option L, the maximum permissible additionally superimposed ripple u_i of the input voltage (rectifier mode) at a specified input frequency f_i has the following values:

Units with max input voltage 40 V:
$u_{i\ max}$ = 12 V_{pp} at 100 Hz or V_{pp} = 1200 Hz/f_i • 1V

Units with max input voltage 80 V:
$u_{i\ max}$ = 22 V_{pp} at 100 Hz or V_{pp} = 2200 Hz/f_i • 1V

C Crowbar

This option is recommended to protect the load against power supply malfunction, but it is not designed to sink external currents.

A fixed-value monitoring circuit checks the output voltage V_o. When the trigger voltage $V_{o\ c}$ is reached, the thyristor crowbar triggers and disables the output. It may be deactivated by removal of the input voltage. In case of a switching transistor defect, an internal fuse prevents excessive current.

Note: As a central overvoltage protection device, the crowbar is usually connected to the external load via distributed inductance of the lines. For this reason, the overvoltage at the load can temporarily exceed the trigger voltage $V_{o\ c}$. Depending on the application, further decentralized overvoltage protection elements may have to be used additionally.

Table 10: Crowbar trigger levels

Characteristics		Conditions	5.1 V		12 V		15 V		24 V		36 V		Unit
			min	max	min	max	min	max	min	max	min	max	
$V_{o\ c}$	Trigger voltage	$V_{i\ min}$ - $V_{i\ max}$ I_o = 0 - $I_{o\ nom}$ $T_{C\ min}$ - $T_{C\ max}$	5.8	6.8	13.5	16	16.5	19	27	31	40	45.5	V
t_s	Delay time			1.5		1.5		1.5		1.5		1.5	µs

D Save Data Input Undervoltage Monitor

Note: Output instead of input undervoltage monitor is available on request (Option D1).

Terminal D and Go– are connected to a normally conducting field effect transistor (JFET). The switching characteristics of the option D output are shown in fig. Definition of V_t and V_H. A 0.5 W Zener diode provides protection against overvoltages.

The voltage V_t can be externally adjusted with a trim potentiometer by means of a screwdriver. The hysteresis V_H of V_t is <2%. Terminal D stays low for a minimum time $t_{low\ min}$, in order to prevent any oscillation. V_t can be set to a value between $V_{i\ min}$ and $V_{i\ max}$. Please note that the JFET becomes conductive when V_D increases above 7 V approx.

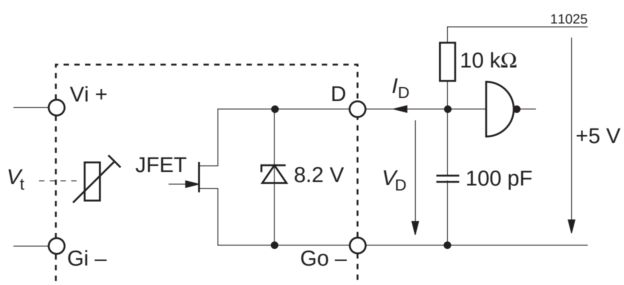


Fig. 13
Test circuit with definition of voltage V_D and current I_D

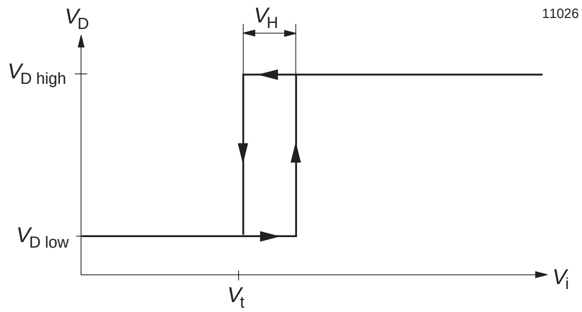


Fig. 14
Definition of V_t and V_H

Data

Table 11: Option D data

Characteristics		Conditions	PSC			Unit
			min	typ	max	
$V_{D\ low}$	Voltage - Terminal D at low impedance	$V_i < V_t$, I_D - 2.5 mA			0.8	V
$V_{D\ high}$	Voltage - Terminal D at high impedance	$V_i > V_t + V_H$, $I_D > 25\ \mu A$	4.75			
$t_{low\ min}$	Minimum duration $V_{D\ low}$			30		ms
$t_{D\ f}$	Response time to $V_{D\ low}$			1		µs
$I_{D\ max}$	Maximum current - Terminal D				20	mA

Application examples:

a) The signal V_D can be utilized in battery powered systems to provide a warning in case of low batteries.

b) In case of power failure, the signal can serve to initiate data save routines.

Accessories

A variety of electrical and mechanical accessories are available including:

– PCB-tags and isolation pads for easy and safe PCB-mounting.

– Ring core chockes for ripple and interference reduction.

For more detailed information please refer to: *Accessories* on the Power-One homepage.

power-one™ Changing the Shape of Power

EC Declaration of Conformity

We

Power-One AG
Ackerstrasse 56 CH-8610 Uster

declare under our sole responsibility that all PSx Series switching regulators carrying the CE-mark are in conformity with the provisions of the Low Voltage Directive (LVD) 73/23/EEC of the European Communities.

Conformity with the directive is presumed by conformity wih the following harmonized standards:

- EN 61204: 1995 (= IEC 61204: 1993, modified)
  Low-voltage power supply devices, d.c. output - Perfomance characteristics and safety requirements

- EN 60950: 1992 + A1: 1993 + A2 (= IEC 950 second edition 1991 + A1: 1992 + A2: 1993)
  Safety of information technology equipment

The installation instructions given in the corresponding data sheet describe correct installation leading to the presumption of conformity of the end product with the LVD. All PSx Series Switching Regulators are components, intended exclusively for inclusion within other equipment by an industrial assembly operation or by professional installers. They must not be operated as stand alone products.

Hence conformity with the Electromagnetic Compatibility Directive 89/336/EEC (EMC Directive) needs not to be declared. Nevertheless, guidance is provided in most product application notes on how conformity of the end product with the indicated EMC standards under the responsibility of the installer can be achieved, from which conformity with the EMC directive can be presumed.

Uster, 14 Oct. 2003

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