General Description

The DS1088L is a low-cost clock generator that produces a square-wave output without external timing components. The fixed-frequency oscillator is available in factory-calibrated frequencies from 312kHz to 133MHz. The DS1088L has a power-down pin for power-sensitive applications.

Applications

Printers

Copiers

Automotive Telematics

Computer Peripherals

POS Terminals

Cable Modems

Features

- **♦** Factory-Programmed, Square-Wave Generator from 312kHz to 133MHz
- **♦** Single Output
- ♦ No External Timing Components Required
- ♦ 2.7V to 3.6V Supply
- ♦ Power-Down Mode
- ♦ 1.0% Frequency Tolerance Over Temperature and Voltage
- ♦ Wide Temperature Range (-20°C to +85°C)

Ordering Information

PART	FREQUENCY (MHz)	TEMP RANGE	PIN-PACKAGE
DS1088LU-02	2.048	-20°C to +85°C	8 µSOP
DS1088LU-10	10.0	-20°C to +85°C	8 µSOP
DS1088LU-16	16.6	-20°C to +85°C	8 µSOP
DS1088LU-66	66.6	-20°C to +85°C	8 µSOP
DS1088LU-100	100.0	-20°C to +85°C	8 µSOP
DS1088LU-yyy	*	-20°C to +85°C	8 μSOP

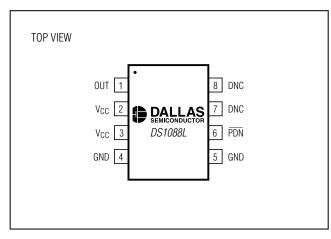
yyy = frequency

Typical Operating Circuit

312kHz TO 133MHz OUTPUT OHT DNC DALLAS SEMICONDUCTOR DS1088L DNC /cc PDN Vcc GND DECOUPLING CAPACITORS $(0.1 \mu F \text{ AND } 0.01 \mu F)$

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Pin Configuration



DALLAS / I X I / I

Maxim Integrated Products 1

For pricing, delivery, and ordering information, please contact Maxim/Dallas Direct! at 1-888-629-4642, or visit Maxim's website at www.maxim-ic.com.

^{*}Email Custom_EconOscillators_Info@dalsemi.com for information/questions concerning custom frequencies.

ABSOLUTE MAXIMUM RATINGS

Voltage Range on V _{CC} Relative to Ground	0.5V to +6.0V	Operating Temperature Range	20°C to +85°C
Voltage Range on PDN Relative		Storage Temperature Range	55°C to +125°C
to Ground*	$-0.5V$ to $(V_{CC} + 0.5V)$	Soldering Temperature	See IPC/JEDEC J-STD-020

^{*}Not to exceed +6.0V.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

RECOMMENDED OPERATING CONDITIONS

 $(T_A = -20^{\circ}C \text{ to } +85^{\circ}C)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP MAX	UNITS
Supply Voltage	Vcc	(Note 1)	2.7	3.6	V
High-Level Input Voltage (PDN)	VIH		0.7 x V _{CC}	$V_{CC} + 0.3$	V
Low-Level Input Voltage (PDN)	V _{IL}		-0.3	0.3 x V _{CC}	V

DC ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 2.7V \text{ to } 3.6V, T_A = -20^{\circ}C \text{ to } +85^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
High-Level Output Voltage (OUT)	VoH	I _{OH} = -4mA, V _{CC} = min	V _C C - 0.4			V
Low-Level Output Voltage (OUT)	V _{OL}	$I_{OL} = 4mA$			0.4	V
High-Level Input Current (PDN)	lιΗ	$V_{CC} = 3.6V$			1	μΑ
Low-Level Input Current (PDN)	IL	$V_{IL} = 0$	-1			μΑ
Supply Current (Active)	Icc	$V_{CC} = 3.6V$, $C_L = 15pF$, $f_0 = 133MHz$		15	24	mA
Standby Current (Power-Down)	Icca	Power-down mode			10	μΑ



OSCILLATOR CHARACTERISTICS

 $(V_{CC} = 2.7V \text{ to } 3.6V, T_A = -20^{\circ}C \text{ to } +85^{\circ}C.)$

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP	MAX	UNITS
Output Frequency Range Available	f ₀			0.3124		133.3	MHz
Output Frequency Tolerance	$\frac{\Delta f_0}{f_0}$	V _{CC} = 3.3V, T _A = +25°C (Note 8)		-0.3		+0.3	%
Voltage Frequency Variation	$\frac{\Delta f_V}{f_0}$	Over voltage range, T _A = +25°C (Note 2)		-0.35		+0.35	%
Tarana aratura Francisco Variation	Δ f $_{ op}$	Over temperature range,	-20°C to +25°C	-0.7		+0.7	0/
Temperature Frequency Variation	$\frac{\Delta f_T}{f_0}$	V _{CC} = 3.3V (Notes 3, 4) +25°C to +85°C	-0.5		+0.5	%	
Frequency Variation Over Voltage and Temperature	$\frac{\Delta f_{V,T}}{f_0}$	Over voltage and temperature range		-1.0		+1.0	%

AC ELECTRICAL CHARACTERISTICS

 $(V_{CC} = 2.7V \text{ to } 3.6V, T_A = -20^{\circ}C \text{ to } +85^{\circ}C.)$

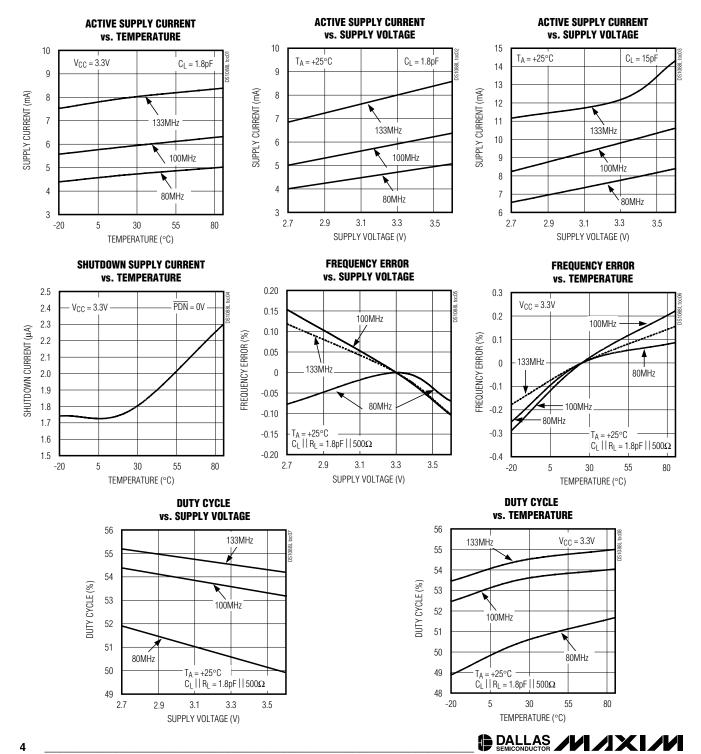
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Power-Up Time	tpor + tstab	(Note 5)			100	μs
OUT Disabled After Entering Power-Down Mode	tpDN	(Note 6)			7	μs
Load Capacitance	CL	(Note 7)		15	50	pF
Output Duty Cycle (OUT)		$f_0 < 80MHz, f_0 \ge 80MHz$	40		60	%
Output Duty Cycle (OOT)		f ₀ < 80MHz		50		70

- Note 1: All voltages are referenced to ground.
- **Note 2:** This is the change in output frequency due to changes in voltage at $T_A = +25$ °C.
- Note 3: This is the change in output frequency due to changes in temperature from the +25°C frequency at VCC = 3.3V.
- Note 4: Guaranteed by design.
- **Note 5:** This indicates the time elapsed between power-up and the output becoming active. An on-chip delay is intentionally introduced to allow the oscillator to stabilize. t_{STAB} is equivalent to approximately 512 clock cycles and will depend on the programmed oscillator frequency.
- Note 6: Output disabled in 2 cycles or less of the output frequency.
- Note 7: Output voltage swings may be impaired at high frequencies combined with high-output loading.
- Note 8: Typical frequency shift due to aging is within ±0.2%. Aging stressing includes level 1 moisture reflow preconditioning (24hr +125°C bake, 168hr +85°C/85%RH moisture soak, and three solder reflow passes +240°C +0°C/-5°C peak) followed by 1000hr (max) Vcc biased +125°C OP/L, 1000hr unbiased +150°C bake, 1000 temperature cycles at -55°C to +125°C and 168hr +121°C/2 ATM steam/unbiased autoclave.



Typical Operating Characteristics

($V_{CC} = 3.3V$, $T_A = +25$ °C, unless otherwise noted.)



Pin Description

PIN	NAME	FUNCTION
1	OUT	Oscillator Output
2	Vcc	Power Supply
3	Vcc	Power Supply
4	GND	Ground
5	GND	Ground
6	PDN	Power-Down. When the pin is high, the oscillator is enabled. When the pin is low, the oscillator is disabled (power-down mode).
7	DNC	Do Not Connect. The DNC pins are internally connected to ground.
8	DNC	Do Not Connect

Detailed Description

The DS1088L is a low-cost clock generator that produces a square-wave output without external timing components. The fixed-frequency oscillator is available in factory-calibrated frequencies from 312kHz to 133MHz. The DS1088L has a power-down pin for power-sensitive applications. A block diagram of the DS1088L is shown in Figure 1.

Output Frequency

The internal oscillator frequency is divided by the factory-programmed prescaler to produce an output frequency of 312kHz to 133MHz. See the *Standard Frequency Options* table for a list of standard frequencies. Contact factory for custom frequencies.

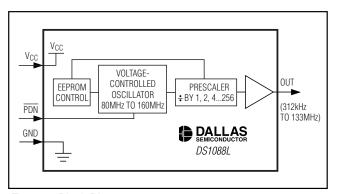


Figure 1. Block Diagram

Power-Down Mode

The PDN pin disables the internal oscillator and the oscillator output for power-sensitive applications. The power-down pin must remain low for at least two output frequency cycles plus 10µs for deglitching purposes. On power-up, the output is disabled until power is stable and the voltage-controlled oscillator has generated 512 clock cycles.

Applications Information

Power-Supply Decoupling

To achieve the best results when using the DS1088L, the power supply must be decoupled with 0.01µF and 0.1µF high-quality, ceramic, surface-mount capacitors. Surface-mount components minimize lead inductance, which improves performance, and tend to have adequate high-frequency response for decoupling applications. These capacitors should be placed as close to the VCC and GND pins as possible.

Chip Topology

TRANSISTOR COUNT: 4507 SUBSTRATE CONNECTED TO GROUND.

Package Information

For the latest package outline information, go to www.maxim-ic.com/DallasPackInfo.

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