

10V Drive Nch MOSFET

R6015FNX

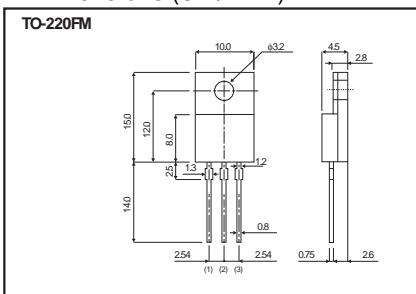
● Structure

Silicon N-channel MOSFET

● Features

- 1) Fast reverse recovery time (t_{rr})
- 2) Low on-resistance.
- 3) Fast switching speed.
- 4) Gate-source voltage
 V_{GSS} guaranteed to be $\pm 30V$.
- 5) Drive circuits can be simple.
- 6) Parallel use is easy.

● Dimensions (Unit : mm)



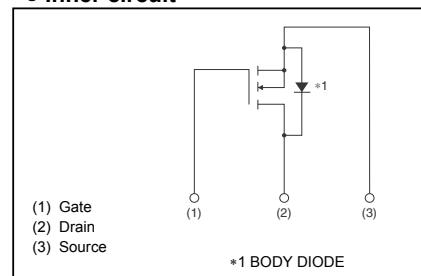
● Application

Switching

● Packaging specifications

Type	Package	Bulk
	Code	-
	Basic ordering unit (pieces)	500
R6015FNX	O	

● Inner circuit



● Absolute maximum ratings (Ta = 25°C)

Parameter	Symbol	Limits	Unit
Drain-source voltage	V_{DSS}	600	V
Gate-source voltage	V_{GSS}	± 30	V
Drain current	Continuous	I_D *3	A
	Pulsed	I_{DP} *1	A
Source current (Body Diode)	Continuous	I_S *3	A
	Pulsed	I_{SP} *1	A
Avalanche Current	I_{AS} *2	7.5	A
Avalanche Energy	E_{AS} *2	15	mJ
Power dissipation (Tc=25°C)	P_D	50	W
Channel temperature	Tch	150	°C
Range of storage temperature	Tstg	-55 to +150	°C

*1 Pw≤10μs, Duty cycle≤1%

*2 L=500μH, $V_{DD}=50V$, $R_g=25\Omega$, starting Tch=25°C

*3 Limited only by maximum temperature allowed.

● Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to Case	R_{th} (ch-c)	2.5	°C / W

● Electrical characteristics (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	-	-	±100	nA	V _{GS} =±30V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR)DSS}	600	-	-	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	-	-	100	µA	V _{DS} =600V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	3	-	5	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS(on)*}	-	0.27	0.35	Ω	I _D =7.5A, V _{GS} =10V
Forward transfer admittance	Y _{fs} *	4.5	-	-	S	I _D =7.5A, V _{DS} =10V
Input capacitance	C _{iss}	-	1660	-	pF	V _{DS} =25V
Output capacitance	C _{oss}	-	1110	-	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	-	45	-	pF	f=1MHz
Turn-on delay time	t _{d(on)*}	-	38	-	ns	I _D =7.5A, V _{DD} ≈300V
Rise time	t _r *	-	45	-	ns	V _{GS} =10V
Turn-off delay time	t _{d(off)*}	-	120	-	ns	R _L =40Ω
Fall time	t _f *	-	35	-	ns	R _G =10Ω
Total gate charge	Q _g *	-	42	-	nC	I _D =15A,
Gate-source charge	Q _{gs} *	-	12	-	nC	V _{DD} ≈300V
Gate-drain charge	Q _{gd} *	-	20	-	nC	V _{GS} =10V

*Pulsed

● Body diode characteristics (Source-Drain) (Ta = 25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward Voltage	V _{SD} *	-	-	1.5	V	I _s =15A, V _{GS} =0V
Reverse Recovery Time	t _{rr} *	60	90	120	ns	I _s =15A, di/dt=100A/µs

*Pulsed

●Electrical characteristic curves ($T_a=25^\circ\text{C}$)

Fig.1 Typical Output Characteristics (I)

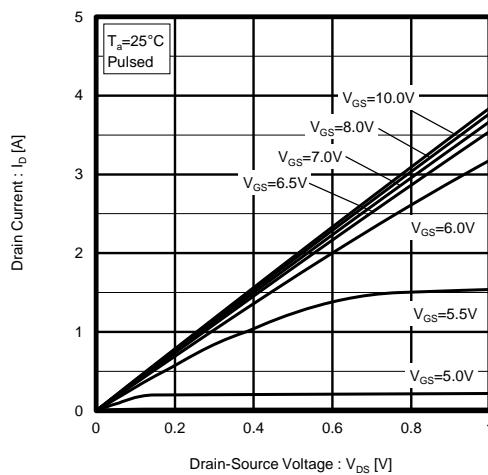


Fig.2 Typical Output Characteristics (II)

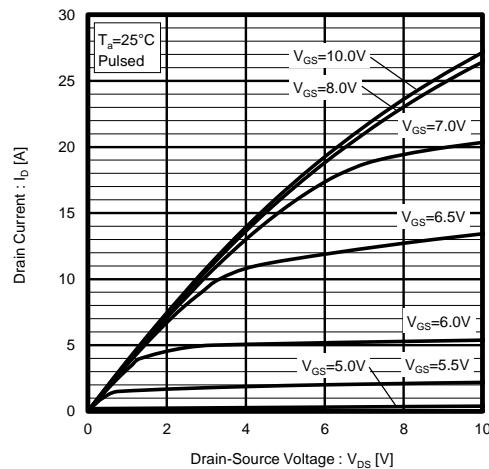


Fig.3 Typical Transfer Characteristics

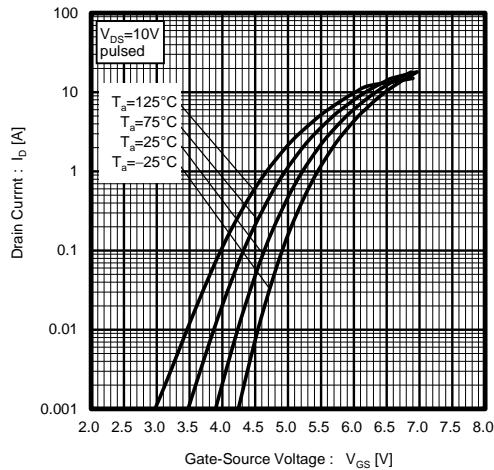


Fig.4 Gate Threshold Voltage vs. Channel Temperature

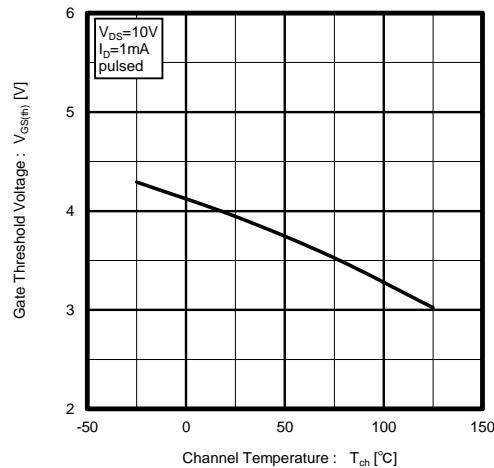


Fig.5 Static Drain-Source On-State Resistance vs. Drain Current

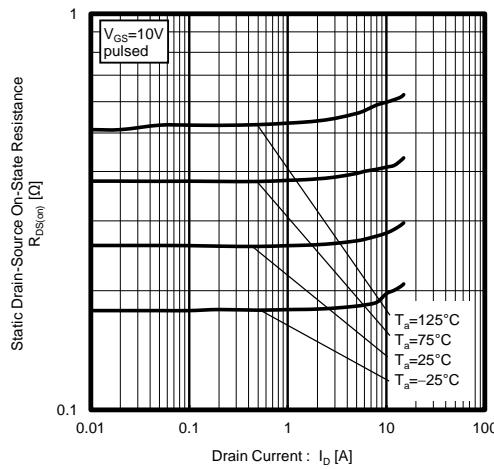


Fig.6 Static Drain-Source On-State Resistance vs. Channel Temperature

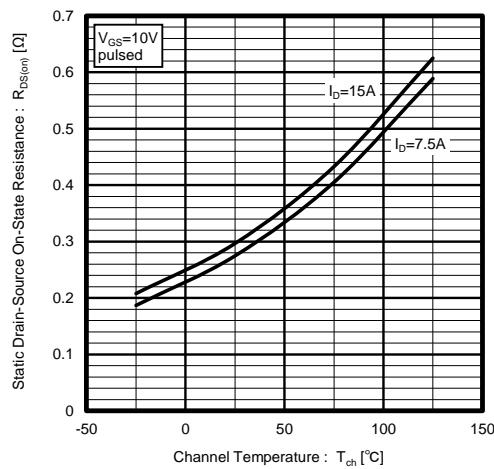


Fig.7 Forward Transfer Admittance vs. Drain Current

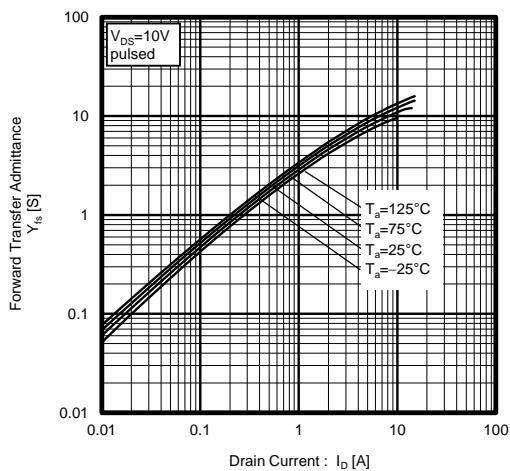


Fig.8 Source Current vs. Source-Drain Voltage

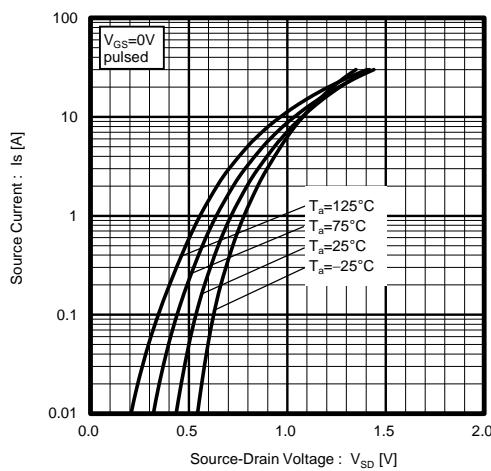


Fig.9 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

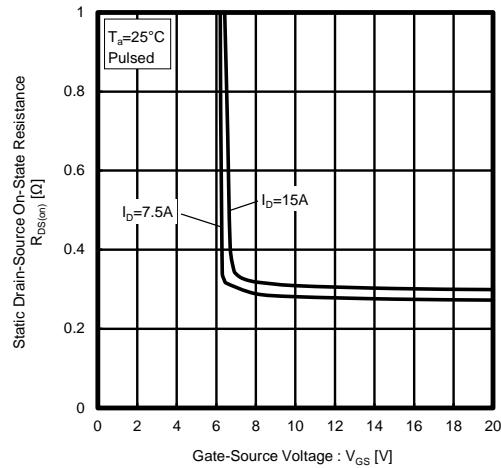


Fig.10 Switching Characteristics

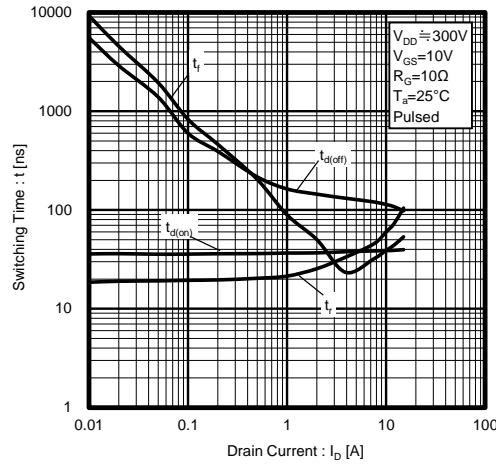


Fig.11 Dynamic Input Characteristics

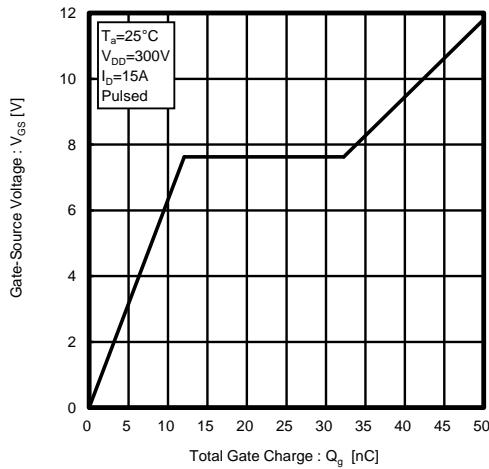


Fig.12 Typical Capacitance vs. Drain-Source Voltage

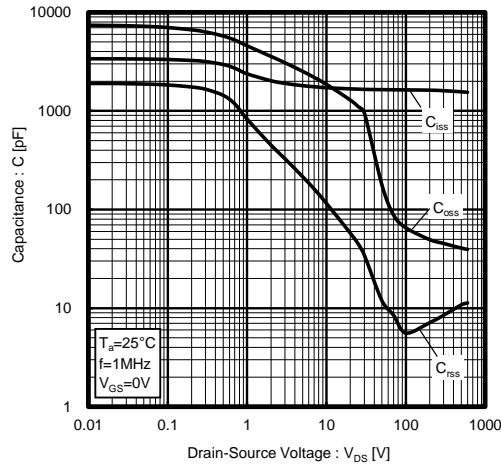


Fig.13 Reverse Recovery Time vs. Source Current

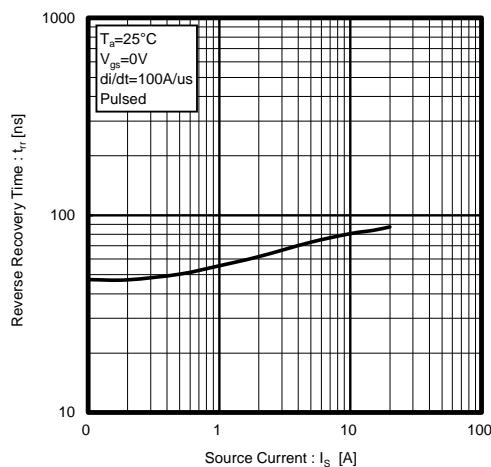


Fig.14 Maximum Safe Operating Area

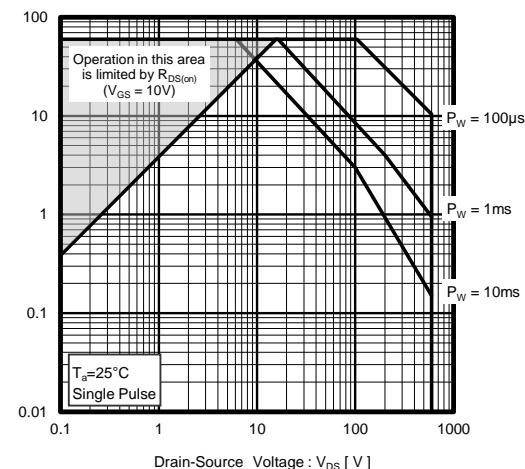
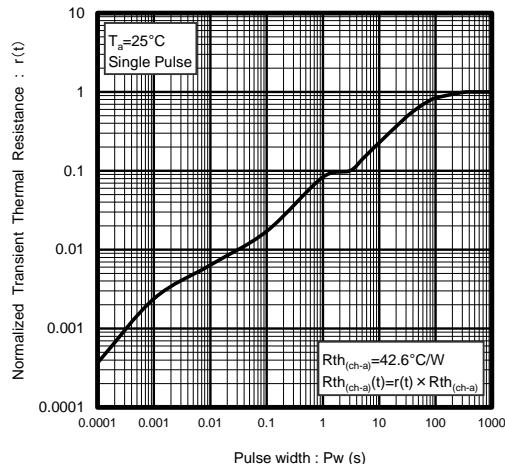


Fig.15 Normalized Transient Thermal Resistance v.s. Pulse Width



● Measurement circuits

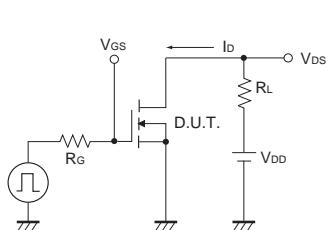


Fig.1-1 Switching Time Measurement Circuit

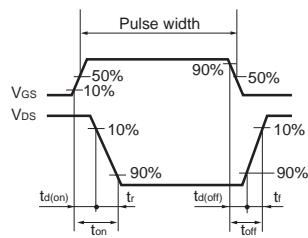


Fig.1-2 Switching Waveforms

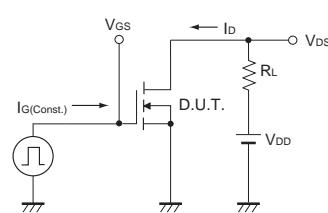


Fig.2-1 Gate Charge Measurement Circuit

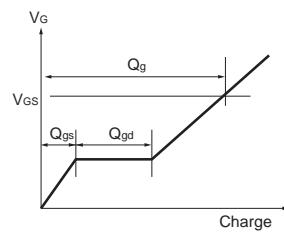


Fig.2-2 Gate Charge Waveform

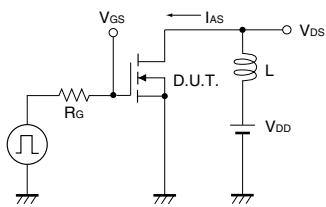


Fig.3-1 Avalanche Measurement Circuit

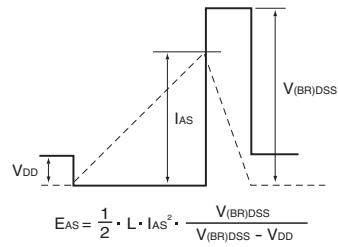


Fig.3-2 Avalanche Waveform

Notes

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