

Silicon Carbide Schottky Diode

FEATURES:

Applications:

- Revolutionary semiconductor material -Silicon Carbide
- SMPS, PFC, snubber
- Switching behavior benchmark
- No reverse recovery
- No temperature influence on the switching behavior
- No forward recovery



Chip Type	V_{BR}	I _F	Die Size	Package	Ordering Code
SIDC24D60SIC3	600V	8A	1.706 x 1.38 mm ²	sawn on foil	Q67050-A4281- A101

MECHANICAL PARAMETER:

Raster size	1.706x 1.38	mm			
Anode pad size	1.405 x 1.08				
Area total / active	2.354 / 1.548	mm ²			
Thickness	355	μm			
Wafer size	75	mm			
Flat position	0	deg			
Max. possible chips per wafer	1649 pcs				
Passivation frontside	Photoimide				
Anode metalization	3200 nm Al				
Cathode metalization	1400 nm Ni Ag –system suitable for epoxy and soft solder die bonding				
Die bond	Electrically conductive glue or solder				
Wire bond	AI, ≤ 350μm				
Reject Ink Dot Size	Ø ≥ 0.3 mm				
Recommended Storage Environment	store in original container, in dry nitrogen, < 6 month at an ambient temperature of 23°C				

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Maximum Ratings

Parameter	Symbol	Condition	Value	Unit
Repetitive peak reverse voltage	V_{RRM}		600	V
Surge peak reverse voltage	V _{RSM}		600	7
Continuous forward current limited by	I _F		8	A
T _{jmax}	'F			
Single pulse forward current	I _{FSM}	T_C =25°C, t_P =10 ms sinusoidal	26	
(depending on wire bond configuration)	-1 OW	. C 20 0, 17 10 1110 0111001001		
Maximum repetitive forward current	I _{FRM}	$T_C = 100^{\circ}C, T_j = 150^{\circ}C,$	32	
limited by T _{jmax}	I FRM	D=0.1	32	
Non repetitive peak forward current	I _{FMAX}	$T_C = 25^{\circ}C$, $tp = 10\mu$ s	80	
Operating junction and storage temperature	$T_{\rm j}$, $T_{ m stg}$		-55+175	°C

Static Electrical Characteristics (tested on chip), T_j =25 °C, unless otherwise specified

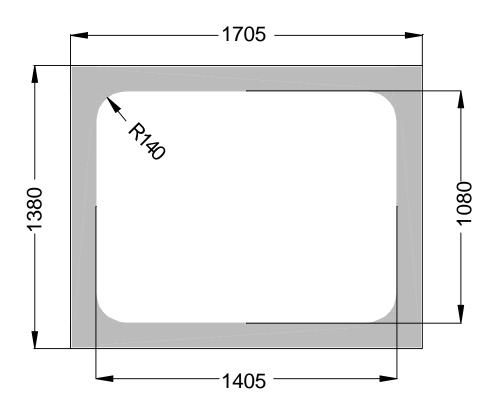
Parameter	Symbol	Condi	itions		Value		Unit	
i arameter	Cyllibol	Conditions		min.	Тур.	max.]	
Reverse leakage current	I _R	V _R =600V	<i>T_j</i> =25° <i>C</i>		28	300	μΑ	
Forward voltage drop	V _F	I _F =8A	T _j =25°C		1.5	1.7	V	

Dynamic Electrical Characteristics, at $T_j = 25$ °C, unless otherwise specified, tested at component

Parameter	Symbol	Cond	Conditions		Value		
raiailletei	Symbol	Conditions		min.	Тур.	max.	Unit
Total capacitive charge	Q_C	I_F =8A di/dt=400A/ms V_R =200V	$T_j = 150 ^{\circ}\mathrm{C}$		24		nC
Switching time	t _{rr}	I _F =8A di/dt=400A/ m s V _R = 200V	$T_j = 150 ^{\circ}\text{C}$		n.a.		ns
Total capacitance	С	$I_F=8A$ di/dt=400A/ms	V _R = 1 V		280		
		$T_j=25$ °C f=1MHz	V _R =150V		26		pF
			V _R =300V		18		



CHIP DRAWING:





FURTHER ELECTRICAL CHARACTERISTICS:

This chip data sheet refers to the INFINEON TECHNOLOGIES SDT08S60 device data sheet

Description:

AQL 0,65 for visual inspection according to failure catalog

Electrostatic Discharge Sensitive Device according to MIL-STD 883

Test-Normen Villach/Prüffeld

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