

# SGM4895 1.1W Fully Differential Audio Power Amplifier

## **GENERAL DESCRIPTION**

The SGM4895 is a fully differential audio power amplifier that is designed for portable communication device applications and demanding applications in mobile phones. It is capable of delivering 1.1W of continuous average power to an  $8\Omega$  BTL load with less than 1% distortion (THD+N) from a 5V battery voltage. It operates from 2.5V to 5.5V power supply.

The SGM4895 features a low-power consumption shutdown mode. To facilitate this, Shutdown may be enabled by logic low. Additionally, the SGM4895 features an internal thermal shutdown protection mechanism.

The SGM4895 contains advanced pop & click circuitry, a minimal count of external components and low-power shutdown mode. All these features make SGM4895 ideal for wireless handsets and other low voltage applications where minimal power consumption is a primary requirement.

The SGM4895 is available in Pb-free CSP-9, DFN-8 (3mm × 3mm), MSOP-8 packages. It operates over an ambient temperature range of  $-40^{\circ}$ C to  $+85^{\circ}$ C.

### **FEATURES**

- Fully Differential Amplifier
- Excellent PSRR: Direct Connection to the Battery
- 1.1W to 8Ω BTL Load from 5V Supply at THD+N < 1% (TYP)</li>
- 2.5V to 5.5V Operation
- Low Shutdown Current
- Shutdown Pin is Compatible with 1.8V Logic
- Improved Pop & Click Circuitry
- Thermal Overload Protection Circuitry
- No Output Coupling Capacitors, Bootstrap Capacitors Required
- External Gain Configuration Capability
- -40°C to +85°C Operating Temperature Range
- Pb-Free CSP-9, DFN-8, MSOP-8 Packages

## **APPLICATIONS**

Portable Systems Wireless Handsets Mobile Phone Handheld Computers PDAs GPS



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## 1.1W Fully Differential Audio Power Amplifier

### **PACKAGE/ORDERING INFORMATION**

MODEL	ORDER NUMBER	PACKAGE DESCRIPTION	PACKAGE OPTION	MARKING INFORMATION	
SGM4895	SGM4895YG/TR	CSP-9	Tape and Reel, 3000	4895YG	
	SGM4895YDB8/TR	DFN-8 (3.0mm×3.0mm)	Tape and Reel, 3000	SGM4895DB	
	SGM4895YMS8/TR	MSOP-8	Tape and Reel, 3000	SGM4895YMS8	

## ABSOLUTE MAXIMUM RATINGS

Supply Voltage	6V					
Input Voltage	–0.3V to (V <sub>+</sub> ) + 0.3V					
Storage Temperature Range	–65°C to +150°C					
Junction Temperature	150°C					
Operating Temperature Range	–40°C to +85°C					
Lead Temperature Range (Soldering 10 sec)						

#### NOTES

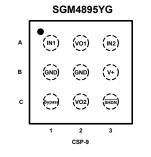
1. Stresses above those listed under Absolute Maximum Ratings may cause permanent damage to the device. This is a stress rating only; functional operation of the device at these or any other conditions above those indicated in the operational section of this specification is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

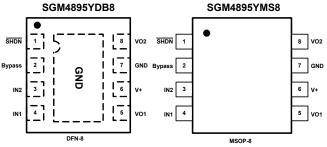
### CAUTION

This integrated circuit can be damaged by ESD if you don't pay attention to ESD protection. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage.

ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because very small parametric changes could cause the device not to meet its published specifications.

## PIN CONFIGURATIONS (Top View)

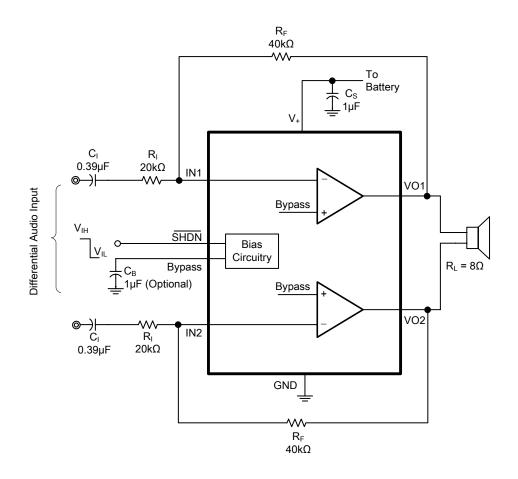






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## **TYPICAL APPLICATION**





## **1.1W Fully Differential Audio Power Amplifier**

## **ELECTRICAL CHARACTERISTICS**

(The following AC specifications apply for  $8\Omega$  load,  $A_V = 1V/V$ ,  $T_A = 25^{\circ}C$ , unless otherwise specified.)

PARAMETER	SYMBOL	CONDITIONS		SGM4895			
PARAMETER				MIN	TYP	MAX	UNITS
Supply Voltage	V+			2.5		5.5	V
	I <sub>SD</sub>	$V_{IN} = 0V, V_{SHDN} = GND, V_{+} = 5.0V$			0.05	1	
Shutdown Current		V <sub>IN</sub> = 0V, V <sub>SHDN</sub> = GND, V <sub>+</sub> = 3.3V			0.04	1	μA
		V <sub>IN</sub> = 0V, V <sub>SHDN</sub> = GND, V <sub>+</sub> = 2.6V			0.04		
	V <sub>OS</sub>	V <sub>IN</sub> = 0V, V <sub>SHDN</sub> = V <sub>+</sub> = 5.0V		-10	1.00	10	mV
Output Offset Voltage		V <sub>IN</sub> = 0V, V <sub>SHDN</sub> = V <sub>+</sub> = 3.3V		-10	1.00	10	
		V <sub>IN</sub> = 0V, V <sub>SHDN</sub> = V <sub>+</sub> = 2.6V			1.00		
	ΙQ	$V_{IN} = 0V, I_O = 0A,$ $V_{SHDN} = V_+$	V <sub>+</sub> = 5.0V, No Load		4.90	6	mA
			V <sub>+</sub> = 5.0V, 8 Ω Load		5.20	8	
Outersont Dever Supply Current			V <sub>+</sub> = 3.3V, No Load		4.30	5.5	
Quiescent Power Supply Current			V <sub>+</sub> = 3.3V, 8 Ω Load		4.70	7.5	
			V <sub>+</sub> = 2.6V, No Load		4.20		
			V <sub>+</sub> = 2.6V, 8 Ω Load		4.30		
Shutdown Voltage Input High	V <sub>SDIH</sub>			1.2			v
Shutdown Voltage Input Low	V <sub>SDIL</sub>					0.4	v
	Po	f = 1kHz, THD+N < 1%	V <sub>+</sub> = 5.0V		1.10		w
Output Dowor			V <sub>+</sub> = 3.6V		0.60		
Output Power			V <sub>+</sub> = 3.0V		0.40		
			V <sub>+</sub> = 2.6V		0.30		
Total Harmonic Distortion + Noise	THD+N	P <sub>o</sub> = 0.6Wrms, f = 1kHz, V <sub>+</sub> = 5.0V			0.01		%
	PSRR	f = 217Hz (Note 1)	V <sub>+</sub> = 5.0V		-80		dB
			V+ = 3.6V		-88		
			V+ = 3.0V		-96		
Power Supply Poinction Patio			V <sub>+</sub> = 2.6V		-92		
Power Supply Rejection Ratio		f = 1kHz (Note 1)	V <sub>+</sub> = 5.0V		-74		
			V+ = 3.6V		-80		
			V <sub>+</sub> = 3.0V		-82		
			V <sub>+</sub> = 2.6V		-80		
Common Mode Rejection Ratio	CMRR	f = 217Hz, V <sub>CM</sub> = 200mV <sub>PP</sub> , V <sub>+</sub> = 5.0V (Note 2)			-70		dB
	T <sub>wu</sub>		V <sub>+</sub> = 5.0V		115		ms
Wake–Up Time			V+ = 3.6V		100		
wake-op line			V+ = 3.0V		100		
		С <sub>в</sub> = 1µF	V+ = 2.6V		90		
	T <sub>SDT</sub>		V <sub>+</sub> = 5.0V		15.0		μs
Shutdown Time			V+ = 3.6V		16.8		
			V+ = 3.0V		18.8		
			V <sub>+</sub> = 2.6V		18.8		

Specifications subject to changes without notice.

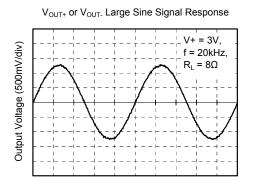
**Note 1:**  $10\Omega$  terminated input.

Note 2: CMRR is affected by the matching between external gain-setting resistor ratios.

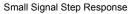


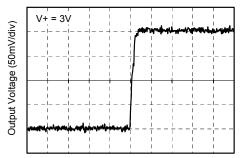
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## **TYPICAL PERFORMANCE CHARACTERISTICS**

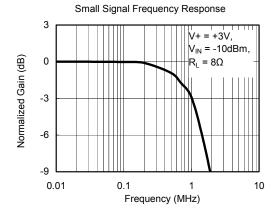


### Time (10µs/div)



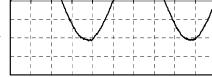


Time (2µs/div)



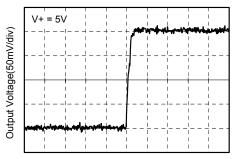
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 $V_{\text{OUT+}}$  or  $V_{\text{OUT-}}$  Large Sine Signal Response V+ = 5V, f = 20kHz, Output Voltage (1V/div)  $R_1 = 8\Omega$ 

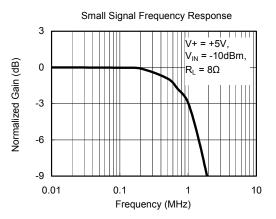


#### Time (10µs/div)

Small Signal Step Response



#### Time(2µs/div)



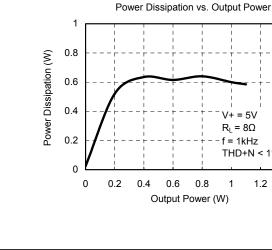
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#### THD+N vs. Output Power THD+N vs. Output Power 10 10 V+ = +5V. V + = +3V, R<sub>L</sub> = 8Ω, R<sub>L</sub> = 8Ω, \_f = 1kHz, 1 f = 1kHz, 1 A<sub>∨</sub> = +1, A<sub>V</sub> = +1, THD+N (%) THD+N (%) -BW < 80kHz BW < 80kHz 0.1 0.1 0.01 0.01 0.001 0.001 0.01 0.1 0.01 0.1 1 Output Power (W) Output Power (W) THD+N vs.Frequency 1 1 0.1 0.1 (%) N+OHL 0.01 THD+N (%) 0.01 ₩V+ = +3V, \_\_\_\_R\_ = 8Ω, P<sub>o</sub> = 300mW 1 1 1 1 1 0.001 0.001 0.01 0.1 0.01 0.1 10 100 1 Frequency (kHz) Power Dissipation vs. Output Power 1 0.5 0.8 0.4 Power Dissipation (W) Power Dissipation (W) 0.6 0.3 0.4 0.2 V+ = 3V $R_L = 8\Omega$ f = 1kHz 0.2 0.1 THD+N < 1% 0 0 0 0.2 0.4 0.6 0.8 1 0 0.2 0.4 Output Power (W)

## **TYPICAL PERFORMANCE CHARACTERISTICS**

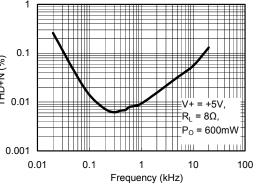


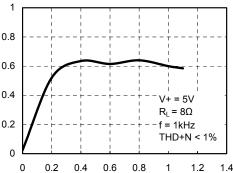
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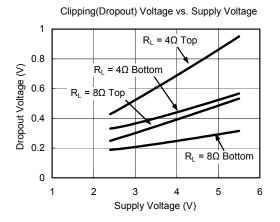


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## **TYPICAL PERFORMANCE CHARACTERISTICS**

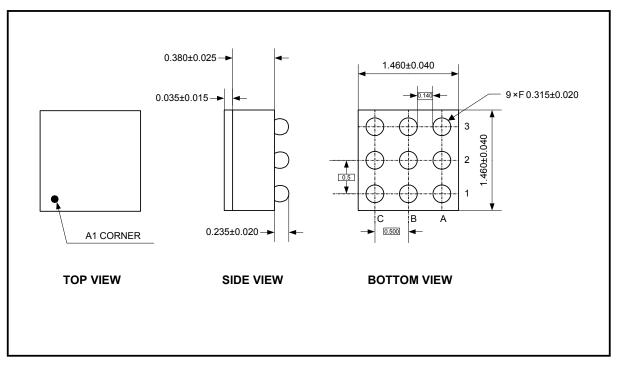




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## PACKAGE OUTLINE DIMENSIONS

## CSP-9

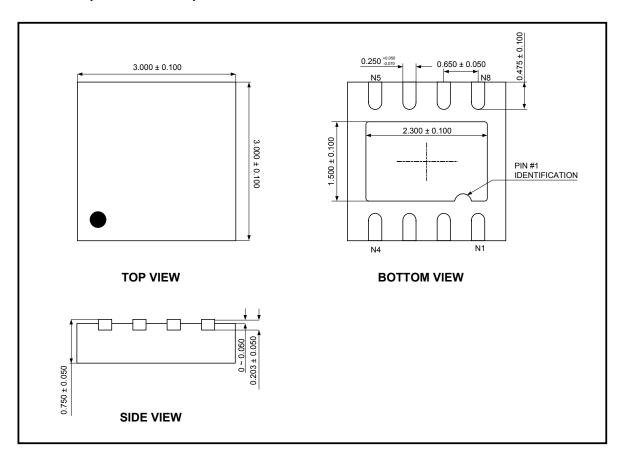


Note: All linear dimensions are in millimeters.



## PACKAGE OUTLINE DIMENSIONS

## **DFN-8 (3mm × 3mm)**



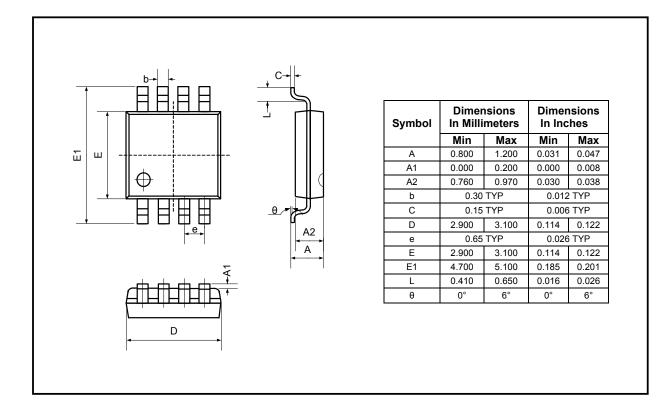
Note: All linear dimensions are in millimeters.



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## PACKAGE OUTLINE DIMENSIONS

### **MSOP-8**



05/2008 REV. A

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