

2.5V Drive Nch MOS FET

RTQ045N03

●Structure

Silicon N-channel
MOS FET

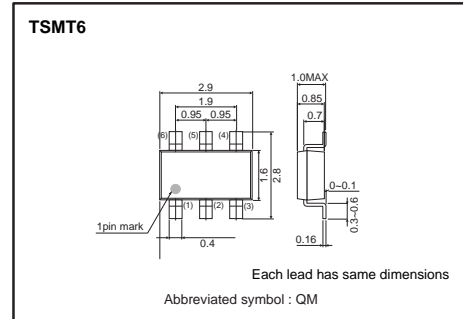
●Features

- 1) Low on-resistance.
- 2) Built-in G-S Protection Diode.
- 3) Small Surface Mount Package (TSMT6) .

●Application

Power switching, DC / DC converter.

●External dimensions (Unit : mm)



●Packaging specifications

Type	Package	Taping
	Code	TR
	Basic ordering unit (pieces)	3000
RTQ045N03		○

●Absolute maximum ratings (Ta=25°C)

Parameter	Symbol	Limits	Unit	
Drain-source voltage	V_{DS}	30	V	
Gate-source voltage	V_{GS}	12	V	
Drain current	Continuous	I_D	±4.5	A
	Pulsed	I_{DP} *1	±18	A
Source current (Body diode)	Continuous	I_S	1.0	A
	Pulsed	I_{SP} *1	4.0	A
Total power dissipation	P_D *2	1.25	W	
Channel temperature	T_{ch}	150	°C	
Storage temperature	T_{stg}	-55~+150	°C	

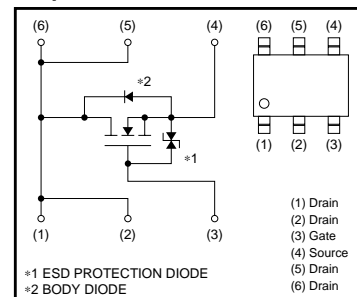
*1 $P_w \leq 10\mu s$, Duty cycle $\leq 1\%$
*2 Mounted on a ceramic board.

●Thermal resistance

Parameter	Symbol	Limits	Unit
Channel to ambient	$R_{th}(ch-a)$ *	100	°C / W

* Mounted on a ceramic board.

●Equivalent circuit



*A protection diode is included between the gate and the source terminals to protect the diode against static electricity when the product is in use. Use the protection circuit when the fixed voltages are exceeded.

Transistors

●Electrical characteristics (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Gate-source leakage	I _{GSS}	–	–	±10	μA	V _{GS} =±12V, V _{DS} =0V
Drain-source breakdown voltage	V _{(BR) DSS}	30	–	–	V	I _D =1mA, V _{GS} =0V
Zero gate voltage drain current	I _{DSS}	–	–	1	μA	V _{DS} =30V, V _{GS} =0V
Gate threshold voltage	V _{GS(th)}	0.5	–	1.5	V	V _{DS} =10V, I _D =1mA
Static drain-source on-state resistance	R _{DS(on)} *	–	30	43	mΩ	I _D =4.5A, V _{GS} =4.5V
		–	32	45		I _D =4.5A, V _{GS} =4V
		–	42	60		I _D =4.5A, V _{GS} =2.5V
Forward transfer admittance	Y _{fs} *	4.5	–	–	S	I _D =4.5A, V _{DS} =10V
Input capacitance	C _{iss}	–	540	–	pF	V _{DS} =10V
Output capacitance	C _{oss}	–	150	–	pF	V _{GS} =0V
Reverse transfer capacitance	C _{rss}	–	100	–	pF	f=1MHz
Turn-on delay time	t _{d(on)} *	–	13	–	ns	I _D =2.25A, V _{DD} ≐15V
Rise time	t _r *	–	31	–	ns	V _{GS} =4.5V
Turn-off delay time	t _{d(off)} *	–	45	–	ns	R _L =6.67Ω
Fall time	t _f *	–	30	–	ns	R _G =10Ω
Total gate charge	Q _g *	–	7.6	10.7	nC	V _{DD} ≐15V
Gate-source charge	Q _{gs} *	–	1.2	–	nC	V _{GS} =4.5V
Gate-drain charge	Q _{gd} *	–	2.7	–	nC	I _D =4.5A

*Pulsed

●Body diode characteristics (Source-drain) (Ta=25°C)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Conditions
Forward voltage	V _{SD} *	–	–	1.2	V	I _S =4A, V _{GS} =0V

*Pulsed

Transistors

●Electrical characteristic curves

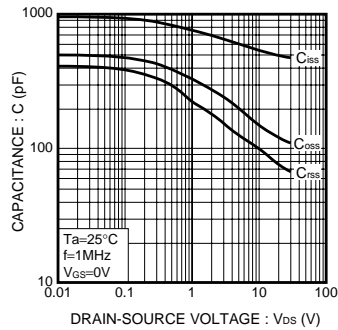


Fig.1 Typical Capacitance vs. Drain-Source Voltage

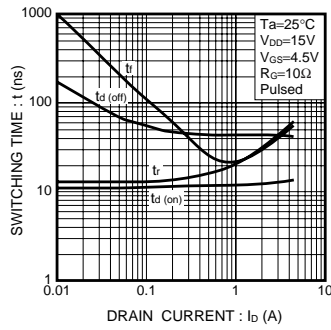


Fig.2 Switching Characteristics

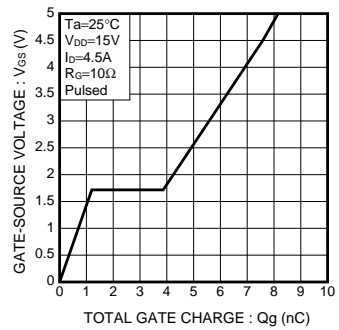


Fig.3 Dynamic Input Characteristics

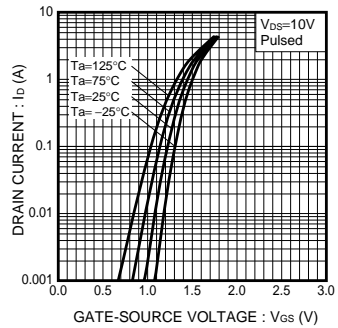


Fig.4 Typical Transfer Characteristics

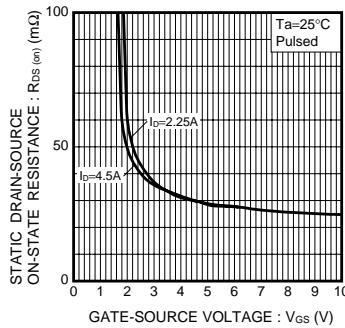


Fig.5 Static Drain-Source On-State Resistance vs. Gate-Source Voltage

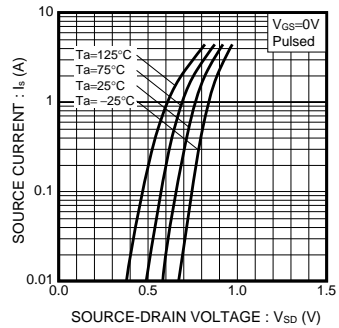


Fig.6 Source Current vs. Source-Drain Voltage

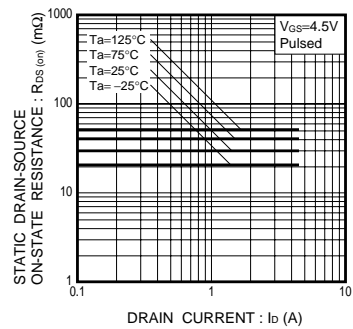


Fig.7 Static Drain-Source On-State Resistance vs. Drain Current (I)

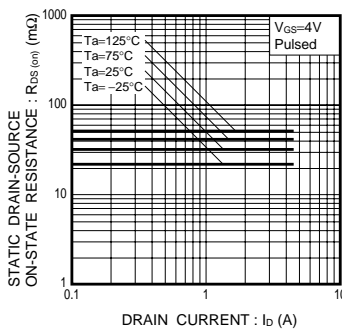


Fig.8 Static Drain-Source On-State Resistance vs. Drain Current (II)

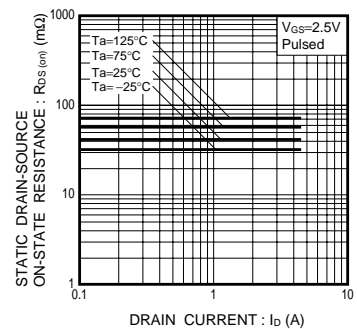


Fig.9 Static Drain-Source On-State Resistance vs. Drain Current (III)

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