





Features

- ●Low insertion loss, High isolation
- Perfect phase/amplitude balance
- ●Low VSWR
- ullet 50 Ω impedance
- Removable SMA connector

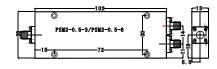
Specifications(measured in a 50 Ω system Ta=-55 $\mathbb{C} \sim$ +85 \mathbb{C})

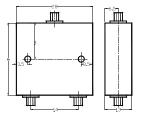
Parameter	Frequency Range	Insertion loss	Isolation	Phase unbalance		VSWR	Pack (m	m)
number	GHz	(Max)	(Min)	(Max)	(Max)	(Max)	Α	В
PSM2-0.5-3	0.5~3	0. 80	19△	3℃	0. 4	1. 5:1		
PSM2-0.5-6	0. 5~6	1. 20	18△	5℃	0. 5	1. 6:1		
PSM 2-1-2	1∼2	0. 50	20 🛆	2℃	0. 3	1.4:1	36. 0	
PSM 2-2-4	2~4	0.60	20 🛆	2℃	0. 3	1.4:1	36. 0	18
PSM2-2-8	2~8	0. 70	18△	4 °∆	0. 4	1. 4:1	36. 0	
PSM 2-3-6	3∼6	0. 65	20 🛆	3℃	0. 3	1. 5:1	25. 4	
PSM2-4-8	4∼8	0.60	22 🛆	3∆	0. 3	1.4:1	25. 4	12. 7
PSM2-4-10	4~10	0.80	20 🛆	5℃	0. 4	1. 5:1	25. 4	12. /
PSM2-7-12.5	7~12.5	0. 70	20 🛆	5℃	0. 4	1. 5:1	25. 4	

[&]quot; △ " Measured at Tc=24±1°C

Absolute Maximum Ratings

Input Power: 5W Storage Temp:+125℃

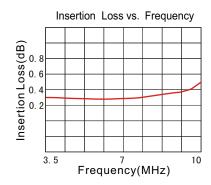


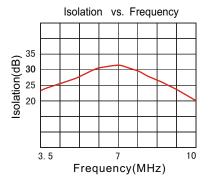


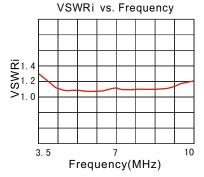
Application Notes

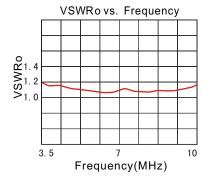
- 1. Input/output pins should be connected to 50Ω microstrip.
- 2. Removable SMA connector is available
- 3. When the input power is maximum the output load VSWR <1. 2 is required

Typical Performance









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