Unit: mm

TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

TK15X60U

Switching Regulator Applications

• Low drain-source ON resistance: RDS (ON) = 0.25Ω (typ.)

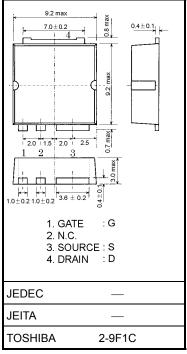
• High forward transfer admittance: $|Y_{fs}| = 8.5 \text{ S (typ.)}$

• Low leakage current: $I_{DSS} = 100 \mu A \text{ (max) (V}_{DS} = 600 \text{ V)}$

• Enhancement-mode: $V_{th} = 3.0 \text{ to } 5.0 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{ mA)}$

Absolute Maximum Ratings (Ta = 25°C)

Characteristics		Symbol	Rating	Unit
Drain-source voltage		V_{DSS}	600	V
Gate-source voltage		V _{GSS}	±30	V
Drain current	DC (Note 1)	I _D	15	Α
	Pulse (Note 1)	I _{DP}	30	A
Drain power dissipation	on (Tc = 25°C)	P _D	125	W
Single pulse avalanche energy (Note 2)		E _{AS}	81	mJ
Avalanche current		I _{AR}	11	Α
Repetitive avalanche energy (Note 3)		E _{AR}	12.5	mJ
Channel temperature		T _{ch}	150	°C
Storage temperature range		T _{stg}	-55 to 150	°C



Weight: 0.74 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of

high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings.

Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

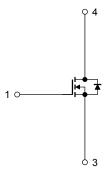
Characteristics	Symbol	Max	Unit
Thermal resistance, channel to case	R _{th (ch-c)}	1.0	°C/W

Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25°C (initial), L = 1.17 mH, R_G = 25 Ω , I_{AR} = 11 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.



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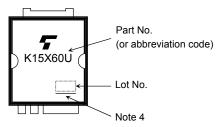
Electrical Characteristics (Ta = 25°C)

Chara	acteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage cur	rent	I _{GSS}	$V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$		_	±1	μΑ
Drain cut-off curre	ent	I _{DSS}	V _{DS} = 600 V, V _{GS} = 0 V	_		100	μΑ
Drain-source brea	akdown voltage	V (BR) DSS	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	600	_	_	V
Gate threshold vo	oltage	V _{th}	V _{DS} = 10 V, I _D = 1 mA	3.0	_	5.0	V
Drain-source ON	resistance	R _{DS} (ON)	V _{GS} = 10 V, I _D = 7.5 A		0.25	0.31	Ω
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 7.5 A	2.1	8.5	_	S
Input capacitance		C _{iss}			950	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	47	_	pF
Output capacitance		Coss			2300	_	
Switching time	Rise time	t _r	$\begin{array}{c c} 10 \text{ V} & I_D = 7.5 \text{ A} & V_{OUT} \\ \hline V_{GS} & \\ \hline 50 \Omega & \\ \hline \\ Duty \leq 1\%, \ t_w = 10 \ \mu s \\ \end{array}$		37		ns
	Turn-ON time	t _{on}			80	_	
	Fall time	t _f			8		
	Turn-OFF time	t _{off}		_	105	_	
Total gate charge		Qg		_	17	_	
Gate-source charge		Q _{gs}	V _{DD} ≈ 400 V, V _{GS} = 10 V, I _D = 15 A	_	10	_	nC
Gate-drain charge		Q _{gd}	<u> </u>		7		

Source-Drain Ratings and Characteristics (Ta = 25°C)

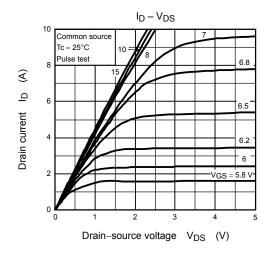
Characteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Continuous drain reverse current (Note	1) I _{DR}	_	_	_	15	Α
Pulse drain reverse current (Note	1) I _{DRP}	_	_	_	30	Α
Forward voltage (diode)	V _{DSF}	I _{DR} = 15 A, V _{GS} = 0 V	_	_	-1.7	V
Reverse recovery time	t _{rr}	I _{DR} = 15 A, V _{GS} = 0 V,	_	530	_	ns
Reverse recovery charge	Q _{rr}	$dI_{DR}/dt = 100 \text{ A}/\mu\text{s}$	_	9.0	_	μС

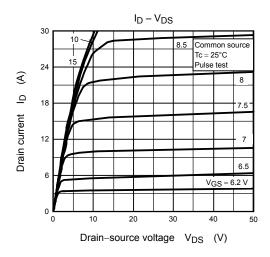
Marking

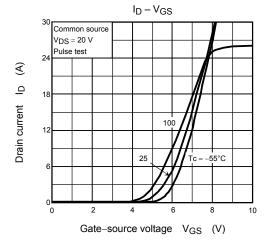


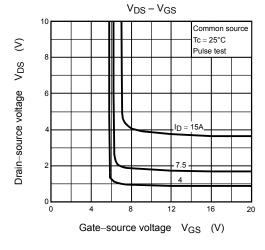
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

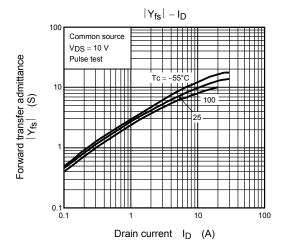
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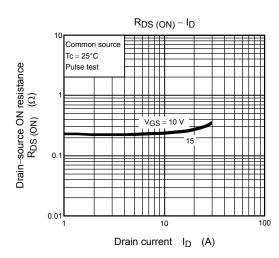




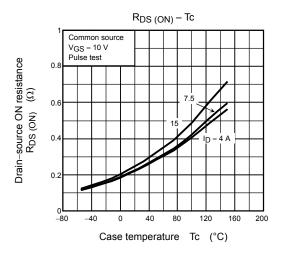


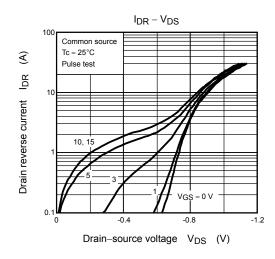


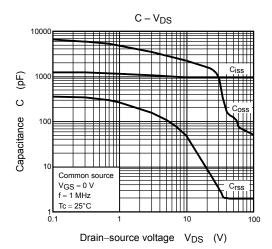


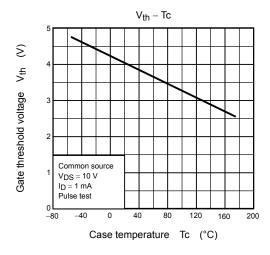


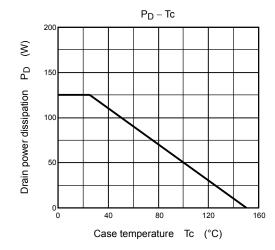
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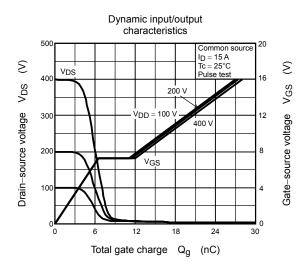




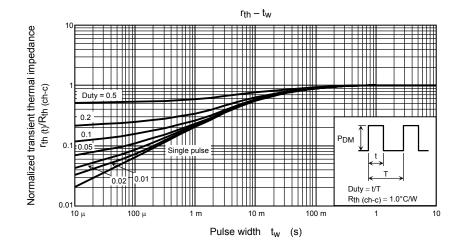


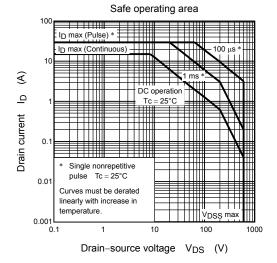


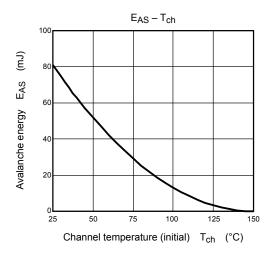


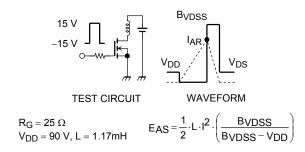


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