TOSHIBA Field Effect Transistor Silicon N Channel MOS Type (DTMOS II)

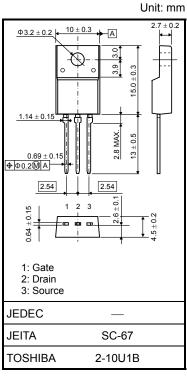
TK13A65U

Switching Regulator Applications

- Low drain-source ON resistance: RDS (ON) = $0.32~\Omega$ (typ.)
- High forward transfer admittance: $|Y_{fs}| = 8.0 \text{ S (typ.)}$
- Low leakage current: $I_{DSS} = 100 \,\mu\text{A} \,(\text{max}) \,(V_{DS} = 650 \,\text{V})$
- Enhancement-mode: $V_{th} = 3.0 \text{ to } 5.0 \text{ V (V}_{DS} = 10 \text{ V}, I_{D} = 1 \text{ mA})$

Absolute Maximum Ratings (Ta = 25°C)

| Characteristics | | Symbol | Rating | Unit | |
|----------------------------------------|------------------------------|------------------|------------|------|--|
| Drain-source voltage | | V _{DSS} | 650 | V | |
| Gate-source voltage | | V _{GSS} | ±30 | V | |
| | DC (Note 1) | I _D | 13 | А | |
| Drain current | Pulse (t = 1 ms) (Note 1) | I _{DP} | 26 | | |
| Drain power dissipation | on (Tc = 25°C) | P _D | 40 | W | |
| Single pulse avalanche energy (Note 2) | | EAS | 86 | mJ | |
| Avalanche current | | I _{AR} | 13 | Α | |
| Repetitive avalanche energy (Note 3) | | E _{AR} | 4.0 | mJ | |
| Channel temperature | | T _{ch} | 150 | °C | |
| Storage temperature range | | T _{stg} | -55 to 150 | °C | |



Weight: 1.7 g (typ.)

Note: Using continuously under heavy loads (e.g. the application of high temperature/current/voltage and the significant change in temperature, etc.) may cause this product to decrease in the reliability significantly even if the operating conditions (i.e. operating temperature/current/voltage, etc.) are within the absolute maximum ratings. Please design the appropriate reliability upon reviewing the Toshiba Semiconductor Reliability Handbook ("Handling Precautions"/"Derating Concept and Methods") and individual reliability data (i.e. reliability test report and estimated failure rate, etc).

Thermal Characteristics

| Characteristics | Symbol | Max | Unit |
|----------------------------------------|------------------------|-------|------|
| Thermal resistance, channel to case | R _{th (ch-c)} | 3.125 | °C/W |
| Thermal resistance, channel to ambient | R _{th (ch-a)} | 62.5 | °C/W |

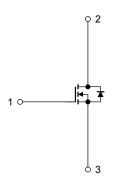
Note 1: Please use devices on conditions that the channel temperature is below 150°C.

Note 2: V_{DD} = 90 V, T_{ch} = 25 °C (initial), L = 0.9 mH, R_G = 25 Ω , I_{AR} = 13 A

Note 3: Repetitive rating: pulse width limited by maximum channel temperature

This transistor is an electrostatic sensitive device. Please handle with caution.







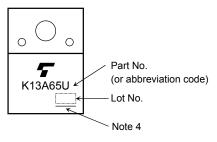
Electrical Characteristics (Ta = 25°C)

| Charac | teristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|------------------------------|---------------|----------------------|------------------------------------------------------------------------------------|-----|------|------|-------|
| Gate leakage current | | I _{GSS} | $V_{GS} = \pm 30 \text{ V}, V_{DS} = 0 \text{ V}$ | _ | _ | ±1 | μΑ |
| Drain cut-off currer | nt | I _{DSS} | V _{DS} = 650 V, V _{GS} = 0 V | _ | _ | 100 | μΑ |
| Drain-source break | down voltage | V (BR) DSS | I _D = 10 mA, V _{GS} = 0 V | 650 | _ | _ | V |
| Gate threshold volt | age | V _{th} | V _{DS} = 10 V, I _D = 1 mA | 3.0 | _ | 5.0 | V |
| Drain-source ON re | esistance | R _{DS (ON)} | V _{GS} = 10 V, I _D = 6.5 A | _ | 0.32 | 0.38 | Ω |
| Forward transfer a | dmittance | Y _{fs} | V _{DS} = 10 V, I _D = 6.5 A | 2.0 | 8.0 | - | S |
| Input capacitance | | C _{iss} | | | 950 | _ | |
| Reverse transfer capacitance | | C _{rss} | $V_{DS} = 10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$ | 1 | 47 | | pF |
| Output capacitance | | C _{oss} | | | 2300 | _ | |
| Switching time | Rise time | t _r | V_{GS} $V_{D} = 6.5 \text{ A}$ V_{OUT} V_{CS} $V_{DD} \approx 200 \text{ V}$ | | 30 | _ | ns ns |
| | Turn-ON time | t _{on} | | | 65 | _ | |
| | Fall time | t _f | | | 8 | | |
| | Turn-OFF time | t _{off} | Duty \leq 1%, $t_W = 10 \ \mu s$ | | 80 | _ | |
| Total gate charge | | Qg | | _ | 17 | _ | |
| Gate-source charge | | Q _{gs} | $V_{DD} \approx 400 \text{ V}, V_{GS} = 10 \text{ V}, I_D = 13 \text{ A}$ | _ | 10 | _ | nC |
| Gate-drain charge | | Q _{gd} | | | 7 | _ | |

Source-Drain Ratings and Characteristics (Ta = 25°C)

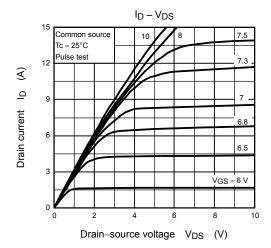
| Characteristics | Symbol | Test Condition | Min | Тур. | Max | Unit |
|-------------------------------------------|------------------|------------------------------------------------|-----|------|------|------|
| Continuous drain reverse current (Note 1) | I _{DR} | _ | _ | _ | 13 | Α |
| Pulse drain reverse current (Note 1) | I _{DRP} | _ | _ | _ | 26 | Α |
| Forward voltage (diode) | V _{DSF} | I _{DR} = 13 A, V _{GS} = 0 V | _ | _ | -1.7 | V |
| Reverse recovery time | t _{rr} | $I_{DR} = 13 \text{ A}, V_{GS} = 0 \text{ V},$ | _ | 430 | _ | ns |
| Reverse recovery charge | Q _{rr} | dI _{DR} /dt = 100 A/μs | _ | 7.0 | _ | μС |

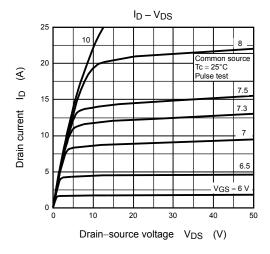
Marking

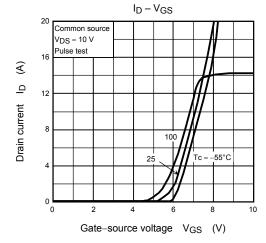


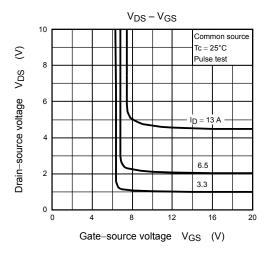
Note 4 : A line under a Lot No. identifies the indication of product Labels [[G]]/RoHS COMPATIBLE or [[G]]/RoHS [[Pb]]

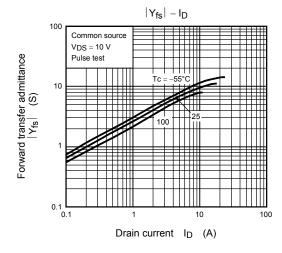
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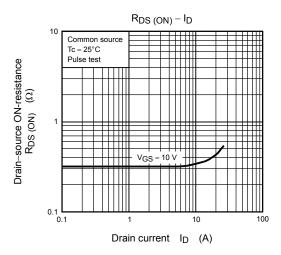


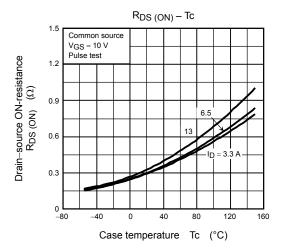


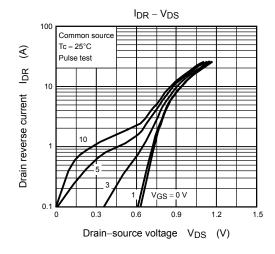


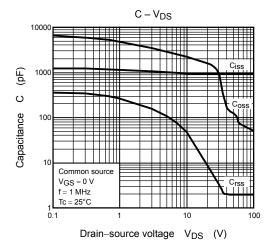


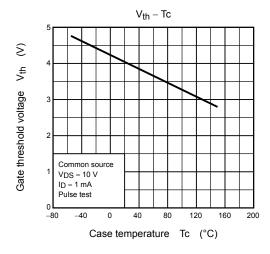


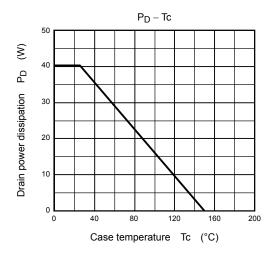


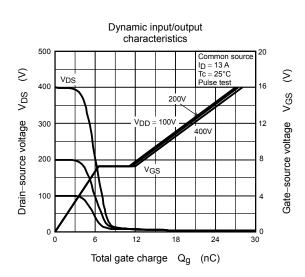


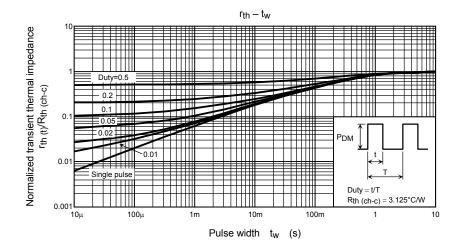


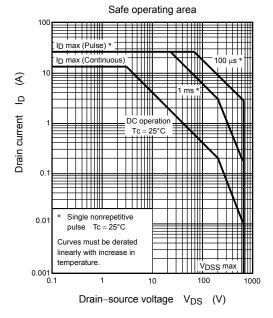


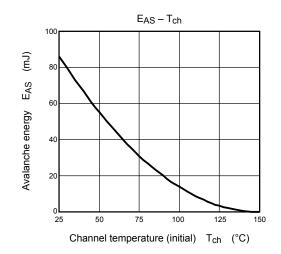


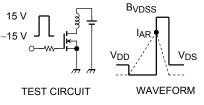












$$\begin{aligned} R_G &= 25~\Omega \\ V_{DD} &= 90~V,~L = 0.9~mH \end{aligned} \qquad E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot \left(\frac{BVDSS}{BVDSS} - VDD \right) \end{aligned}$$

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