Very Low Forward Voltage Trench-based Schottky Rectifier

Exceptionally Low $V_F = 0.42 \text{ V}$ at $I_F = 5 \text{ A}$

Features

- Fine Lithography Trench-based Schottky Technology for Very Low Forward Voltage and Low Leakage
- Fast Switching with Exceptional Temperature Stability
- Low Power Loss and Lower Operating Temperature
- Higher Efficiency for Achieving Regulatory Compliance
- Low Thermal Resistance
- High Surge Capability
- Pb-Free and Halide-Free Packages are Available

Typical Applications

- Switching Power Supplies including Notebook / Netbook Adapters, ATX and Flat Panel Display
- High Frequency and DC-DC Converters
- Freewheeling and OR-ing diodes
- Reverse Battery Protection
- Instrumentation

Mechanical Characteristics

- Case: Epoxy, Molded
- Epoxy Meets Flammability Rating UL 94-0 @ 0.125 in
- Finish: All External Surfaces Corrosion Resistant and Terminal Leads are Readily Solderable
- Lead Temperature for Soldering Purposes: 260°C Maximum for 10 sec

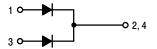


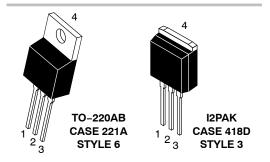
ON Semiconductor®

http://onsemi.com

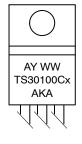
VERY LOW FORWARD
VOLTAGE, LOW LEAKAGE
SCHOTTKY BARRIER
RECTIFIERS 30 AMPERES,
100 VOLTS

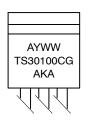
PIN CONNECTIONS





MARKING DIAGRAMS





A = Assembly Location

/ = Year

WW = Work Week

AKA = Polarity Designator

x = G or H

G = Pb-Free Package H = Halide-Free Package

ORDERING INFORMATION

See detailed ordering and shipping information in the package dimensions section on page 2 of this data sheet.

This document contains information on some products that are still under development. ON Semiconductor reserves the right to change or discontinue these products without notice.

MAXIMUM RATINGS

Rating			Value	Unit
Peak Repetitive Reverse Voltage Working Peak Reverse Voltage DC Blocking Voltage		V _{RRM} V _{RWM} V _R	100	V
Average Rectified Forward Current (Rated V _R , T _C = 125°C)	Per device Per diode	I _{F(AV)}	30 15	А
Peak Repetitive Forward Current (Rated V _R , Square Wave, 20 kHz, T _C = 120°C)	Per device Per diode	I _{FRM}	60 30	A
Nonrepetitive Peak Surge Current (Surge applied at rated load conditions halfwave, single phase, 60 Hz)		I _{FSM}	160	А
Operating Junction Temperature		TJ	-40 to +150	°C
Storage Temperature		T _{stg}	-40 to +150	°C
Voltage Rate of Change (Rated V _R)		dv/dt	10,000	V/µs

Stresses exceeding Maximum Ratings may damage the device. Maximum Ratings are stress ratings only. Functional operation above the Recommended Operating Conditions is not implied. Extended exposure to stresses above the Recommended Operating Conditions may affect device reliability.

THERMAL CHARACTERISTICS

Rating		Symbol	Value	Unit
Maximum Thermal Resistance	Junction-to-Case Junction-to-Ambient	$R_{ heta JC} \ R_{ heta JA}$	1.3 70	°C/W

ELECTRICAL CHARACTERISTICS (Per Leg unless otherwise noted)

Rating	Symbol	Тур	Max	Unit
Maximum Instantaneous Forward Voltage (Note 1)	٧ _F			V
$(I_F = 5 \text{ A}, T_J = 25^{\circ}\text{C})$		0.47	_	
$(I_F = 7.5 \text{ A}, T_J = 25^{\circ}\text{C})$		0.52	_	
$(I_F = 15 \text{ A}, T_J = 25^{\circ}\text{C})$		0.66	0.80	
(I _F = 5 A, T _{.I} = 125°C)		0.42	_	
$(I_{\rm F} = 7.5 \text{A}, T_{\rm J} = 125 \text{C})$		0.48	_	
(I _F = 15 A, T _J = 125°C)		0.60	0.65	
Maximum Instantaneous Reverse Current (Note 1)	I _R			
$(V_{B} = 70 \text{ V}, T_{I} = 25^{\circ}\text{C})$	11	15		μΑ
$(V_{\rm B} = 70 \text{ V}, T_{\rm J} = 125^{\circ}\text{C})$		12		mΑ
(Rated dc Voltage, T _J = 25°C)		65	675	μΑ
(Rated dc Voltage, T _J = 125°C)		32	60	mA

^{1.} Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \leq 2.0%

ORDERING INFORMATION

Device	Package	Shipping
NTST30U100CTG	TO-220AB (Pb-Free)	50 Units / Rail
NTST30U100CTH (In Development)	TO-220AB (Halide-Free)	50 Units / Rail
NTSB30U100CT-1G	I ² PAK (Pb-Free)	50 Units / Rail

TYPICAL CHARACTERISITICS

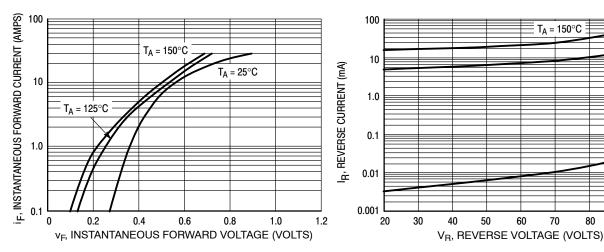


Figure 1. Typical Forward Voltage

Figure 2. Typical Reverse Current

 $T_A = 125^{\circ}C$

 $T_A = 25^{\circ}C$

90

100

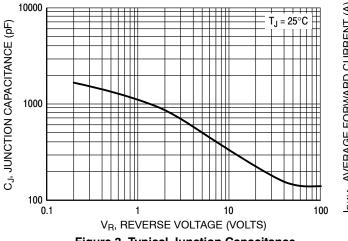


Figure 3. Typical Junction Capacitance

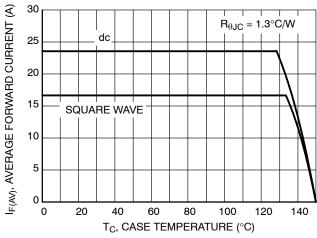


Figure 4. Current Derating per Leg

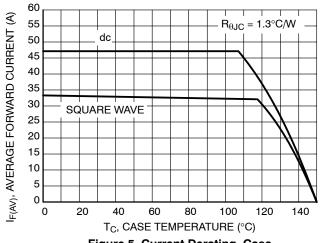


Figure 5. Current Derating, Case

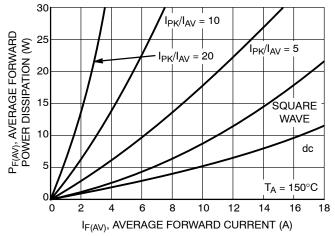


Figure 6. Forward Power Dissipation

TYPICAL CHARACTERISITICS

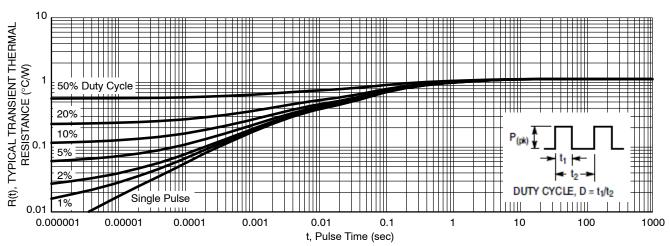
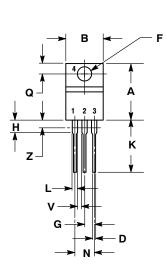
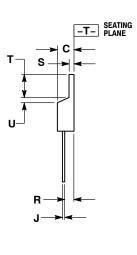


Figure 7. Typical Transient Thermal Response, Junction-to-Case

PACKAGE DIMENSIONS

TO-220 CASE 221A-09 **ISSUE AF**





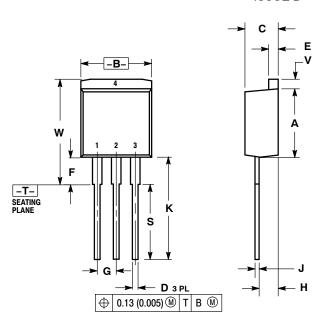
- NOTES:
 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
 2. CONTROLLING DIMENSION: INCH.
 3. DIMENSION Z DEFINES A ZONE WHERE ALL BODY AND LEAD IRREGULARITIES ARE ALLOWED.

	INCHES		MILLIN	IETERS
DIM	MIN	MAX	MIN	MAX
Α	0.570	0.620	14.48	15.75
В	0.380	0.405	9.66	10.28
С	0.160	0.190	4.07	4.82
D	0.025	0.035	0.64	0.88
F	0.142	0.161	3.61	4.09
G	0.095	0.105	2.42	2.66
Н	0.110	0.155	2.80	3.93
J	0.014	0.025	0.36	0.64
K	0.500	0.562	12.70	14.27
L	0.045	0.060	1.15	1.52
N	0.190	0.210	4.83	5.33
Q	0.100	0.120	2.54	3.04
R	0.080	0.110	2.04	2.79
S	0.045	0.055	1.15	1.39
T	0.235	0.255	5.97	6.47
U	0.000	0.050	0.00	1.27
٧	0.045		1.15	
Z		0.080		2.04

- STYLE 6:
 PIN 1. ANODE
 2. CATHODE
 3. ANODE
 4. CATHODE

PACKAGE DIMENSIONS

I²PAK (TO-262) CASE 418D-01 ISSUE D



- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
- 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	0.335	0.380	8.51	9.65	
В	0.380	0.406	9.65	10.31	
С	0.160	0.185	4.06	4.70	
D	0.026	0.035	0.66	0.89	
E	0.045	0.055	1.14	1.40	
F	0.122 REF		3.10 REF		
G	0.100 BSC		2.54	BSC	
Н	0.094	0.110	2.39	2.79	
J	0.013	0.025	0.33	0.64	
K	0.500	0.562	12.70	14.27	
S	0.390 REF		9.90	REF	
V	0.045	0.070	1.14	1.78	
W	0.522	0.551	13.25	14.00	

STYLE 3:

PIN 1. ANODE 2. CATHODE

3. ANODE 4. CATHODE

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