secos
Elektronische Bauelemente

SSRF4N60

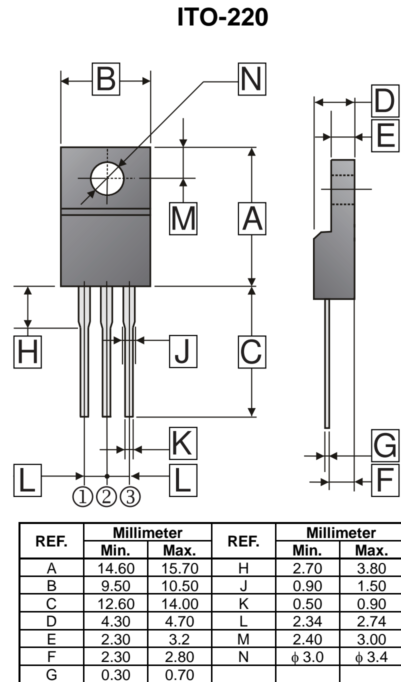
4A , 600 V , $R_{DS(ON)}$ 2.4 Ω
N-Channel Enhancement Mode Power MOSFET

RoHS Compliant Product
A suffix of "-C" specifies halogen and lead-free

DESCRIPTION

The N-Channel MOSFET is used an advanced termination scheme to provide enhanced voltage-blocking capability without degrading performance over time. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance. This device is well suited for high efficiency switched mode power suppliers, active power factor correction, electronic lamp ballasts based half bridge topology.

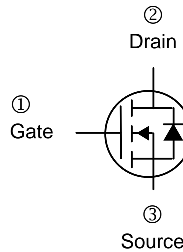
ITO-220



REF.	Millimeter		REF.	Millimeter	
	Min.	Max.		Min.	Max.
A	14.60	15.70	H	2.70	3.80
B	9.50	10.50	J	0.90	1.50
C	12.60	14.00	K	0.50	0.90
D	4.30	4.70	L	2.34	2.74
E	2.30	3.2	M	2.40	3.00
F	2.30	2.80	N	ϕ 3.0	ϕ 3.4
G	0.30	0.70			

FEATURES

- Robust high voltage termination
- Avalanche energy specified
- Diode is characterized for use in bridge circuits
- Source to Drain diode recovery time comparable to a discrete fast recovery diode.



ABSOLUTE MAXIMUM RATINGS(T_C=25℃ unless otherwise specified)

Parameter	Symbol	Ratings	Unit
Drain-Source Voltage	V_{DS}	600	V
Gate-Source Voltage	V_{GS}	±30	V
Continuous Drain Current	I_D	4.0	A
Pulsed Drain Current	I_{DM}	16	A
Power Dissipation[2]	P_D	33	W
Derating factor above 25℃		0.26	W / ℃
Single Pulsed Avalanche Energy[1]	E_{AS}	330	mJ
Repetitive Avalanche Energy[2]	E_{AR}	7.3	mJ
Operating Junction and Storage Temperature Range	T_J, T_{stg}	150,-55~150	°C
Thermal Resistance Rating			
Maximum Junction to Ambient	$R_{θJA}$	62.5	℃ / W
Maximum Junction to Case	$R_{θJC}$	3.79	

Notes:
1. L=30mH, I_{AS}=4.4A, V_{DD}=85V, R_G=25Ω, Starting T_J=25℃
2. Repetitive Rating: Pulse width limited by maximum junction temperature

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ELECTRICAL CHARACTERISTICS (T_A=25°C unless otherwise specified)

Parameter	Symbol	Min.	Typ.	Max.	Unit	Test Conditions
Static						
Gate-Threshold Voltage	$V_{GS(th)}$	2.0	-	4.0	V	V_{DS}=V_{GS}, I_D=250μA
Drain-Source On-Resistance	$R_{DS(ON)}$	-	2.0	2.4	Ω	V_{GS}=10V, I_D=2A
Drain-Sounce Breakdown Voltage	BV_{DSS}	600	-	-	V	V_{GS}=0, I_D=250μA
Zero Gate Voltage Drain Current	I_{DSS}	-	-	10	μA	V_{DS}=600V, V_{GS}=0
Gate-Body Leakage Current, Forward	I_{GSSF}	-	-	100	nA	V_{GS}=30V, V_{DS}=0
Gate-Body Leakage Current, Reverse	I_{GSSR}	-	-	-100	nA	V_{GS}= -30V, V_{DS}=0
Dynamic						
Total Gate Charge [1.2]	Q_g	-	19.8	-	nC	V_{DS}=480V, I_D=4.4A, V_{GS}=10V
Gate-Source Charge [1.2]	Q_{gs}	-	4	-		
Gate-Drain Charge [1.2]	Q_{gd}	-	7.2	-		
Turn-on Delay Time [1.2]	$T_{d(on)}$	-	27	-	nS	V_{DD}=300V, I_D =4.4A, R_G=25Ω
Rise Time [1.2]	T_r	-	19	-		
Turn-off Delay Time [1.2]	$T_{d(off)}$	-	160	-		
Fall Time [1.2]	T_f	-	22	-		
Input Capacitance	C_{iss}	-	672	-	pF	V_{DS}=25V, V_{GS}=0, f =1.0MHz
Output Capacitance	C_{oss}	-	66	-		
Reverse Transfer Capacitance	C_{rss}	-	4.7	-		
Maximum Continuous Drain-Source Diode Forward Current	I_S	-	-	4.0	A	
Maximum Pulsed Drain-Source Diode Forward Current	I_{SM}	-	-	16	A	
Drain-Source Diode Forward Voltage	V_{SD}	-	-	1.4	V	V_{GS}=0, I_S=4.0A
Reverse Recovery Time	T_{rr}	-	300	-	nS	V_{GS}=0, I_S=4.0A, I_F / dt =100A/μs
Reverse Recovery Charge [1]	Q_{rr}	-	2.2	-	μC	

Notes:
1. Pulse Test: Pulse width < 300us, Duty cycle ≤ 2%.
2. Basically not affected by working temperature.

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CHARACTERISTIC CURVE

Fig 1. On-State Characteristics

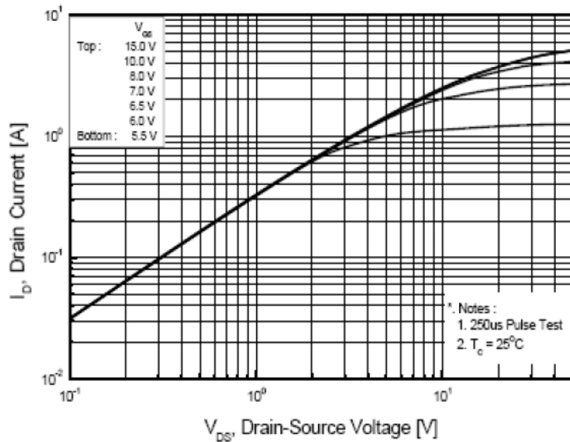


Fig 2. Transfer Characteristics

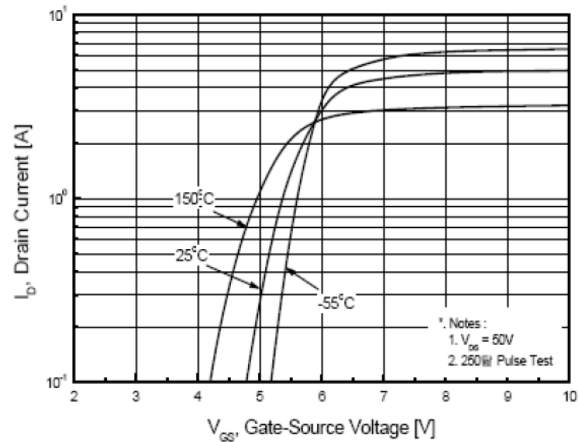


Fig 3. On Resistance Variation vs. Drain Current and Gate Voltage

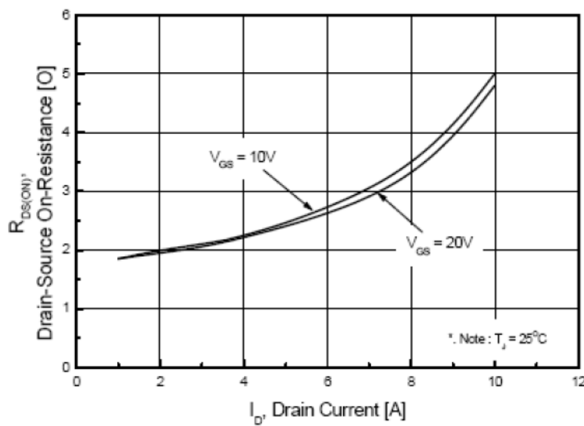


Fig 4. On State Current vs. Allowable Case Temperature

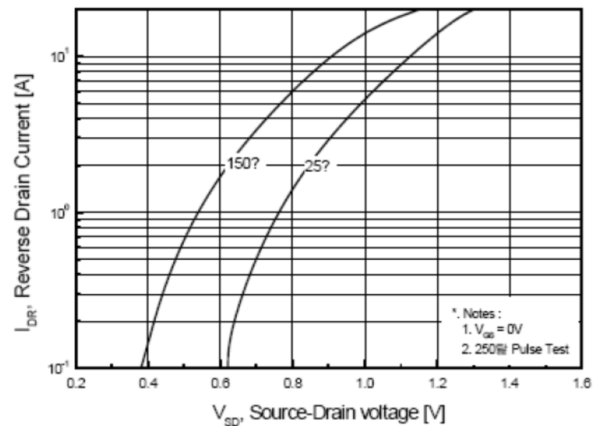


Fig 5. Capacitance Characteristics (Non-Repetitive)

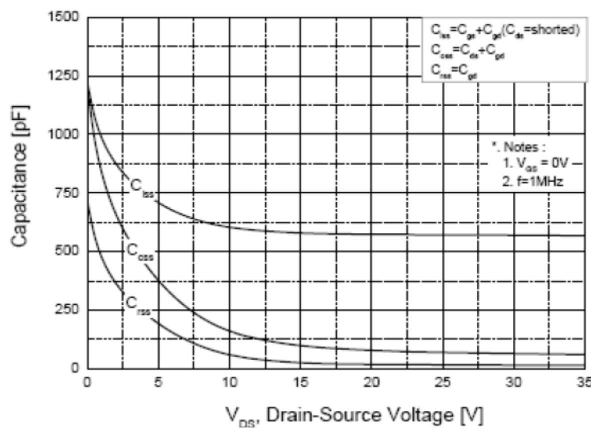
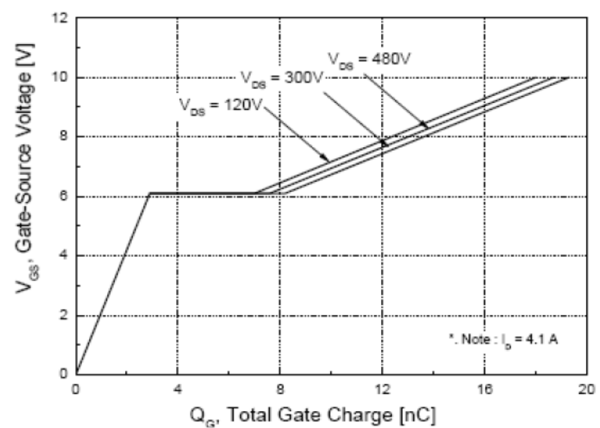


Fig 6. Gate Charge Characteristics



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CHARACTERISTIC CURVE

Fig 7. Breakdown Voltage Variation vs. Junction Temperature

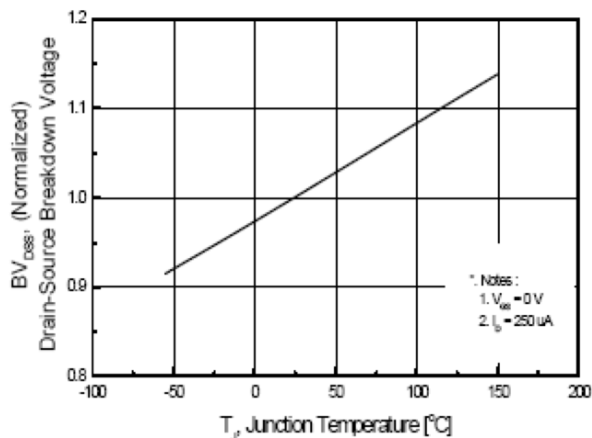


Fig 8. On-Resistance Variation vs. Junction Temperature

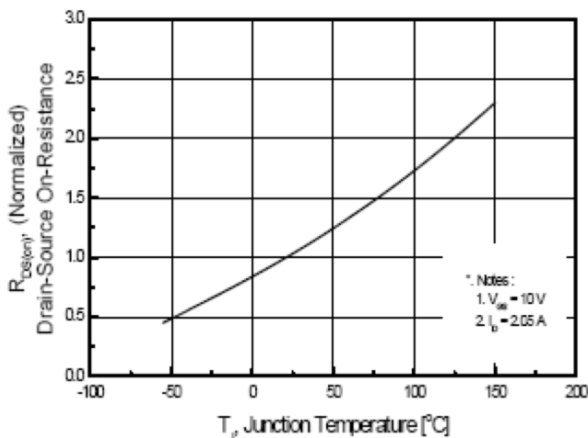


Fig 9. Maximum Safe Operating Area (TO-220F)

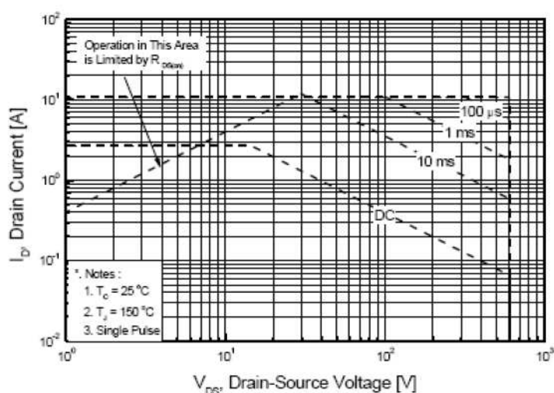


Fig 10. Maximum Drain Current vs. Case Temperature. (TO220F)

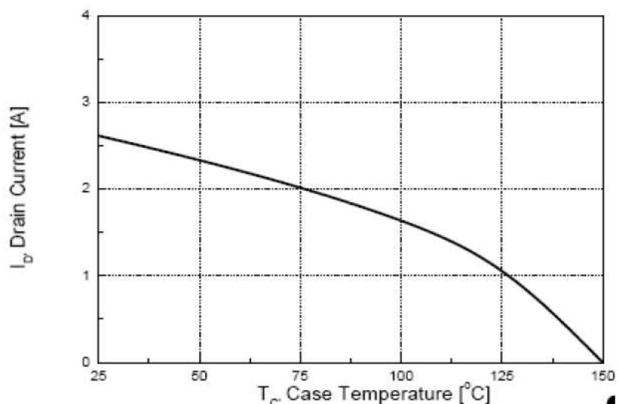
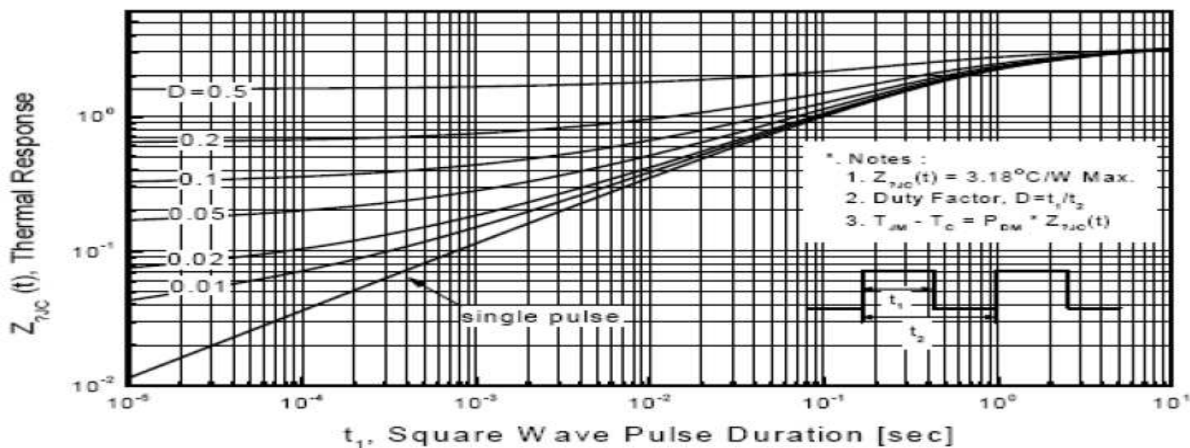


Fig 11. Transient Thermal Response Curve(TO220F)



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