IP4283CZ10

ESD protection for ultra high-speed interfaces

Rev. 01 — 7 May 2009

Product data sheet



1. Product profile

1.1 General description

The IP4283CZ10 is designed to protect high-speed interfaces such as HDMI, DisplayPort, eSATA and LVDS against ElectroStatic Discharge (ESD). The device includes high-level ESD protection diodes for ultra high-speed signal lines and is available in two package variants: UTLP10 and TSSOP10.

All signal lines are protected by a special diode configuration offering ultra-low line capacitance of only 0.6 pF. These diodes provide protection to downstream components from ESD voltages up to ± 8 kV contact according to IEC 61000-4-2, level 4.

1.2 Features

- Pb-free, RoHS compliant and free of Halogen and Antimony (Dark Green compliant)
- System ESD protection for HDMI, DisplayPort, eSATA and LVDS
- All signal lines with integrated rail-to-rail clamping diodes for downstream ESD protection of ±8 kV according to IEC 61000-4-2, level 4
- Matched 0.5 mm trace spacing
- Signal lines with ≤ 0.05 pF matching capacitance between signal pairs
- Line capacitance of only 0.6 pF for each channel
- 4-channel, UTLP10 or TSSOP10 Pb-free package
- HDMI 1.3a compliant
- DisplayPort compliant
- Design-friendly 'pass-thru' signal routing

1.3 Applications

The IP4283CZ10 is designed for high-speed receiver and transmitter port protection:

- TVs, monitors
- DVD recorders and players
- Notebooks, main board graphics cards and ports
- Set-top boxes and game consoles



2. Pinning information

Table 1. Pinning

Pin	Symbol	Description	Simplified outline	Graphic symbol
1	TMDS_CH1-	negative channel 1 ESD protection	10	
2	TMDS_CH1+	positive channel 1 ESD protection		
3	GND	ground	2 9	
4	TMDS_CH2-	negative channel 2 ESD protection	3 8	3, 8 _{001aai619}
5	TMDS_CH2+	positive channel 2 ESD protection	4 7	ООТАВІВТЯ
6	n.c.	not connected		□ 5
7	n.c.	not connected	TSSOF	P10
8	GND	ground	Transparent top view	
9	n.c.	not connected	XSON10U	
10	n.c.	not connected		

3. Ordering information

Table 2. Ordering information

Type number	Package							
	Name	Description	Version					
IP4283CZ10-TB	XSON10U	plastic extremely thin small outline package; no leads; 10 terminals; UTLP based; body 1 \times 2.5 \times 0.5 mm	SOT1059-1					
IP4283CZ10-TT	TSSOP10	plastic thin shrink small outline package; 10 leads; body width 3 mm	SOT552-1					

4. Limiting values

Table 3. Limiting values

In accordance with the Absolute Maximum Rating System (IEC 60134).

Symbol	Parameter	Conditions	Min	Max	Unit
V_{I}	input voltage		GND - 0.5	+5.5	V
V _{esd}	electrostatic discharge voltage	all pins to ground; IEC 61000-4-2, level 4; contact discharge	-8	+8	kV
T _{stg}	storage temperature		– 55	+125	°C
T_{amb}	ambient temperature		-40	+85	°C

5. Characteristics

Table 4. Characteristics

 $T_{amb} = 25 \,^{\circ}C$ unless otherwise specified.

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
V_{BRzd}	Zener diode breakdown voltage	I = 1 mA		6	-	9	V
I _{LRzd}	Zener diode reverse leakage current	per TMDS channel; V = 3.0 V		-	-	1	μΑ
V_{F}	forward voltage			-	0.7	-	V
C _{ch(TMDS)}	TMDS channel capacitance	$f = 1 \text{ MHz}; V_{\text{bias}} = 2.5 \text{ V}$	<u>[1]</u>	-	0.6	-	pF
$\Delta C_{ch(TMDS)}$	TMDS channel capacitance difference	$f = 1 \text{ MHz}; V_{\text{bias}} = 2.5 \text{ V}$	<u>[1]</u>	-	0.05	-	pF
C _{ch(mutual)}	mutual channel capacitance	between signal pin and pin n.c.; f = 1 MHz; V _{bias} = 2.5 V	<u>[1]</u>	-	0.07	-	pF
R _{dyn}	dynamic resistance	I = 1 A; T _{amb} = 25 °C	[3]				
		positive transient		-	2.4	-	Ω
		negative transient		-	1.3	-	Ω
V _{CL(ch)trt(pos)}	positive transient channel clamping voltage	V_{esd} = 8 kV HBM; T_{amb} = 25 °C	[2]	-	8	-	V

^[1] This parameter is guaranteed by design.

^[2] Human Body Model according to JESD22-A-J114D.

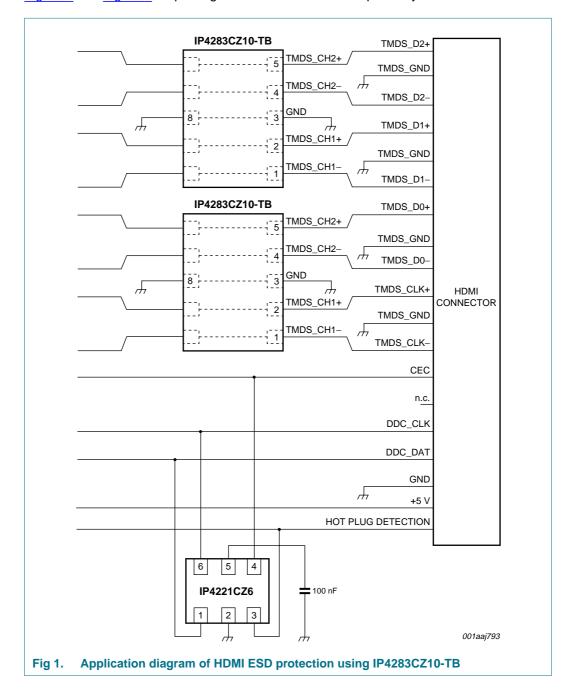
^[3] According to IEC 61000-4-5/9.

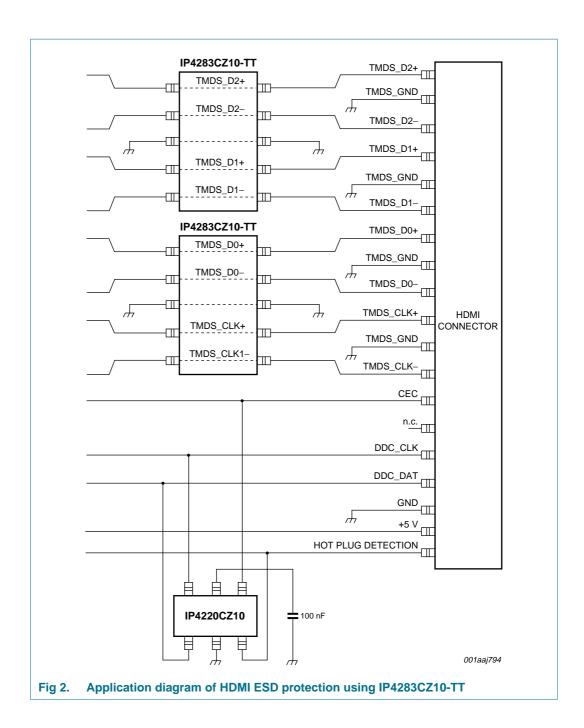
6. Application information

The IP4283CZ10 is designed mainly to provide high-level ESD protection for high-speed serial data buses such as HDMI, DisplayPort, eSATA and LVDS data lines.

It is recommended that when designing the printed-circuit board, careful consideration is given to impedance matching, and signal coupling.

A basic application diagram for the ESD protection of an HDMI interface is shown in Figure 1 and Figure 2 for package versions TB and TT respectively.





7. Package outline

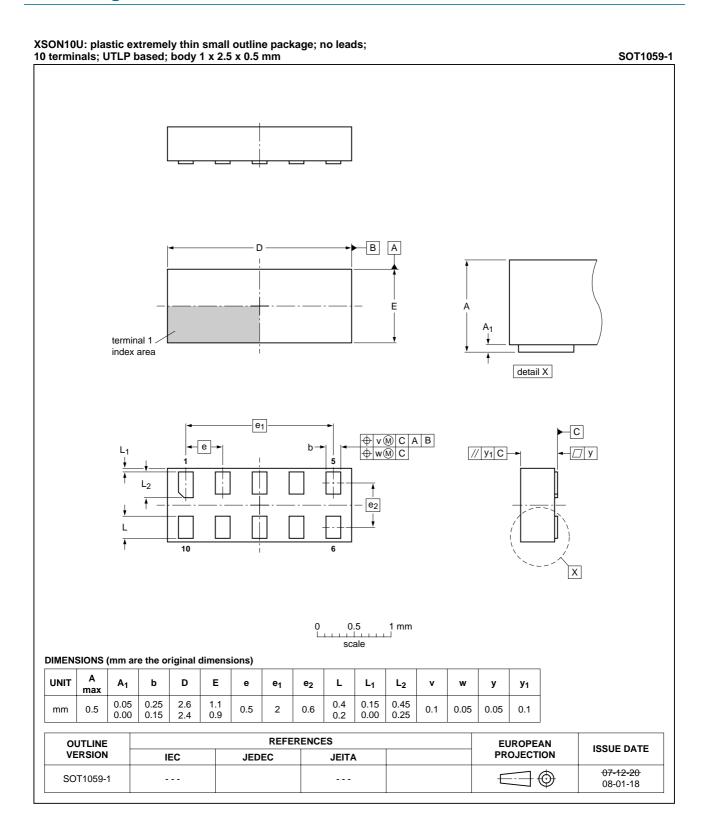
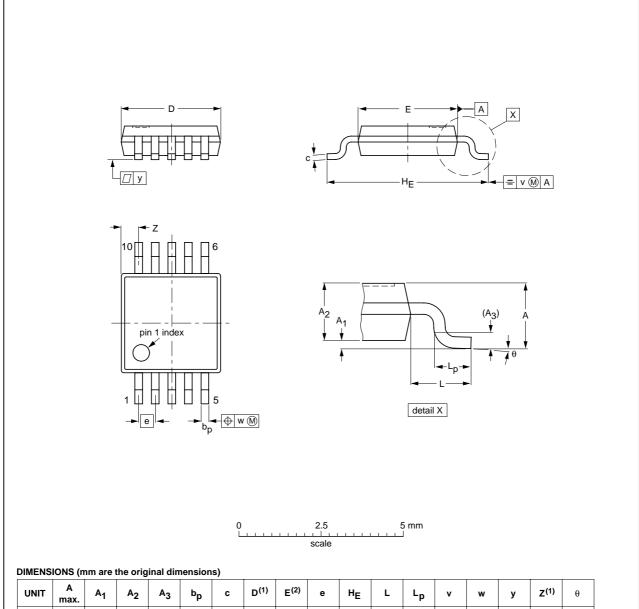


Fig 3. Package outline SOT1059-1 (XSON10U)

TSSOP10: plastic thin shrink small outline package; 10 leads; body width 3 mm

SOT552-1



UNIT	A max.	A ₁	A ₂	A ₃	bp	С	D ⁽¹⁾	E ⁽²⁾	е	HE	L	Lp	v	w	у	Z ⁽¹⁾	θ
mm	1.1	0.15 0.05	0.95 0.80	0.25	0.30 0.15	0.23 0.15	3.1 2.9	3.1 2.9	0.5	5.0 4.8	0.95	0.7 0.4	0.1	0.1	0.1	0.67 0.34	6° 0°

- 1. Plastic or metal protrusions of 0.15 mm maximum per side are not included.
- 2. Plastic or metal protrusions of 0.25 mm maximum per side are not included.

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Fig 4. Package outline SOT552-1 (TSSOP10)

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8. Abbreviations

Table 5. Abbreviations

Acronym	Description
DVD	Digital Versatile Disc
eSATA	external Serial Advanced Technology Attachment
ESD	ElectroStatic Discharge
НВМ	Human Body Model
HDMI	High-Definition Multimedia Interface
LVDS	Low-Voltage Differential Signaling
RoHS	Restriction of Hazardous Substances
TMDS	Transition Minimized Differential Signaling
UTLP	Ultra-Thin Leadless Package

9. Revision history

Table 6. Revision history

Document ID	Release date	Data sheet status	Change notice	Supersedes
IP4283CZ10_1	20090507	Product data sheet	-	-

10. Legal information

10.1 Data sheet status

Document status[1][2]	Product status[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
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