NPN Silicon Epitaxial Planar Transistor

for switching and amplifier applications. Especially suitable for AF-driver stages and low power output stages.

The transistor is subdivided into three groups, G, H and I, according to its DC current gain. As complementary type the PNP transistor 9012 is recommended.



1. Emitter 2. Base 3. Collector TO-92 Plastic Package

Absolute Maximum Ratings (T_a = 25 °C)

Parameter	Symbol	Value	Unit
Collector Base Voltage	V_{CBO}	40	V
Collector Emitter Voltage	V_{CEO}	30	V
Emitter Base Voltage	V_{EBO}	5	V
Collector Current	I _C	500	mA
Power Dissipation	P _{tot}	625	mW
Junction Temperature	T _j	150	°C
Storage Temperature Range	T _{stg}	- 55 to + 150	°C

Characteristics at T_a = 25 °C

Parameter	Symbol	Min.	Max.	Unit
DC Current Gain at V_{CE} = 1 V, I_C = 50 mA	h _{FE} h _{FE} h _{FE}	110 177 250 40	183 250 380	- - - -
Collector Base Cutoff Current at V _{CB} = 35 V	I _{CBO}	-	100	nA
Emitter Base Cutoff Current at V _{EB} = 5 V	I _{EBO}	-	100	nA
Collector Bae Breakdown Voltage at I _C = 100 μA	V _{(BR)CBO}	40	-	V
Collector Emitter Breakdown Voltage at I _C = 1 mA	V _{(BR)CEO}	30	-	V
Emitter Base Breakdown Voltage at I _E = 100 μA	V _{(BR)EBO}	5	-	V
Collector Emitter Saturation Voltage at $I_C = 500$ mA, $I_B = 50$ mA	V _{CE(sat)}	-	0.6	V
Base Emitter Saturation Voltage at $I_C = 500 \text{ mA}$, $I_B = 50 \text{ mA}$	V _{BE(sat)}	-	1.2	V
Base Emitter Voltage at $V_{CE} = 1 \text{ V}$, $I_C = 100 \text{ mA}$	V _{BE}	-	1	V
Gain Bandwidth Product at $V_{CE} = 6 \text{ V}$, $I_{C} = 20 \text{ mA}$	f _T	100	-	MHz



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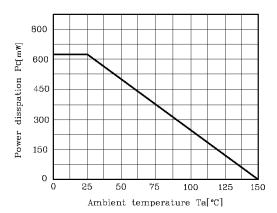






Dated: 19/03/2009

Fig. 1 Ptot- Ta



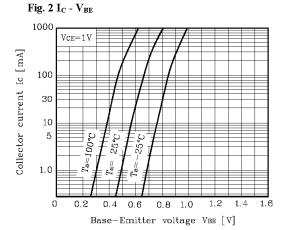


Fig. 3 I_{C} - V_{CE}

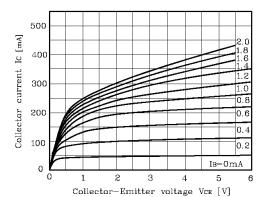


Fig. 4 $V_{\text{CE(SAT)}}$ - I_{C}

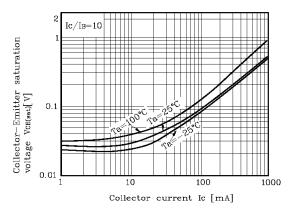
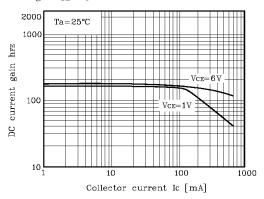


Fig. 5 h_{FE} - $I_{\rm C}$





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