SPECIFICATION

	Device	Name :	 IGB	TMod	ule			
	Type Na	ame:	 7MBR5(OSB06	0-01			
-	Spec.	<u>No. :</u>	 MSe	6M 058	51			
	Date	:	 Jun. –	02 - 2	2000			
								1
						Fuji Electri		1+4
						Matsumoto Fa	actory	,
DATE DRAWN Jun - 2 - 100 7	NAME	APPROVED	 		F	Fuji Electric	; Co.,I	Ltd.
DRAWN Jun 2 - 100 J CHECKED June - 2 - 00 S	, Notta	Truperta		DWG.NO.	MS	6M 0551		1/10

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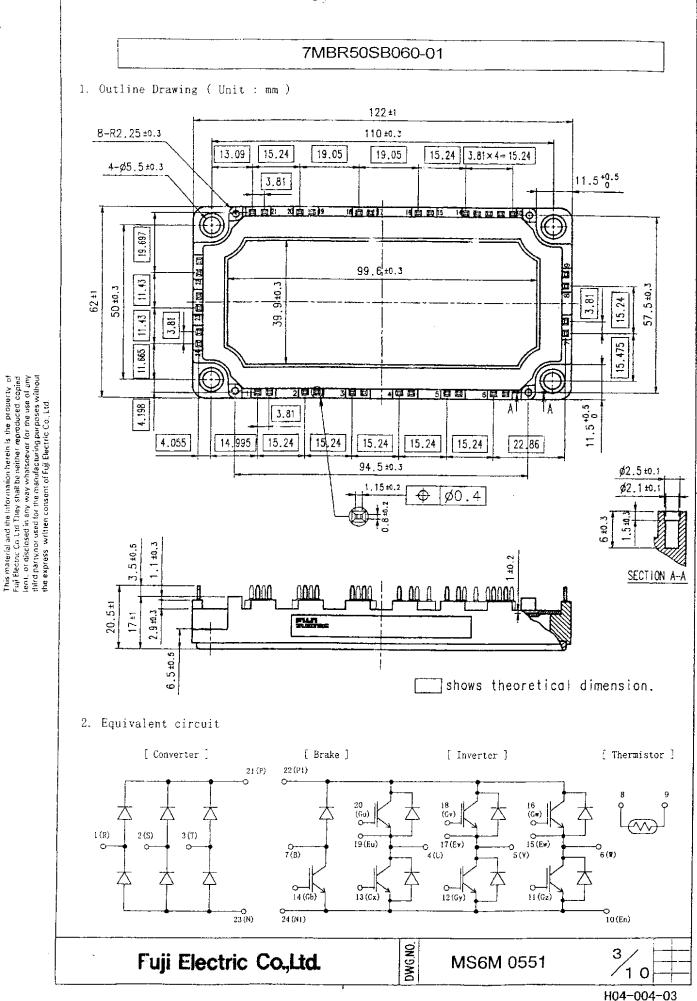
Date	Classi- fication	Ind.	Contant	Applied			
			Content	date	Drawn	Checked	Approve
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Date	Classi- fication	Ind.	Gontent	Applied date	Drawn	Checked	Appr
Jun - 2 - 'cc	enactment			lssued date		S, hydra	T.No
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		Items	Symbols	Conditions	Maximum Ratings	Units
	Collec	ctor-Emitter voltage	VCES		600	V
	Gate-	Emitter voltage	VGES		+-20	V
nverter			lc	Continuous	50	A
nve	Collec	otor current	lcp	1ms	100	Á
_			-lc		50	Α
	Collec	ctor Power Dissipation	Pc	1 device	200	W
	Collec	otor-Emitter voltage	VCES		600	V
	Gat.	Emitter voltage	VGES		+-20	V
Brake	Collec	Collector current		Continuous	30	Α
Ъ			Іср	1ms	60	A
	Collec	ctor Power Dissipation	Pc	1 device	120	W
	Repe	titive peak reverse Voltage(Diode)	VRRM		600	V
	Repe	Repetitive peak reverse Voltage			800	V
Converter	Avera	Average Output Current		50Hz/60Hz sine wave	50	А
Con	Surge	Surge Current (Non-Repetitive)		Tj=150C,10ms	350	A
0	$ ^2t$	(Non-Repetitive)	l ² t	half sine wave	613	A ² s
June	ction te	mperature	Tj		150	С
Stor	age ter	nperature	Tstg		-40~ +125	С
sola	ation	between terminal and copper base(*1)	Viso	AC : 1min.	2500	V
volt	age	between thermistor and others (*2)	7		2500	V
Mou	inting S	crew Torque ^(*3)			3.5	Nm

3. Absolute Maximum Ratings (at Tc= 25C unless otherwise specified)

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(*1) All terminals should be connected together when isolation test will be done.

(*2) Terminal 8 and 9 should be connected together. Terminal 1 to 7 and 10 to 24

should be connected together and shorted to copper base.

(*3) Recommendable Value : 2.5~3.5 Nm (M5)

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•			Ì				Ch	aracteri	stics	<u> </u>
	Items	Symbols		Con	ditions	;	min.	typ.	Max.	Un
	Zero gate voltage Collector current	ICES	VGE	0 V,	VCE	600 V			1.0	m
	Gate-Emitter leakage current	IGES	VCE	0 V,	VGE	+-20 V	1		200	n/
	Gate-Emitter	VGE(th)								+
	threshold voltage		IVCE	20 V,	IC =	50 m	5.5	7.8	8.5	\
	Collector-Emitter	VCE(sat)	VGE	15 V,	chip			1.8		\Box
ter	saturation voltage		lc =	50 A	termi	nal		1.95	2.4	
Inverter	Input capacitance	Cies	VGE f =	0 V, 1 M	VCE	10 V		5000		p
	Turn-on time	ton	·	300 V				0.45	1.2	
			-							-
		tr		50 A				0.25	0.6	
		tr _(i)	4	+-15 V				0.08		u
	Turn-off time	toff	RG =	51 of	ım			0.40	1.0	1
		tf						0.05	0.35	
	Forward on voltage	VF	1F =	50 A	chip			1.75		
					termi	nal		1.9	2.6	
	Reverse recovery time	trr	IF =	50 A					300	n
	Zero gate voltage	ICES	VGE	0.1/	VCE	600 V		·	10	
	Collector current								1.0	m
	Gate-Emitter leakage current	IGES	VCE			+-20 V			200	n
θ	Collector-Emitter	VCE(sat)	VGE	15 V,	<u> </u>			1.8		\
Brake	saturation voltage		lc =		termir	nal		1.95	2.4	
m i	Turn-on time	ton	Vcc≖	300 V				0.45	1.2	
:		tr	lc =	30 A				0.25	0.6	u
1	Turn-off time	toff	VGE	+-15 V				0.40	1.0	1
1		tf	RG =	82 oh	m			0.05	0.35	1
	Reverse current	IRRM	VR =	600 V	·····				1.0	l m
ē	Forward on voltage	VFM	F =	50 A	chip			1.1		$\overline{}$
ven					termir	ial		1.2	1.5	v
Converter	Reverse current	IRRM	VB -	800 V					1.0	m
	· · · · · · · · · · · · · · · · · · ·		T = 25			· · · · · · · · · · · · · · · · · · ·		5000	1.0	l
listo	Resistance	R	T = 25C T = 100C			405		500	ohn	
Thermistor			<u> </u>				465	495	520	
F	B value	В	T = 25	50C			3305	3375	3450	K
	rmal resistance characteristics									
пе				• • •		f	Cha	racteris	tics	
i ne				Conc	litions	ŀ	min.	typ.	Max.	Uni
ne	Items	Symbols								
	Items	Symbols	Inverte	er IGBT					0.63	
	Items rmal resistance	Symbols	<u>~ </u>						0.63	
			<u>~ </u>	er IGBT er FWD						C/V

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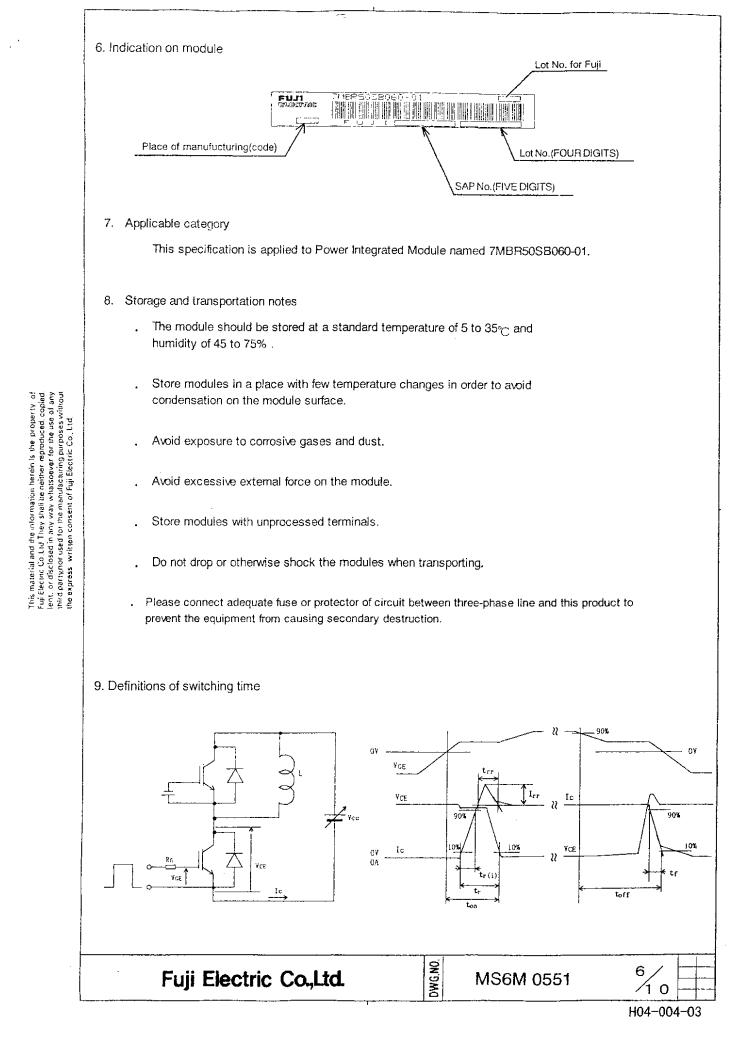
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Items	Symbols	Conditions	min.	typ.	Max.	Units
		Inverter IGBT			0.63	
Thermal resistance	Rth(j-c)	Inverter FWD			1.33	1
(1 device)		Brake IGBT			1.04	c∕w
		Converter Diode			0.90	1
Contact Thermal resistance	Rth(c-f)	with Thermal Compound		0.05	<u> </u>	C/W

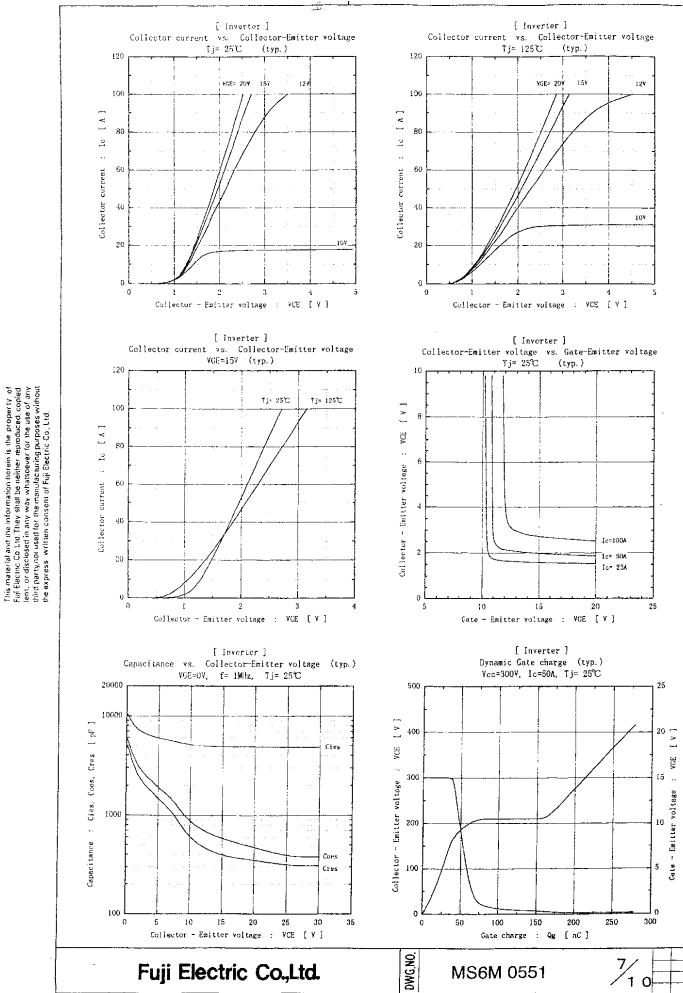
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* This is the value which is defined mounting on the additional cooling fin with thermal compound.

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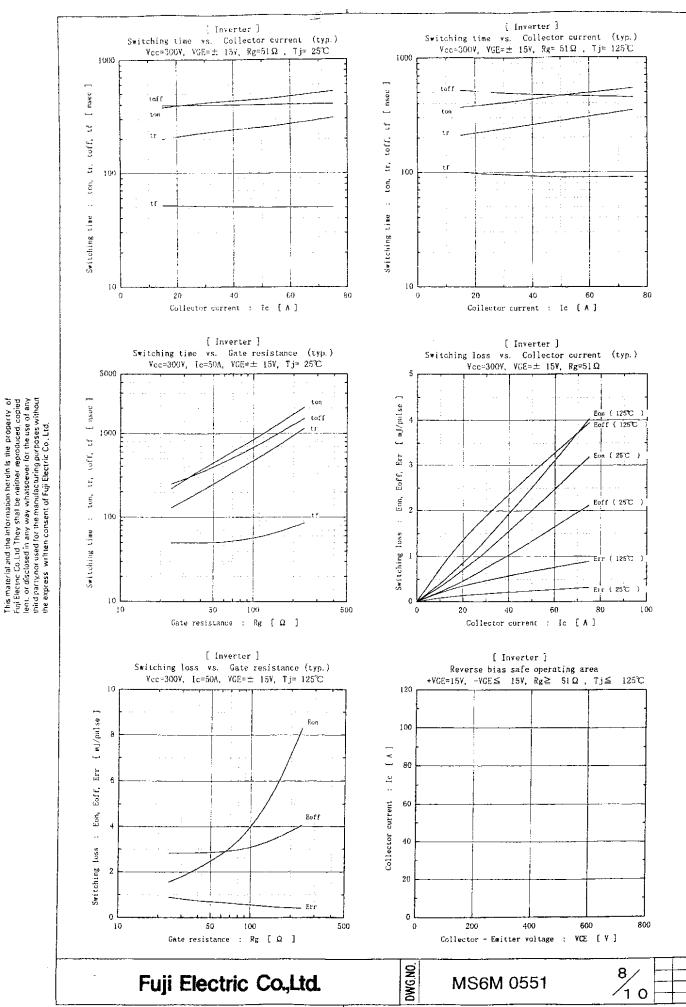




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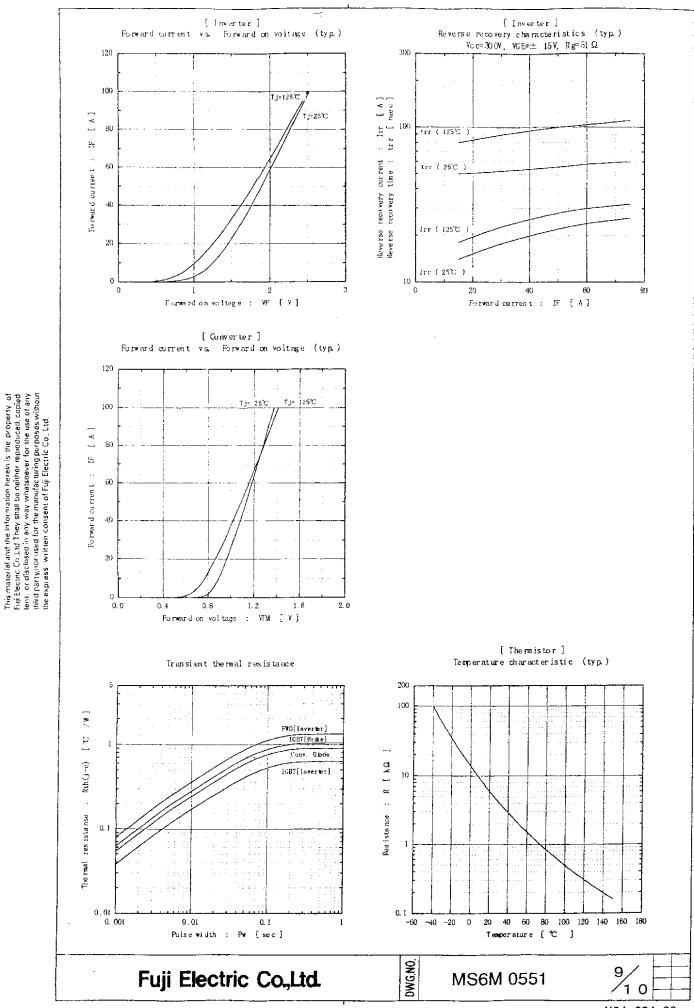
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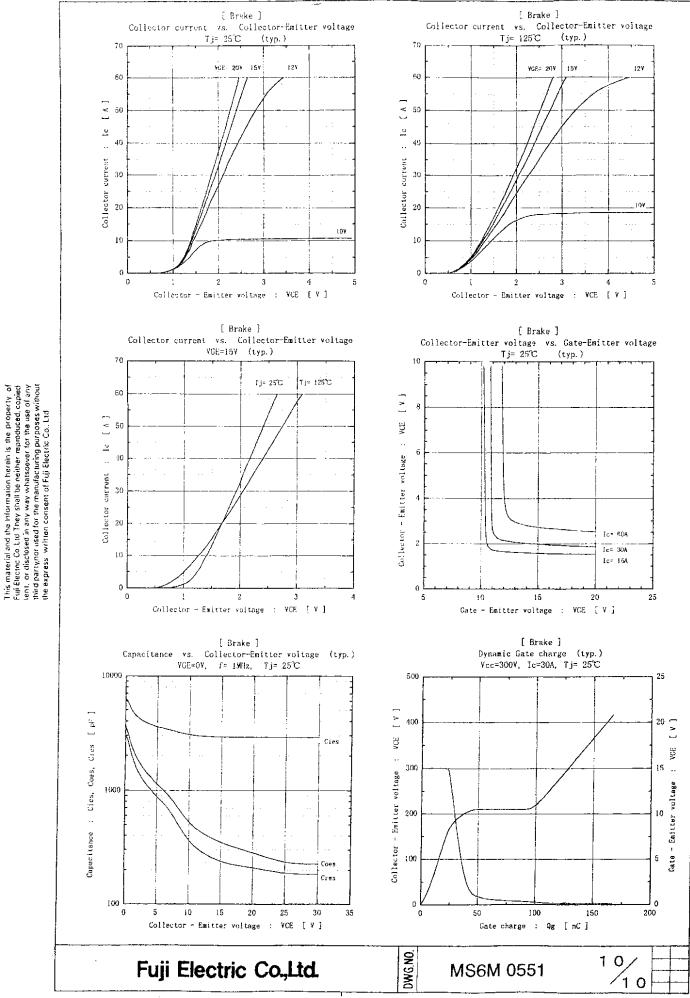


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