Plastic Power Transistors

NPN Silicon DPAK For Surface Mount Applications

Designed for high-gain audio amplifier applications.

Features

- Pb-Free Package is Available
- High DC Current Gain -

$$h_{FE} = 120 \text{ (Min)} @ I_C = 500 \text{ mA}$$

= 40 (Min) @ $I_C = 2 \text{ A}$

• Low Collector-Emitter Saturation Voltage -

$$V_{CE(sat)} = 0.3 \text{ Vdc (Max)} @ I_C = 1 \text{ A}$$

• High Current-Gain - Bandwidth Product -

$$f_T = 65 \text{ MHz (Min)} @ I_C = 100 \text{ mA}$$

• Epoxy Meets UL 94 V-0 @ 0.125 in

• ESD Ratings: Human Body Model, 3B > 8000 V Machine Model, C > 400 V

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Base Voltage	V _{CB}	Da 50 Shee	et4Mdccc
Collector–Emitter Voltage	V _{CEO}	50	Vdc
Emitter-Base Voltage	V _{EB}	5	Vdc
Collector Current Continuous Peak	I _C	2 3	Adc
Base Current	I _B	0.4	Adc
Total Device Dissipation @ T _C = 25°C Derate above 25°C	P _D	12.5 0.1	W W/°C
Total Device Dissipation @ T _A = 25°C* Derate above 25°C	P _D	1.4 0.011	W W/°C
Operating and Storage Junction Temperature Range	T _J , T _{stg}	-65 to +150	°C

Maximum ratings are those values beyond which device damage can occur. Maximum ratings applied to the device are individual stress limit values (not normal operating conditions) and are not valid simultaneously. If these limits are exceeded, device functional operation is not implied, damage may occur and reliability may be affected.

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance Junction-to-Case Junction-to-Ambient*	$R_{ heta JC} \ R_{ heta JA}$	10 89.3	°C/W

^{*}These ratings are applicable when surface mounted on the minimum pad sizes recommended



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SILICON **POWER TRANSISTORS** 2 AMPERES **50 VOLTS 12.5 WATTS**

MARKING **DIAGRAM**



DPAK CASE 369C



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= Year = Work Week

ORDERING INFORMATION

Device	Package	Shipping [†]
NJD2873T4	DPAK	2500 Units / Reel
NJD2873T4G	DPAK (Pb–Free)	2500 Units / Reel

[†]For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

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NJD2873T4/D

ELECTRICAL CHARACTERISTICS ($T_C = 25$ °C unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS		1	I	
Collector–Emitter Sustaining Voltage (Note 1) $(I_C = 10 \text{ mAdc}, I_B = 0)$	V _{CEO(sus)}	50	-	Vdc
Collector Cutoff Current (V _{CB} = 50 Vdc, I _E = 0)	I _{CBO}	-	100	nAdc
Emitter Cutoff Current (V _{BE} = 5 Vdc, I _C = 0)	I _{EBO}	-	100	nAdc
ON CHARACTERISTICS	1			
DC Current Gain (Note 1) (I _C = 0.5 A, V _{CE} = 2 V) (I _C = 2 Adc, V _{CE} = 2 Vdc)	h _{FE}	120 40	360 -	-
Collector–Emitter Saturation Voltage (Note 1) (I _C = 1 A, I _B = 0.05 A)	V _{CE(sat)}	_	0.3	Vdc
Base–Emitter Saturation Voltage (Note 1) (I _C = 1 A, I _B = 0.05 Adc)	V _{BE(sat)}	-	1.2	Vdc
Base–Emitter On Voltage (Note 1) (I _C = 1 Adc, V _{CE} = 2 Vdc)	V _{BE(on)}	-	1.2	Vdc
DYNAMIC CHARACTERISTICS	•			
Current–Gain – Bandwidth Product (Note 2) ($I_C = 100 \text{ mAdc}$, $V_{CE} = 10 \text{ Vdc}$, $f_{test} = 10 \text{ MHz}$)	f _T	65	_	MHz
Output Capacitance (V _{CB} = 10 Vdc, I _E = 0, f = 0.1 MHz) DataSheet4U.com	C _{ob}	_	80	pF

^{1.} Pulse Test: Pulse Width = 300 $\mu s,$ Duty Cycle \approx 2%. 2. $f_T=\left|h_{fe}\right| \bullet f_{test}.$

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TYPICAL CHARACTERISTICS

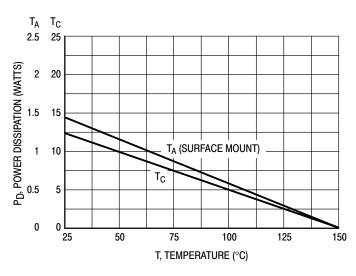


Figure 1. Power Derating

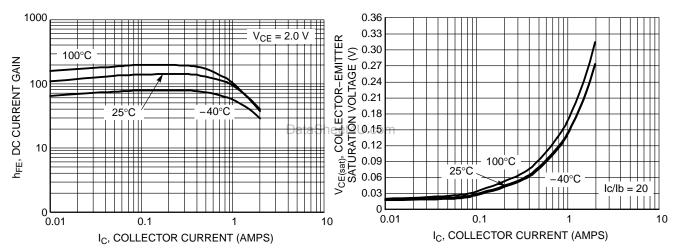


Figure 2. DC Current Gain

Figure 3. Collector-Emitter Saturation Voltage

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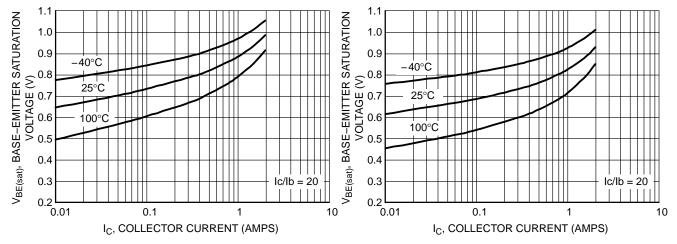


Figure 4. Base-Emitter Saturation Voltage

Figure 5. Base-Emitter Saturation Voltage

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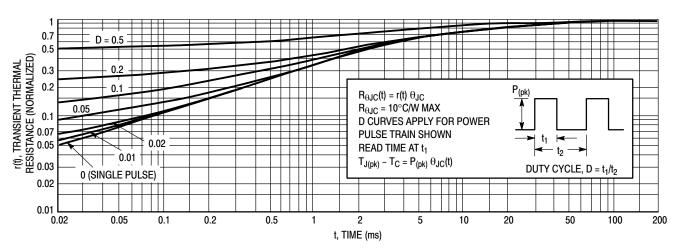


Figure 6. Thermal Response

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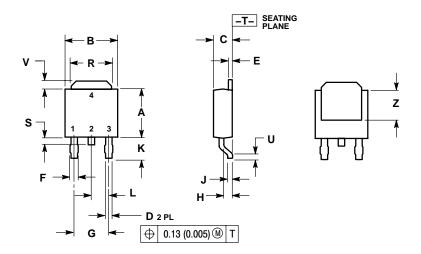
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PACKAGE DIMENSIONS

DPAK CASE 369C-01 **ISSUE O**



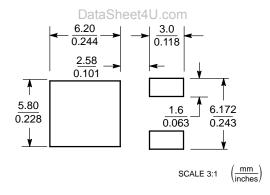
- NOTES: 1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INCHES		MILLIN	ETERS
DIM	MIN	MAX	MIN	MAX
Α	0.235	0.245	5.97	6.22
В	0.250	0.265	6.35	6.73
С	0.086	0.094	2.19	2.38
D	0.027	0.035	0.69	0.88
Е	0.018	0.023	0.46	0.58
F	0.037	0.045	0.94	1.14
G	0.180 BSC		4.58 BSC	
Н	0.034	0.040	0.87	1.01
J	0.018	0.023	0.46	0.58
K	0.102	0.114	2.60	2.89
L	0.090	BSC	2.29	BSC
R	0.180	0.215	4.57	5.45
S	0.025	0.040	0.63	1.01
U	0.020		0.51	
٧	0.035	0.050	0.89	1.27
Z	0.155		3.93	

- STYLE 1: PIN 1. BASE 2. COLLECTOR 3. EMITTER 4. COLLECTOR

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SOLDERING FOOTPRINT*



*For additional information on our Pb-Free strategy and soldering details, please download the ON Semiconductor Soldering and Mounting Techniques Reference Manual, SOLDERRM/D.

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