TOSHIBA Field Effect Transistor Silicon N Channel MOS Type

HN1K04FU

High Speed Switching Applications

Analog Switch Applications

- High input impedance and extremely low drive current.
- + Vth is low and it is possible to drive directly at low-voltage CMOS. : Vth = 0.8 to 2.5 V
- Switching speed is fast.
- Suitable for high-density mounting because of a compact package.

Maximum Ratings (Ta = 25°C) (Q1, Q2 common)

Characteristics	Symbol	Rating	Unit
Drain-source voltage	V _{DS}	50	V
Gate-source voltage	V _{GSS}	10	V
DC drain current	۱ _D	50	mA
Drain power dissipation	P _D (Note)	200	mW
Channel temperature	T _{ch}	150	°C
Storage temperature range	T _{stg}	-55 to 150	°C

Electrical Characteristics (Ta = 25°C) (Q1, Q2 common)



Note: TOTAL rating

Weight: 6.8 mg

Symbol **Test Condition** Characteristic Min Typ. Max Unit Gate leakage current $V_{GS} = 10 V, V_{DS} = 0 V$ 1 μA IGSS $I_D=100~\mu A,~V_{GS}=0~V$ 50 V Drain-source breakdown voltage V (BR) DSS ____ ____ $V_{DS} = 50V, V_{GS} = 0V$ Drain cut-off current IDSS 1 μA Gate threshold voltage $V_{DS} = 5V, I_D = 0.1 \text{ mA}$ 0.8 V Vth 25 Forward transfer admittance |Y_{fs}| $V_{DS} = 5V, I_D = 10 \text{ mA}$ 20 mS ____ ____ $I_D = 10 \text{ mA}, V_{GS} = 4.0 \text{ V}$ 20 Drain-source ON resistance R_{DS} (ON) 50 Ω Input capacitance Ciss $V_{DS} = 5 \text{ V}, \text{ V}_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$ 6.3 pF ____ _ Reverse transfer capacitance Crss $V_{DS} = 5 V$, $V_{GS} = 0 V$, f = 1 MHz1.3 pF Output capacitance Coss V_{DS} = 5 V, V_{GS}=0 V, f = 1 MHz 5.7 pF _ $V_{DD} = 5 V, I_D = 10 mA,$ 0.11 ton V_{GS} = 0 to 4.0 V Switching time μS $V_{DD} = 5 V, I_D = 10 mA,$ 0.15 toff $V_{GS} = 0$ to 4.0 V

Unit: mm

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Equivalent Circuit (top view)





Marking

(Q1, Q2 common)

Switching Time Test Circuit

(a) Test circuit



 $\begin{array}{l} V_{DD}=5 \ V\\ D.U. \leq 1\%\\ V_{IN}: \ t_r, \ t_f < 5 \ ns\\ (Z_{out}=50 \ \Omega)\\ Common \ Source\\ Ta=25^{\circ}C \end{array}$



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(Q1, Q2 common)













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(Q1, Q2 common)







*: TOTAL rating

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