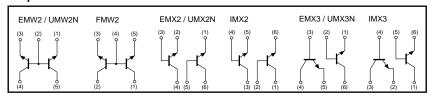
General purpose (dual transistors)

EMW2 / EMX2 / EMX3 / UMW2N / UMX2N / UMX3N / FMW2 / IMX2 / IMX3

Features

1) Two 2SC2412AK chips in a EMT or UMT or SMT package.

Equivalent circuit



● Absolute maximum ratings (Ta = 25°C)

	Parameter	Symbol	Limits	Unit	
Collector-base voltage			Vсво 60		
Collector-emitter voltage			50	V	
Emitter-base voltage			7	V	
Collector current			150	mA	
Collector power dissipation	EMW2 / EMX2 / EMX3 / UMW2N / UMX2N / UMX3N	D-	150(TOTAL)	*1 mW *2	
	FMW2 / IMX2 / IMX3	Pc	300(TOTAL)		
Junction temperature			150	°C	
Storage temperature		Tstg	-55~+150	°C	

^{*1 120}mW per element must not be exceeded *2 200mW per element must not be exceeded

● Electrical characteristics (Ta = 25°C)

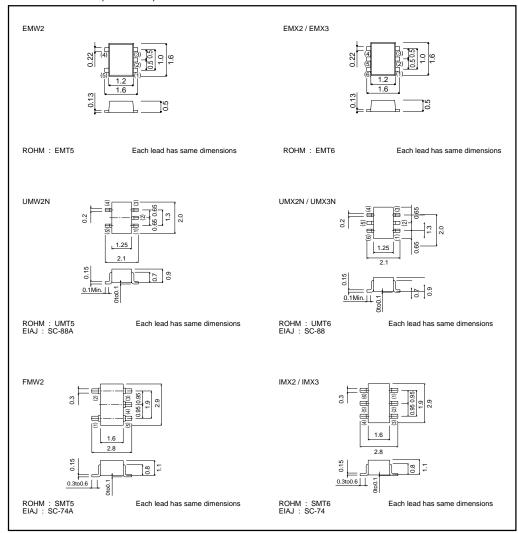
Parameter	Symbol	Min.	Тур.	Max.	Unit	Conditions
Collector-base breakdown voltage	ВУсво	60	-	-	V	Ic=50μA
Collector-emitter breakdown voltage	BVceo	50	-	-	V	Ic=1mA
Emitter-base breakdown voltage	ВУЕВО	7	-	-	V	Iε=50μA
Collector cutoff current	Ісво	-	-	0.1	μА	Vcb=60V
Emitter cutoff current	IEBO	-	-	0.1	μА	V _{EB} =7V
Collector-emitter saturation voltage	VcE(sat)	-	-	0.4	V	Ic/I _B =50mA/5mA
DC current transfer ratio	hfE	120	-	560	-	VcE=6V, Ic=1mA
Transition frequency	f⊤	-	180	-	MHz	Vce=12V, Ie=-2mA, f=100MHz *
Output capacitance	Cob	-	2	3.5	pF	VcB=12V, IE=0mA, f=1kHz
*Transition frequency of the device.						

Package, marking, and packaging specifications

<u> </u>	-								
Туре	EMW2	EMX2	EMX3	UMW2N	UMX2N	UMX3N	FMW2	IMX2	IMX3
Package	EMT5	EMT6	EMT6	UMT5	UMT6	UMT6	SMT5	SMT6	SMT6
Marking	W2	X2	Х3	W2	X2	Х3	W2	X2	Х3
Code	T2R	T2R	T2R	TR	TR	TR	T148	T108	T108
Basic ordering unit (pieces)	8000	8000	8000	3000	3000	3000	3000	3000	3000



●External dimensions (Units : mm)



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