No.4721

unit

PNP Epitaxial Planar Silicon Transistor

Muting Circuits, Driver Applications

## Features

- · On-chip bias resistors (R1 =  $47k\Omega$ , R2 =  $47k\Omega$ ).
- · Very small-sized package making 2SA1866-applied sets small and slim.
- · Small ON resistance.
- · High gain-bandwidth product fr.

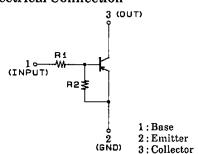
| Absolute Maximum Ratings at Ta = 25°C           |                               |                                |               | unit |
|---|-------------------------------|--------------------------------|---------------|------|
| Collector-to-Base Voltage                       | $V_{CBO}$                     |                                | -15           | V    |
| Collector-to-Emitter Voltage                    | $V_{CEO}$                     |                                | -15           | V    |
| Emitter-to-Base Voltage                         | $V_{EBO}$                     |                                | -10           | v    |
| Input Voltage                                   | $V_{IN}$                      |                                | -14           | v    |
| Collector Current                               | $I_{\mathbf{C}}$              |                                | -50           | mA   |
| Collector Current (Pulse)                       | $\widetilde{\mathrm{I_{CP}}}$ |                                | -100          | mA   |
| Base Current                                    | $I_{B}$                       |                                | -10           | mA   |
| Collector Dissipation                           | $\tilde{P_C}$                 |                                | 150           | mW   |
| Junction Temperature                            | Tj                            |                                | 150           | °C   |
| Storage Temperature                             | Tstg                          |                                | -55  to  +150 | °C   |
| Electrical Characteristics at Ta = 25°C min typ |                               |                                |               | max  |
| Collector Cutoff Current                        | $I_{CBO}$                     | $V_{CB} = -10 V_1 I_E = 0$     | JP            | -0.1 |
| Collector Cutoff Current                        | $I_{CEO}$                     | $V_{CE} = -10V I_{B} = 0$      |               | -0.5 |
| Emitter Cutoff Current                          | IEBO                          | $V_{EB} = -5V, I_{C} = 0$      | -30 -53       |      |
| DC Current Gain                                 | $h_{FE}$                      | $V_{CE} = -2V_{A}I_{C} = -5mA$ | 100           |      |
| Gain-Bandwidth Product                          | $f_T \overset{\sim}{\times}$  | $V_{CE} = -5V, I_{C} = -10mA$  | 600           |      |

 $\mu A$  $\mu$ A  $\mu$ A MHz  $V_{CB} = -10V, f = 1MHz$   $I_{C} = -2mA, I_{B} = -0.2mA$ **Output Capacitance** Cob\* 0.9 рF V<sub>CE(sat)</sub> C-E Saturation Voltage -20-60mV C-B Breakdown Voltage  $V_{(BR)CBO} I_C = -10 \mu A, I_E = 0$ -15V  $V_{(BR)CEO}$   $I_C = -1mA_*R_{BE} = \infty$ C-E Breakdown Voltage -15V  $V_{CE} = -2V, I_{C} = -100 \mu A$   $V_{CE} = -0.3V, I_{C} = -5 mA$ Input OFF-State Voltage  $V_{IN(off)}$ -0.8 - 1.2 - 1.5V -1.0 -2.3Input ON-State Voltage  $V_{IN(on)}$ v -4.0Input Resistance R1 32 47 62  $\mathbf{k}\Omega$ Resistance Ratio R1/R2 0.9 1.0 1.1 ON Resistance Ron  $V_{IN} = -10V, f = 1MHz$ 10.0 Ω

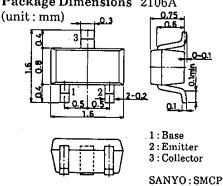
\* : Characteristic of the constituent transistor.

## Marking: CA

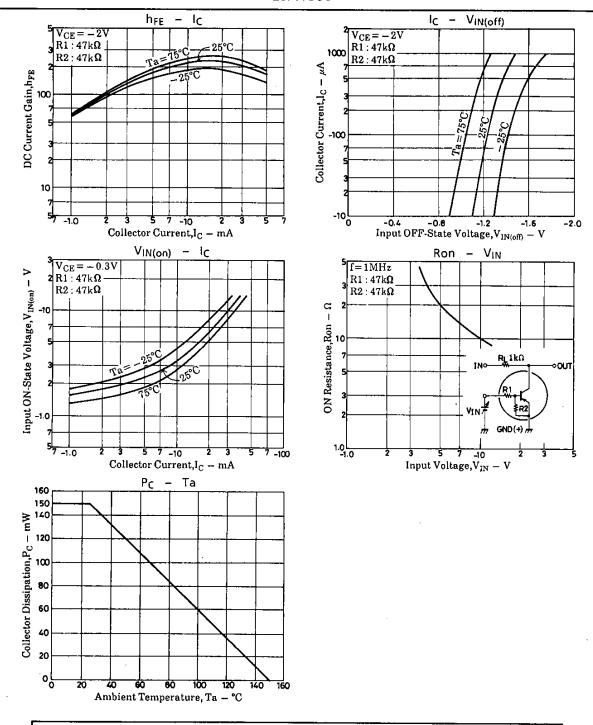
## **Electrical Connection**



## Package Dimensions 2106A



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