

No.3686A

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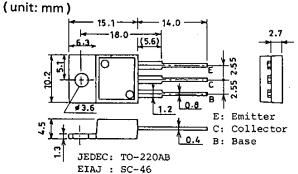
PNP Triple Diffused Planar Silicon Transistor High-Voltage Amp, High-Voltage Switching

Features

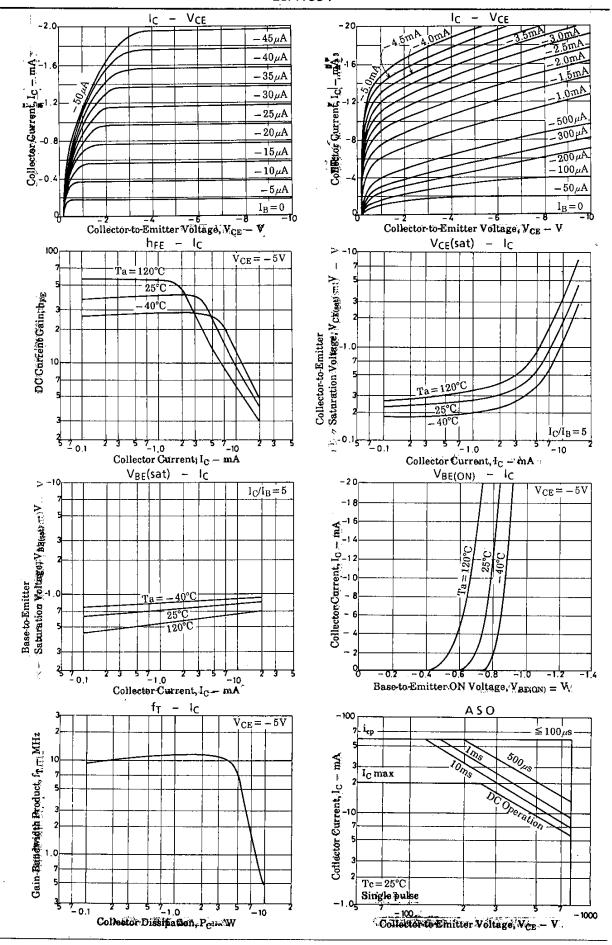
- · High breakdown voltage ($V_{CEO} \min = -800V$).
- · Small c_{ob} (c_{ob} typ=1.6pF).
- · High reliability (Adoption of HVP processes).

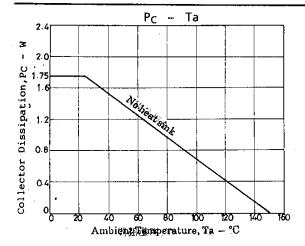
Absolute Maximum Ratings at Ta = 25°C					unit	
Collector-to-Base Voltage	V_{CBO}		-	-800	V	
Collector-to-Emitter Voltage	V_{CEO}			-800	V	
Emitter-to-Base Voltage	V_{EBO}			-7	$\mathbf{v}^{-\epsilon}$	
Collector Current	$I_{\mathbf{C}}$			-20	mA	
Peak Collector Current	i _{cp}			-60	mA	•
Collector Dissipation	\tilde{P}_{C}^{p}			1.75	W	
Junction Temperature	Tj			150	$^{\circ}\mathrm{C}$	
Storage Temperature	Tstg		-55 to +150		°C	
Electrical Characteristics at T	Γa=25°C		min	typ	max	unit
Collector Cutoff Current	I_{CBO}	$V_{CB} = -800V, I_E = 0$		7.	1	μ A
Emitter Cutoff Current	I _{EBO}	$V_{EB} = -5V_{IC} = 0$			-1	μA
DC Current Gain	h_{FE}	$V_{CE} = -5V_{IC} = -2mA$	20		50	•
Gain-Bandwidth Product	f_{T}	$V_{CE} = -10V, I_{C} = -2mA$		10		MHz
Output Capacitance	$\mathbf{c_{ob}}$	$V_{CB} = -100V, f = 1MHz$		1.6		рF
C-E Saturation Voltage	V _{CE(sat)}	$I_{C} = -1 \text{mA}, I_{B} = -200 \mu \text{A}$			-1	v
B-E Saturation Voltage	V _{BE(sat)}	$I_C = -1 \text{mA}, I_B = -200 \mu \text{A}$			-1.5	V
C-B Breakdown Voltage	V _{(BR)CBO}	$I_{\rm C} = -100 \mu {\rm A}, I_{\rm E} = 0$	-800			v
C-E Breakdown Voltage	V _{(BR)CEO}	$I_C = -1 \text{mA}, R_{BE} = \infty$	-800			v
E-B Breakdown Voltage	V _{(BR)EBO}	$I_{\rm E} = -100 \mu A, \overline{I_{\rm C}} = 0$	-7			v
Thermal Resistance	$R_{th(j-c)}$	Junction - Case			8.3	°C/w

Package Dimensions 2010B



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