

Silicon PNP Power Transistors

2SA1789

DESCRIPTION

- With TO-247 package
- Complement to type 2SC4653
- Low collector saturation voltage

APPLICATIONS

- For audio output applications

PINNING

PIN	DESCRIPTION
1	Base
2	Collector; connected to mounting base
3	Emitter

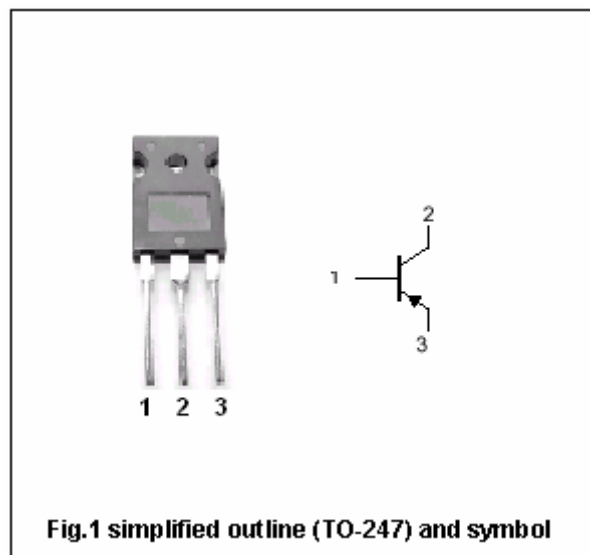


Fig.1 simplified outline (TO-247) and symbol

Absolute maximum ratings($T_c=25^\circ\text{C}$)

SYMBOL	PARAMETER	CONDITIONS	VALUE	UNIT
V_{CBO}	Collector-base voltage	Open emitter	-60	V
V_{CEO}	Collector-emitter voltage	Open base	-60	V
V_{EBO}	Emitter-base voltage	Open collector	-5	V
I_C	Collector current		-12	A
P_C	Collector power dissipation	$T_c=25^\circ\text{C}$	80	W
T_j	Junction temperature		150	$^\circ\text{C}$
T_{stg}	Storage temperature		-55~150	$^\circ\text{C}$

Silicon PNP Power Transistors

2SA1789

CHARACTERISTICS

T_j=25°C unless otherwise specified

SYMBOL	PARAMETER	CONDITIONS	MIN	TYP.	MAX	UNIT
V _{(BR)CEO}	Collector-emitter breakdown voltage	I _C =-25mA; I _B =0	-60			V
V _{(BR)CBO}	Collector-base breakdown voltage	I _C =-1mA; I _E =0	-60			V
V _{(BR)EBO}	Emitter-base breakdown voltage	I _E =-1mA; I _C =0	-5			V
V _{CEsat}	Collector-emitter saturation voltage	I _C =-6A; I _B =-0.6 A			-0.5	V
V _{BEsat}	Base-emitter saturation voltage	I _C =-6A; I _B =-0.6 A			-2.0	V
I _{CBO}	Collector cut-off current	V _{CB} =-60V; I _E =0			-10	μ A
I _{EBO}	Emitter cut-off current	V _{EB} =-5V; I _C =0			-10	μ A
h _{FE}	DC current gain	I _C =-2A ; V _{CE} =-2V	60		320	

◆ h_{FE} classifications

D	E	F
60-120	100-200	160-320

Silicon PNP Power Transistors

2SA1789

PACKAGE OUTLINE

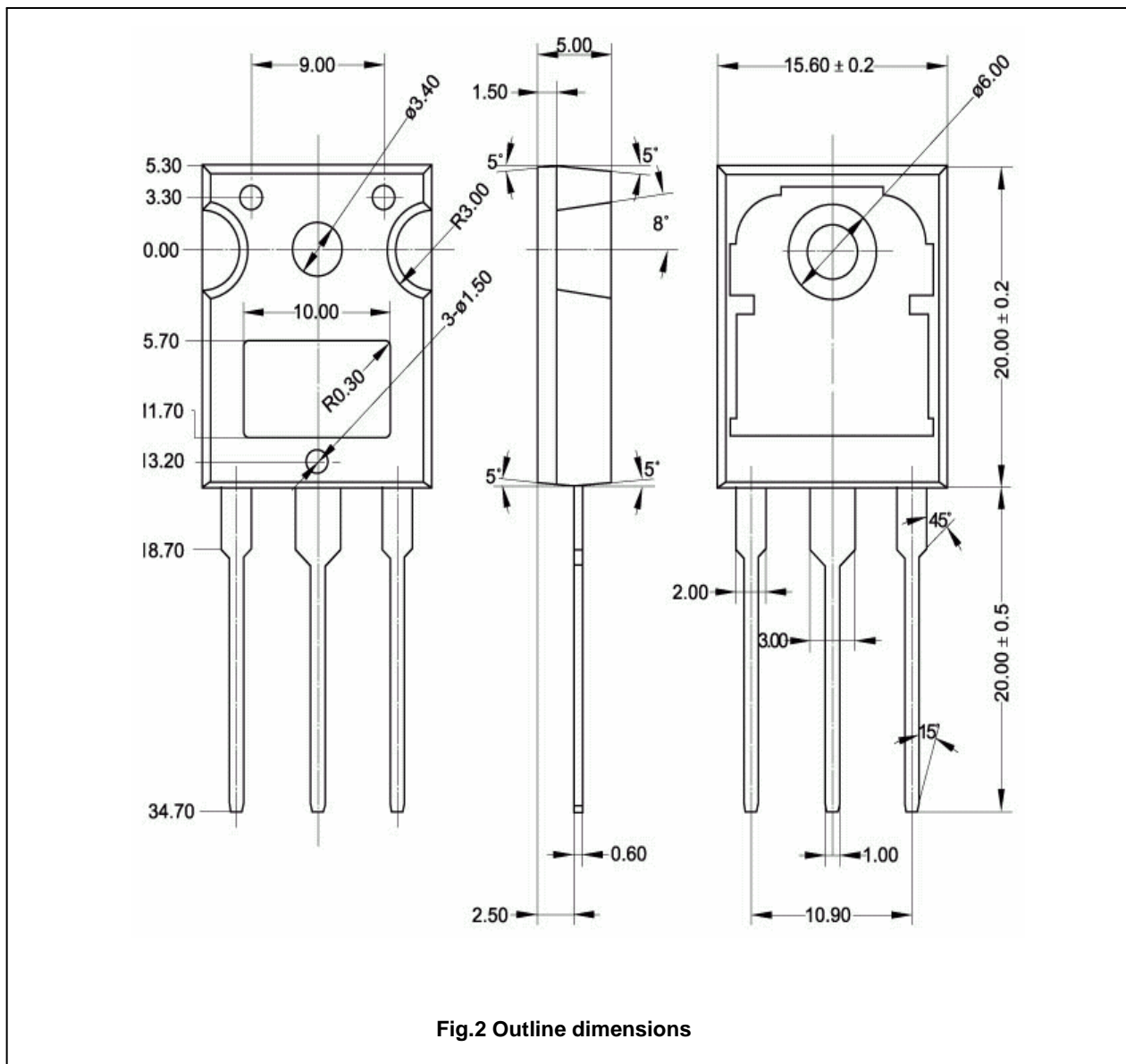


Fig.2 Outline dimensions