MIC923

410MHz Low-Power SC70 Op Amp

Final Information

General Description

The MIC923 is a high-speed operational amplifier with a gain-bandwidth product of 410MHz. The part is unity gain stable. It has a very low 2.5mA supply current, and features the *Teeny*™ SC70 package.

Supply voltage range is from ±2.5V to ±9V, allowing the MIC923 to be used in low-voltage circuits or applications requiring large dynamic range.

The MIC923 requires a minimum gain of +2 or –1 but is stable driving any capacitive load. It has excellent PSRR and CMRR, making it much easier to use than most conventional high-speed devices. Low supply voltage, low power consumption, and small packaging make the MIC923 ideal for portable equipment. The ability to drive capacitative loads also makes it possible to drive long coaxial cables.

Features

- 410MHz gain bandwidth product
- 2.5mA supply current
- *Teeny*™ SC70 packaging
- 2200V/µs slew rate
- Drives any capacitive load
- Stable with gain ≥2 or –1

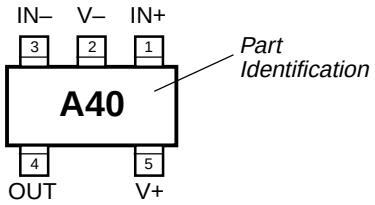
Applications

- Video
- Imaging
- Ultrasound
- Portable equipment
- Line drivers

Ordering Information

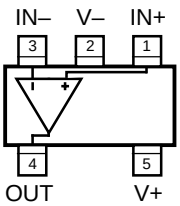
Part Number	Junction Temp. Range	Package
MIC923BC5	–40°C to +85°C	SC-70

Pin Configuration



SC-70

Functional Pinout



SC-70

Pin Description

Pin Number	Pin Name	Pin Function
1	IN+	Noninverting Input
2	V–	Negative Supply (Input)
3	IN–	Inverting Input
4	OUT	Output: Amplifier Output
5	V+	Positive Supply (Input)

Downloaded from Elcodis.com electronic components distributor

Absolute Maximum Ratings (Note 1)

Supply Voltage ($V_{V+} - V_{V-}$) 20V
Differential Input Voltage ($|V_{IN+} - V_{IN-}|$) ..... 4V, **Note 3**
Input Common-Mode Range (V_{IN+}, V_{IN-}) ..... V_{V+} to V_{V-}
Lead Temperature (soldering, 5 sec.) ..... 260°C
Storage Temperature (T_S) ..... 150°C
ESD Rating, **Note 4** 1.5kV

Operating Ratings (Note 2)

Supply Voltage (V_S) ..... ±2.5V to ±9V
Junction Temperature (T_J) ..... –40°C to +85°C
Package Thermal Resistance
SC70-5 (θ_{JA}) ..... 450°C/W

Electrical Characteristics (±5V)

V+ = +5V, V– = –5V, V_{CM} = 0V, R_L = 10MΩ; T_J = 25°C, **bold** values indicate –40°C ≤ T_J ≤ +85°C; unless noted.

Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{OS}	Input Offset Voltage		**-5**	0.8	**5**	mV
V_{OS}	V_{OS} Temperature Coefficient			15		µV/°C
I_B	Input Bias Current			1.7	**4.5**	µA
I_{OS}	Input Offset Current		**-2**	0.3	**2**	µA
V_{CM}	Input Common-Mode Range		**–3.25**		**+3.25**	V
CMRR	Common-Mode Rejection Ratio	–2.5V < V_{CM} < +2.5V	75	80		dB
PSRR	Power Supply Rejection Ratio	±3.5V < V_S < ±9V	**68**	87		dB
A_{VOL}	Large-Signal Voltage Gain	R_L = 2kΩ, V_{OUT} = ±2V	**65**	74		dB
		R_L = 100Ω, V_{OUT} = ±1V		77		dB
V_{OUT}	Maximum Output Voltage Swing	positive, R_L = 2kΩ	**+3**	3.6		V
		negative, R_L = 2kΩ		–3.6	**–3**	V
		positive, R_L = 100Ω	**+2.7**	3.0		V
		negative, R_L = 100Ω, **Note 5**		–2.6	**–2.3**	V
GBW	Gain-Bandwidth Product	C_L = 1.7pF		320		MHz
SR	Slew Rate	C=1.7pF, Av =2, R_L = 1MΩ, R_F = 2kΩ negative SR = 720V/µs		970		V/µs
I_{SC}	Short-Circuit Output Current	source	**65**	78		mA
		sink	**40**	47		mA
I_S	Supply Current	No Load		2.5	**3**	mA
	Input Voltage Noise	f = 10kHz		9		nV/√Hz
	Input Current Noise	f = 10kHz		1.1		pA/√Hz

Electrical Characteristics

V+ = +9V, V– = –9V, V_{CM} = 0V, R_L = 10MΩ; T_J = 25°C, **bold** values indicate –40°C ≤ T_J ≤ +85°C; unless noted

Symbol	Parameter	Condition	Min	Typ	Max	Units
V_{OS}	Input Offset Voltage		**-5**	0.4	**5**	mV
V_{OS}	Input Offset Voltage Temperature Coefficient			15		µV/°C
I_B	Input Bias Current			1.7	**4.5**	µA
I_{OS}	Input Offset Current			0.3	**2**	µA
V_{CM}	Input Common-Mode Range		**–7.25**		**+7.25**	V
CMRR	Common-Mode Rejection Ratio	–6.5V < V_{CM} < +6.5V	**58**	83		dB
PSRR	Power Supply Rejection Ratio	±3.5V < V_S < ±9V	**68**	87		dB

Downloaded from Elcodis.com electronic components distributor

Symbol	Parameter	Condition	Min	Typ	Max	Units
A_{VOL}	Large-Signal Voltage Gain	R_L = 2kΩ, V_{OUT} = ±3V	**65**	76		dB
		R_L = 100Ω, V_{OUT} = ±1V		86		dB
V_{OUT}	Maximum Output Voltage Swing	positive, R_L = 2kΩ	**7**	7.5		V
		negative, R_L = 2kΩ		–7.5	**–7**	V
GBW	Gain-Bandwidth Product	C_L = 1.7pF, R_L = 100Ω		410		MHz
SR	Slew Rate	C=1.7pF, Av =2, R_L = 1MΩ, R_F = 2kΩ positive SR = 2100V/µs		2200		V/µs
I_{SC}	Short-Circuit Output Current	source	**70**	84		mA
		sink	**40**	50		mA
I_S	Supply Current	No Load		2.5	**3**	mA
	Input Voltage Noise	f = 10kHz		9		nV/√Hz
	Input Current Noise	f = 10kHz		1.1		pA/√Hz

Note 1. Exceeding the absolute maximum rating may damage the device.

Note 2. The device is not guaranteed to function outside its operating rating.

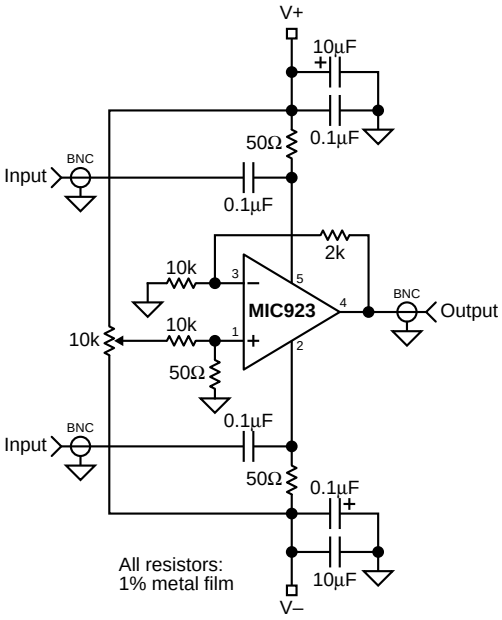
Note 3. Exceeding the maximum differential input voltage will damage the input stage and degrade performance (in particular, input bias current is likely to change).

Note 4. Devices are ESD sensitive. Handling precautions recommended. Human body model, 1.5k in series with 100pF.

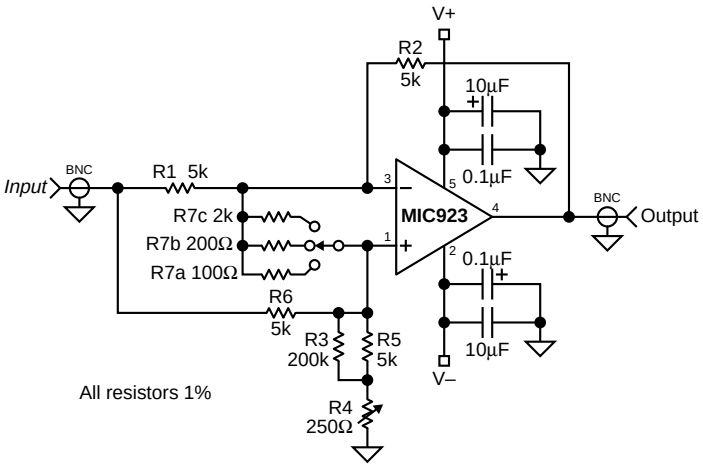
Note 5. Output swing limited by the maximum output sink capability, refer to the short-circuit current vs. temperature graph in “Typical Characteristics.”

Downloaded from Elcodis.com electronic components distributor

Test Circuits

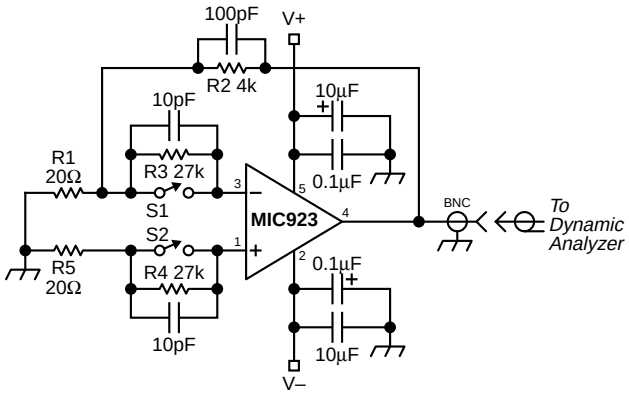


PSRR vs. Frequency

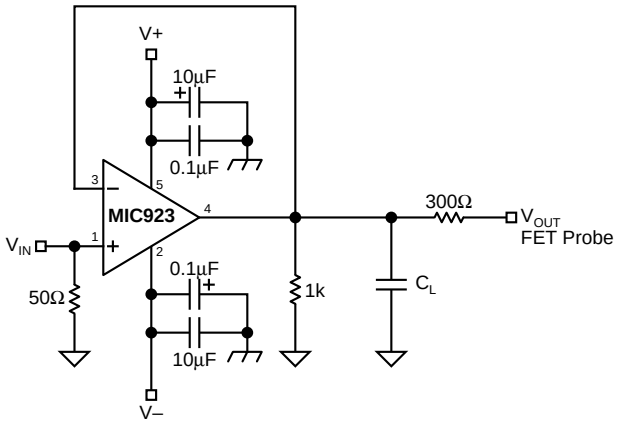


$$V_{OUT} = V_{ERROR}\left(1 + \frac{R2}{R1} + \frac{R2 + R5 + R4}{R7}\right)$$

CMRR vs. Frequency



Noise Measurement

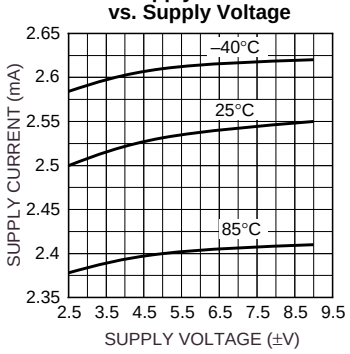


Closed Loop Frequency Response Measurement

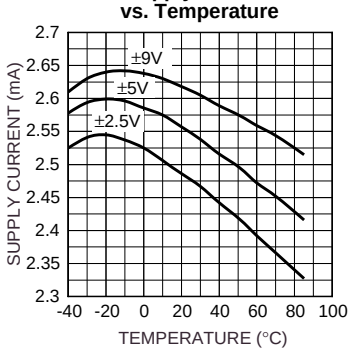
Downloaded from Elcodis.com electronic components distributor

Typical Characteristics

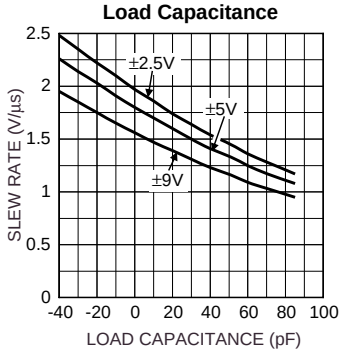
Supply Current vs. Supply Voltage



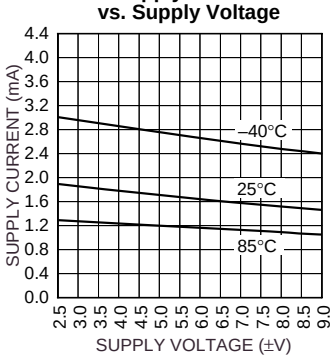
Supply Current vs. Temperature



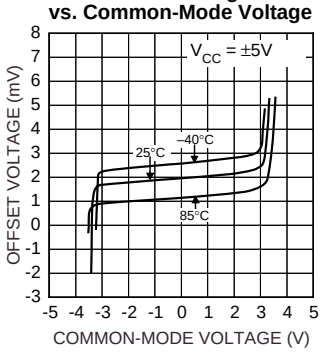
Positive Slew Rate vs. Load Capacitance



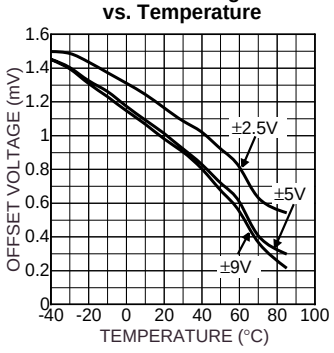
Supply Current vs. Supply Voltage



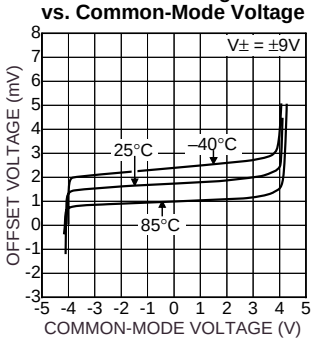
Offset Voltage vs. Common-Mode Voltage



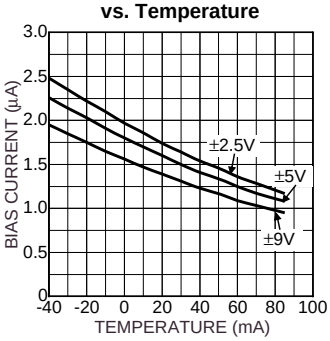
Offset Voltage vs. Temperature



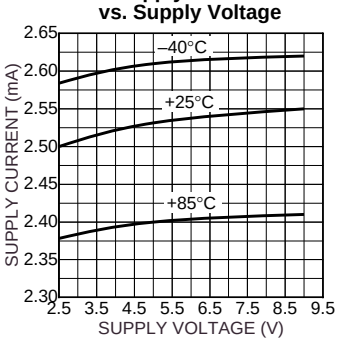
Offset Voltage vs. Common-Mode Voltage



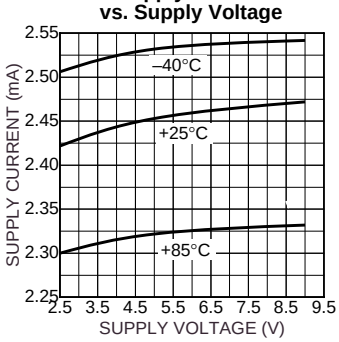
Bias Current vs. Temperature



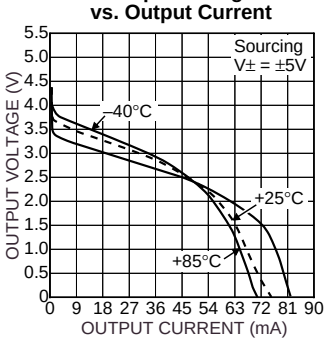
Supply Current vs. Supply Voltage



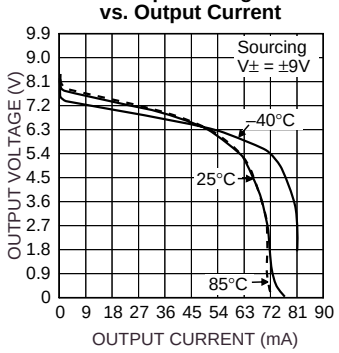
Supply Current vs. Supply Voltage



Output Voltage vs. Output Current

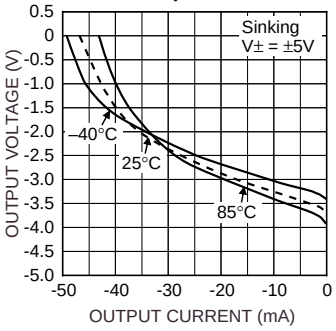


Output Voltage vs. Output Current

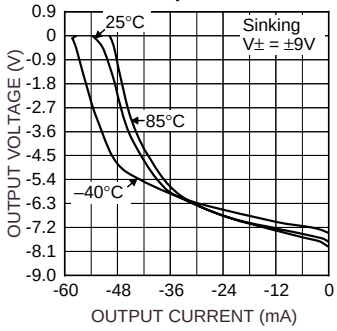


Downloaded from Elcodis.com electronic components distributor

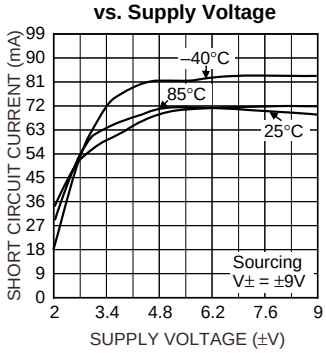
Output Voltage vs. Output Current



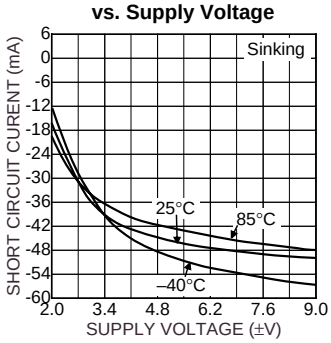
Output Voltage vs. Output Current



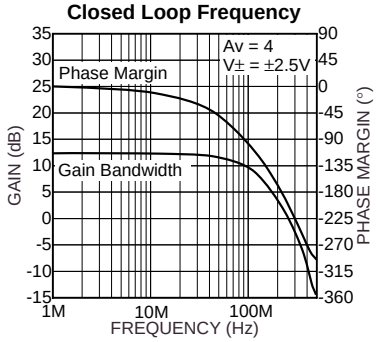
Short Circuit Current vs. Supply Voltage



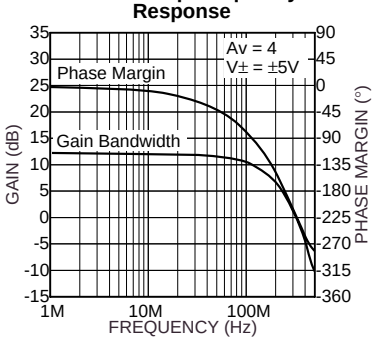
Short Circuit Current vs. Supply Voltage



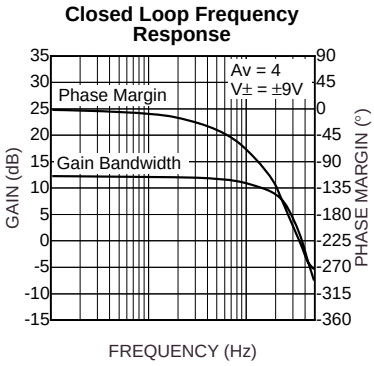
Closed Loop Frequency



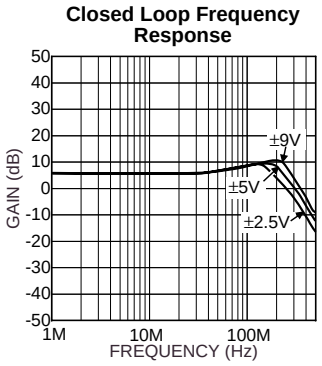
Closed Loop Frequency Response



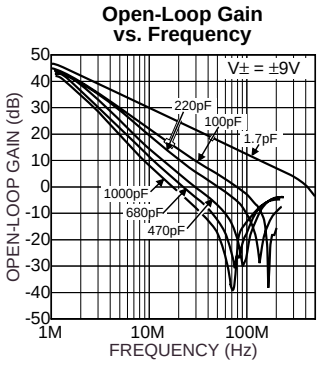
Closed Loop Frequency Response



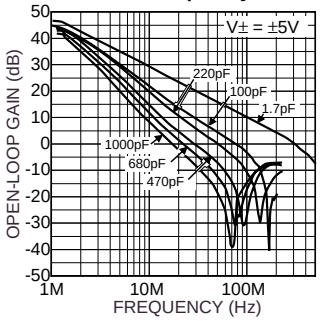
Closed Loop Frequency Response



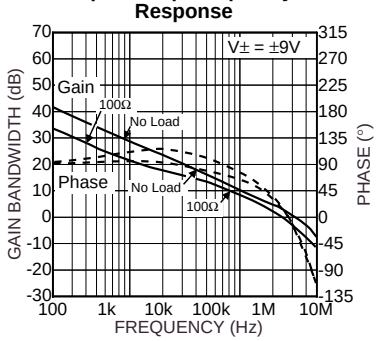
Open-Loop Gain vs. Frequency



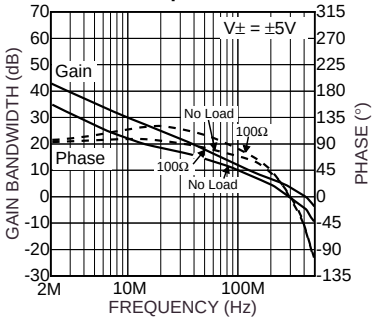
Open-Loop Gain vs. Frequency



Open-Loop Frequency Response



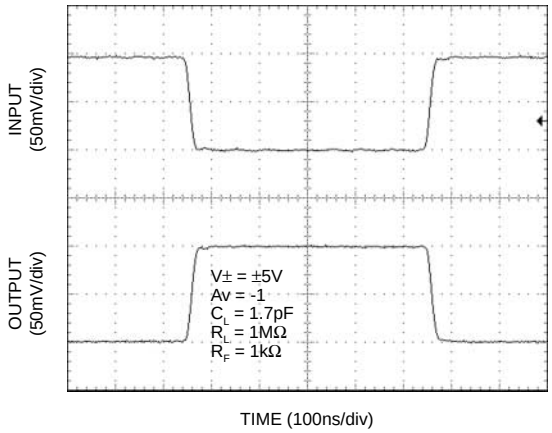
Open-Loop Frequency Response



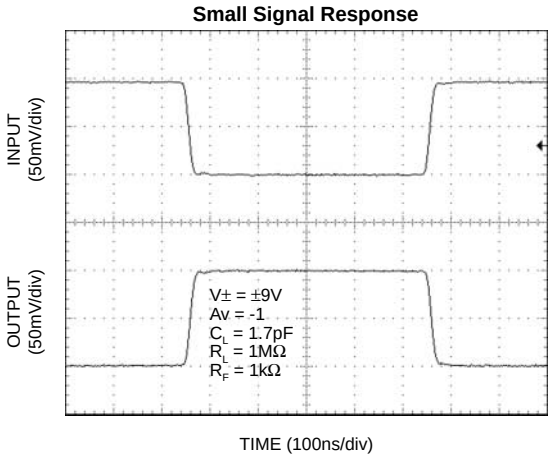
Downloaded from Elcodis.com electronic components distributor

Functional Characteristics

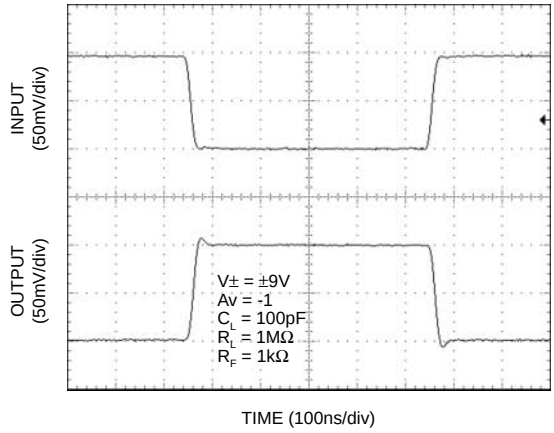
Small Signal Response



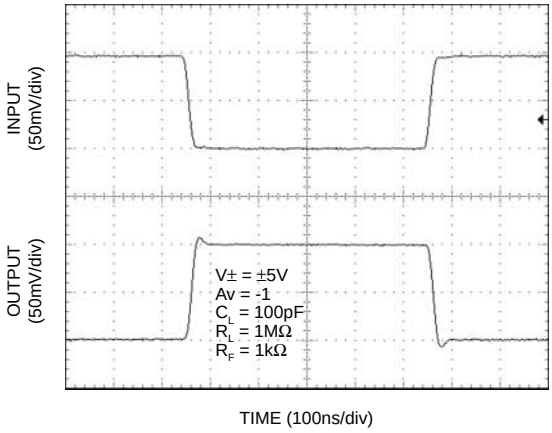
Small Signal Response



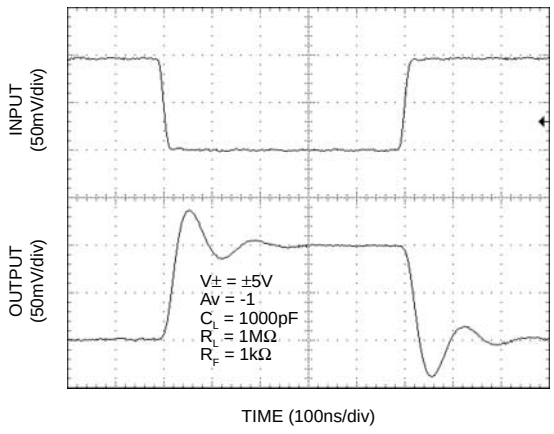
Small Signal Response



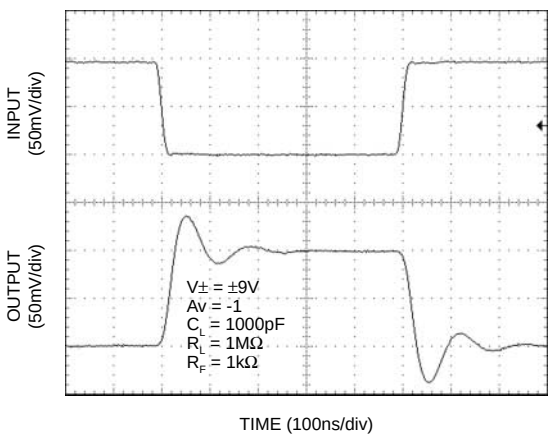
Small Signal Response



Small Signal Response

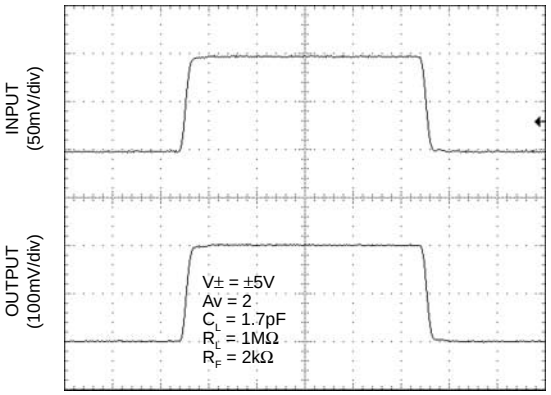


Small Signal Response



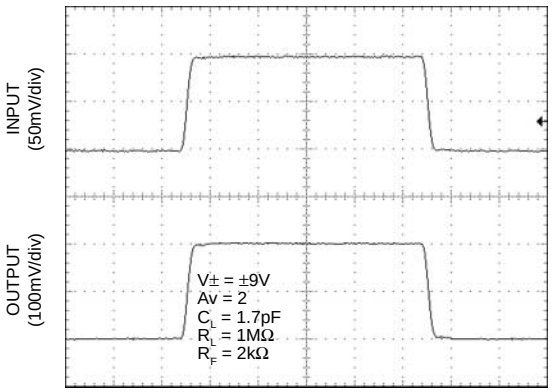
Downloaded from Elcodis.com electronic components distributor

Small Signal Response



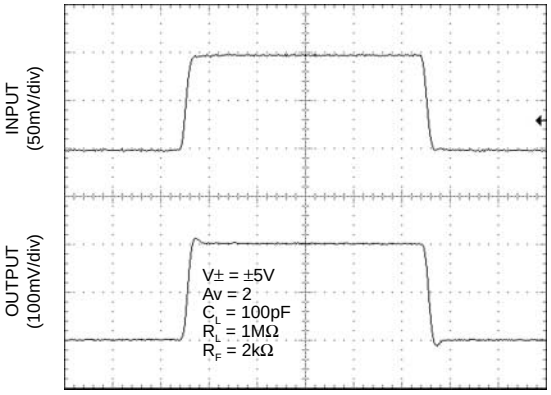
TIME (100ns/div)

Small Signal Response



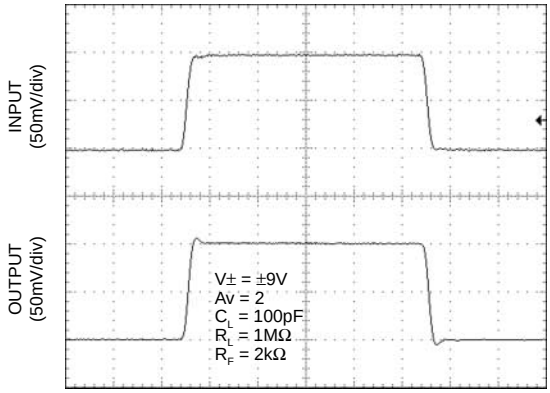
TIME (100ns/div)

Small Signal Response



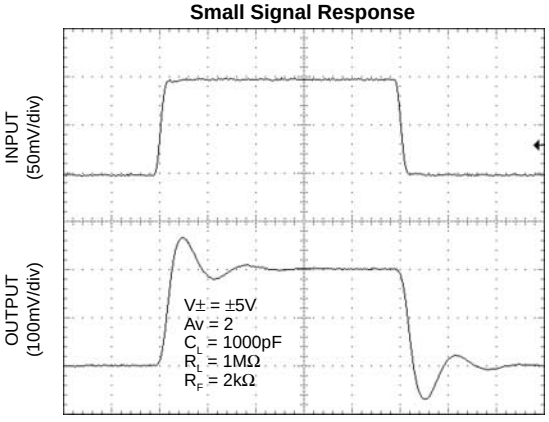
TIME (100ns/div)

Small Signal Response



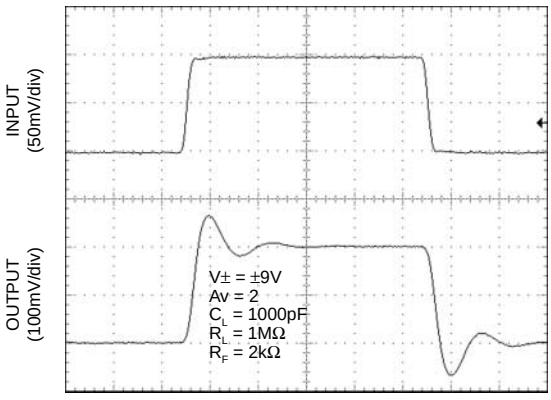
TIME (100ns/div)

Small Signal Response



TIME (100ns/div)

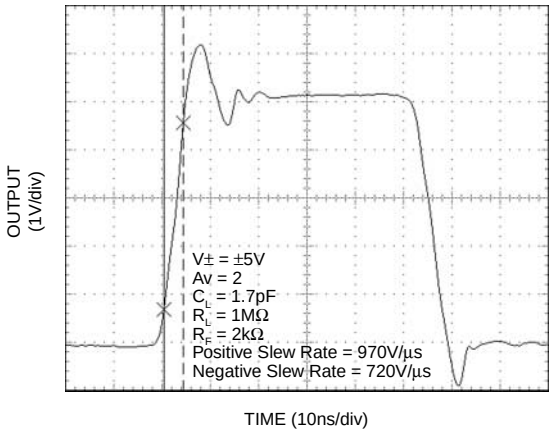
Small Signal Response



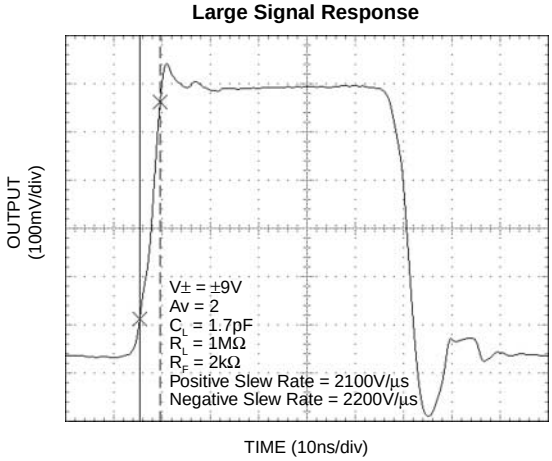
TIME (100ns/div)

Downloaded from Elcodis.com electronic components distributor

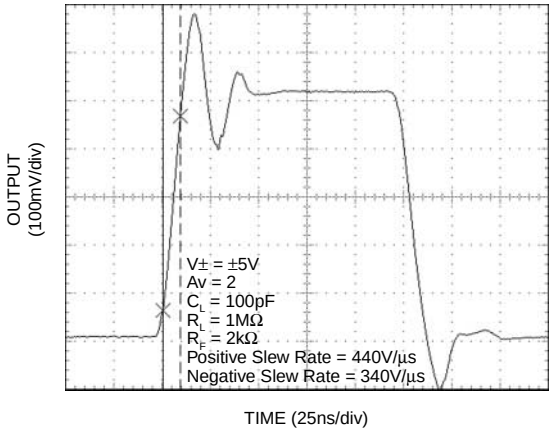
Large Signal Response



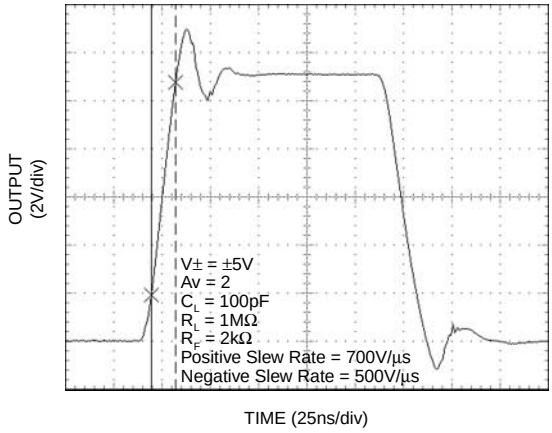
Large Signal Response



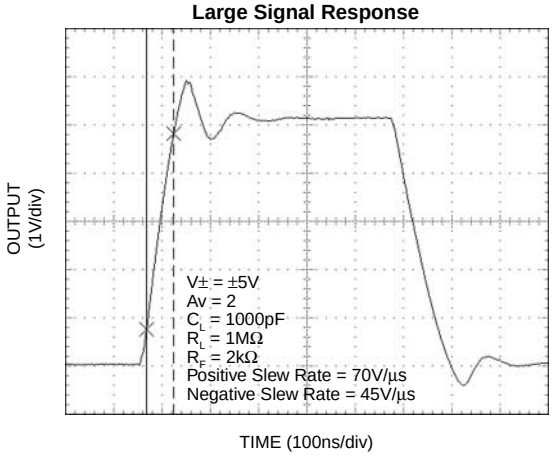
Large Signal Response



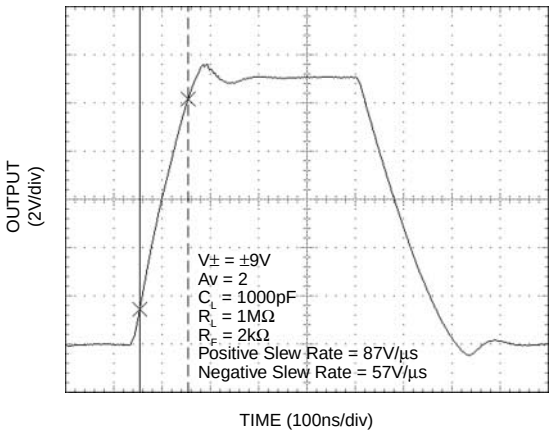
Large Signal Response



Large Signal Response



Large Signal Response



Downloaded from Elcodis.com electronic components distributor

Applications Information

The MIC923 is a high-speed, voltage-feedback operational amplifier featuring very low supply current and excellent stability. This device is unity gain stable, capable of driving high capacitance loads.

Driving High Capacitance

The MIC923 is stable when driving high capacitance, making it ideal for driving long coaxial cables or other high-capacitance loads. Most high-speed op amps are only able to drive limited capacitance.

> Note: increasing load capacitance does reduce the speed of the device. In applications where the load capacitance reduces the speed of the op amp to an unacceptable level, the effect of the load capacitance can be reduced by adding a small resistor (<100Ω) in series with the output.

Feedback Resistor Selection

Conventional op amp gain configurations and resistor selection apply, the MIC923 is NOT a current feedback device.

Also, for minimum peaking, the feedback resistor should have low parasitic capacitance, usually 470Ω is ideal. To use the part as a follower, the output should be connected to input via a short wire.

Layout Considerations

All high speed devices require careful PCB layout. The following guidelines should be observed: Capacitance, particularly on the two inputs pins will degrade performance; avoid large copper traces to the inputs. Keep the output signal away from the inputs and use a ground plane.

It is important to ensure adequate supply bypassing capacitors are located close to the device.

Power Supply Bypassing

Regular supply bypassing techniques are recommended. A 10µF capacitor in parallel with a 0.1µF capacitor on both the positive and negative supplies are ideal. For best performance all bypassing capacitors should be located as close to the op amp as possible and all capacitors should be low ESL (equivalent series inductance), ESR (equivalent series resistance). Surface-mount ceramic capacitors are ideal.

Thermal Considerations

The SC70-5 package, like all small packages, have a high thermal resistance. It is important to ensure the IC does not exceed the maximum operating junction (die) temperature of 85°C. The part can be operated up to the absolute maximum temperature rating of 125°C, but between 85°C and 125°C performance will degrade, in par-ticular CMRR will reduce.

An MIC923 with no load, dissipates power equal to the quiescent supply current × supply voltage

$$P_{D(no\,load)} = \left(V_{V+} - V_{V-}\right) I_S$$

When a load is added, the additional power is dissipated in the output stage of the op amp. The power dissipated in the device is a function of supply voltage, output voltage and output current.

$$P_{D(output\,stage)} = \left(V_{V+} - V_{OUT}\right) I_{OUT}$$

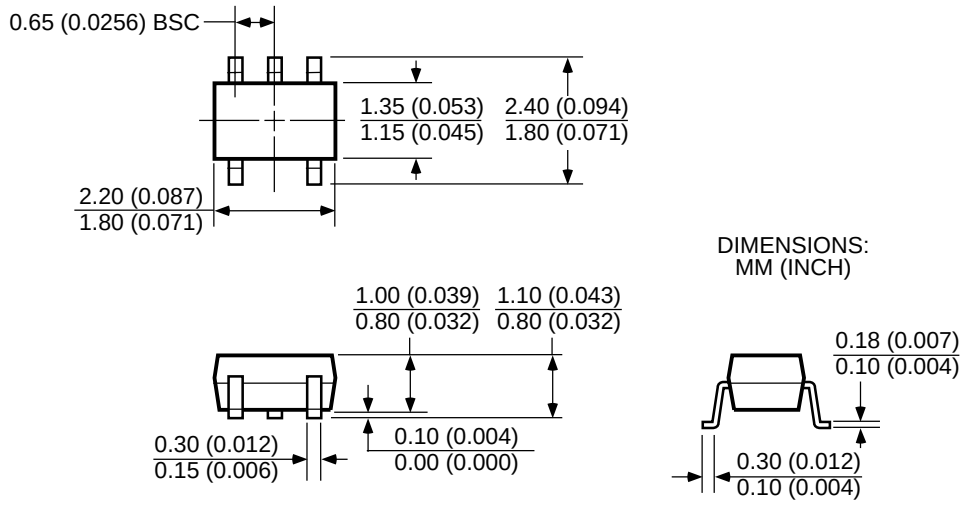
$$Total\ Power\ Dissipation = P_{D(no\,load)} + P_{D(output\,stage)}$$

Ensure the total power dissipated in the device is no greater than the thermal capacity of the package. The SC70-5 package has a thermal resistance of 450°C/W.

$$\text{Max. Allowable Power Dissipation} = \frac{T_{J(max)} - T_{A(max)}}{450°C/W}$$

Downloaded from Elcodis.com electronic components distributor

Package Information



SC-70 (C5)

MICREL INC. 1849 FORTUNE DRIVE SAN JOSE, CA 95131 USA

TEL + 1 (408) 944-0800 FAX + 1 (408) 944-0970 WEB http://www.micrel.com

This information is believed to be accurate and reliable, however no responsibility is assumed by Micrel for its use nor for any infringement of patents or other rights of third parties resulting from its use. No license is granted by implication or otherwise under any patent or patent right of Micrel Inc.

© 2002 Micrel Incorporated

Downloaded from Elcodis.com electronic components distributor