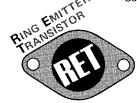


SILICON HIGH SPEED POWER TRANSISTOR

2SA 1078

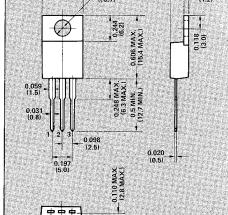
September 1979





JEDEC TO-220

OUTLINE DIMENSION 0,169 MAX (4.3 MAX.)



1: Base 2: Collector 3: Emitter Dimension in inches and (millimeters)

SILICON PNP RING EMITTER TRANSISTOR (RET)

The 2SA1078 is a silicon PNP general purpose, medium power transistor fabricated with Fujitsu's unique Ring Emitter Transistor (RET) technology. RET devices are constructed with multiple emitters connected through diffused ballast resistors which provide uniform current density. This structure permits the design of medium power transistors with exceptional frequency response in high current applications.

The 2SA1078 is especially well-suited for High frequency power amplifiers, Audio power amplifiers and drivers.

A NPN complement, 2SC2528, is available.

- High f_T = 140 MHz (typ)
- **Excellent Safe Operating Area**
- Improved reverse Second-Breakdown Capability
- **Excellent Current Gain Linearity**

ABSOLUTE MAXIMUM RATINGS

Rating	Symbol	Value	Únit
Collector to Base Voltage	V _{CBO}	120	V
Emitter to Base Voltage	V _{EBO}	5	٧ .
Collector to Emitter Voltage	V _{CEO}	120	٧
Collector Current	l _C	2	Α
Collector Power Dissipation (T _C = 25°C)	Pc	25	w
Junction Temperature	Тј	150	°C
Storage Temperature Range	T _{stg}	-65~+150	°C

ELECTRICAL CHARACTERISTICS $(T_a = 25^{\circ}C)$

Parameter Sys	S. comboni	Taux Caradiniana	Limits			
	Symbol	Test Conditions -	Min.	Тур.	Max.	- Unit
Collector Cutoff Current	СВО	V _{CB} = 120V, I _E = 0	_	_	1	μΑ
Emitter Cutoff Current	I _{EBO}	$V_{EB} = 5V$, $I_{C} = 0$	_	_	1	μΑ
Collector Cutoff Current	I _{CEO}	V _{CE} = 120V, I _B = 0			100	μΑ
Collector to Base Breakdown Voltage	V _{(BR)CBO}	$I_{C} = 1\mu A, I_{E} = 0$	120	_	_	V
Emitter to Base Breakdown Voltage	V _{(BR)EBO}	$I_{E} = 1\mu A, I_{C} = 0$	5		_	· V
Collector to Emitter Breakdown Voltage	V _{(BR)CEO}	I _C == 1mA, R _{BE} = ∞	120	. –	_	V
DC Current Gain	h _{FE1}	$V_{CE} = 5V$, $I_{C} = 0.3A*$	60	_	350	
DC Current Gain	h _{FE2}	$V_{CE} = 5V$, $I_{C} = 0.7A*$	50	_	_	
Collector to Emitter Saturation Voltage	V _{CE(sat)}	I _C = 0.7A, I _B = 0.07A*	_	0.45	1.0	V
Base to Emitter Voltage	V _{BE}	$V_{CE} = 5V$, $I_{C} = 0.7A*$	_	0.8	1.7	V
Gain-Bandwidth Product	f _T	V _{CE} =10V,1 _C =0.5A,f=10MHz	_	140		MHz
Output Capacitance	Cob	V _{CB} =20V,I _E =0,f=1MHz	_	100	_	pF

* Pulsed: Pulse Width ≤ 300µs Duty Cycle ≤ 6%