

DATA SHEET

BLF547 UHF push-pull power MOS transistor

Product specification

October 1992

UHF push-pull power MOS transistor

BLF547

FEATURES

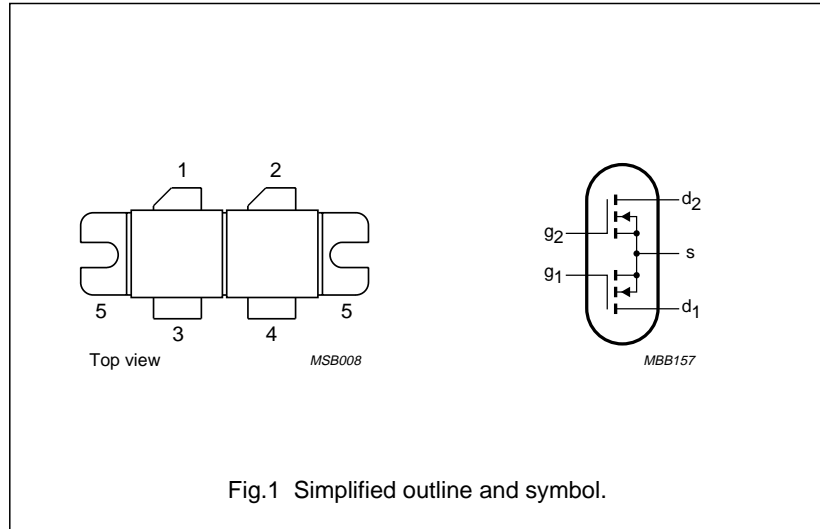
- High power gain
- Easy power control
- Good thermal stability
- Gold metallization ensures excellent reliability
- Designed for broadband operation.

DESCRIPTION

Dual push-pull silicon N-channel enhancement mode vertical D-MOS transistor designed for communications transmitter applications in the UHF frequency range.

The transistor is encapsulated in a 4-lead, SOT262A2 balanced flange envelope, with two ceramic caps. The mounting flange provides the common source connection for the transistors.

PIN CONFIGURATION



CAUTION

The device is supplied in an antistatic package. The gate-source input must be protected against static charge during transport and handling.

PINNING - SOT262A2

PIN	DESCRIPTION
1	drain 1
2	drain 2
3	gate 1
4	gate 2
5	source

WARNING

Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO discs are not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

QUICK REFERENCE DATA

RF performance at $T_n = 25\text{ °C}$ in a push-pull common-source test circuit.

MODE OF OPERATION	f (MHz)	V _{DS} (V)	P _L (W)	G _p (dB)	η _D (%)
CW, class-B	500	28	100	> 10	> 50

UHF push-pull power MOS transistor

BLF547

LIMITING VALUES

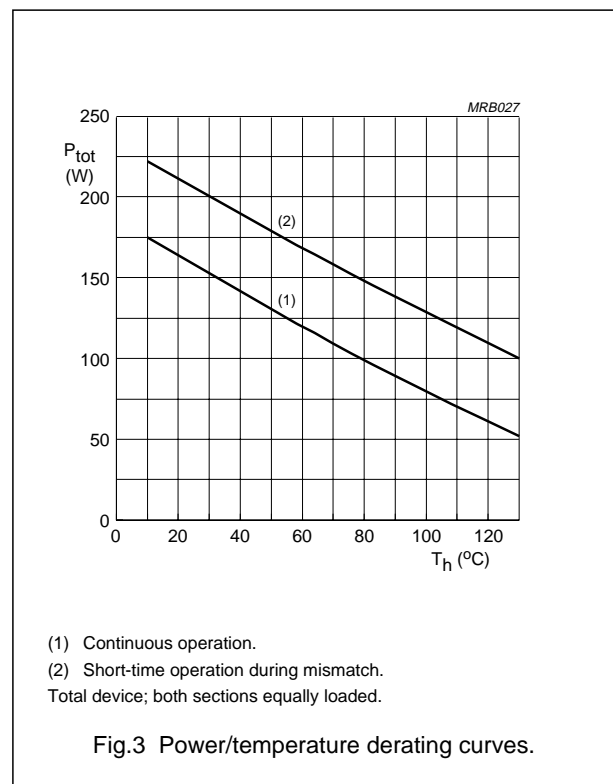
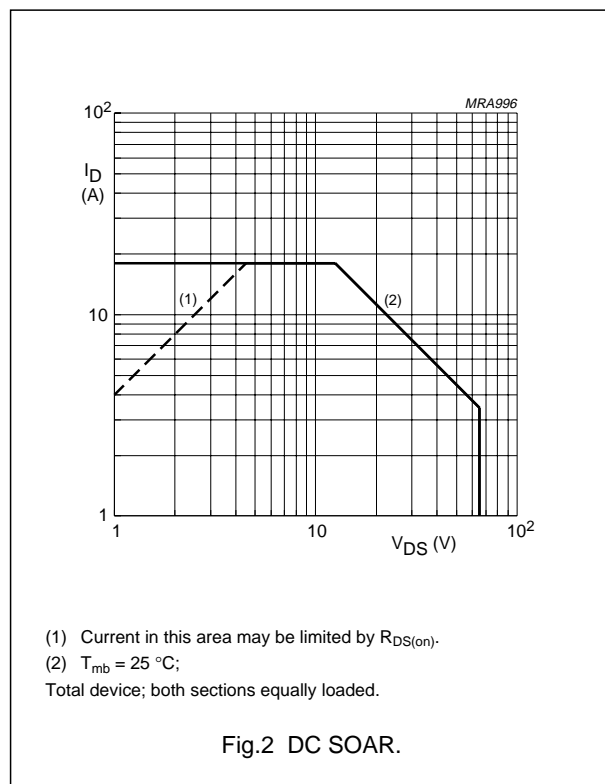
In accordance with the Absolute Maximum System (IEC 134).

Per transistor section unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V_{DS}	drain-source voltage		–	65	V
$\pm V_{GS}$	gate-source voltage		–	20	V
I_D	DC drain current		–	9	A
P_{tot}	total power dissipation	up to $T_{mb} = 25\text{ }^\circ\text{C}$; total device; both sections equally loaded	–	225	W
T_{stg}	storage temperature		–65	150	$^\circ\text{C}$
T_j	junction temperature		–	200	$^\circ\text{C}$

THERMAL RESISTANCE

SYMBOL	PARAMETER	CONDITIONS	THERMAL RESISTANCE
$R_{th\ j-mb}$	thermal resistance from junction to mounting base	$T_{mb} = 25\text{ }^\circ\text{C}$; $P_{tot} = 225\text{ W}$ total device; both sections equally loaded	max. 0.78 K/W
$R_{th\ mb-h}$	thermal resistance from mounting base to heatsink	total device; both sections equally loaded	max. 0.15 K/W

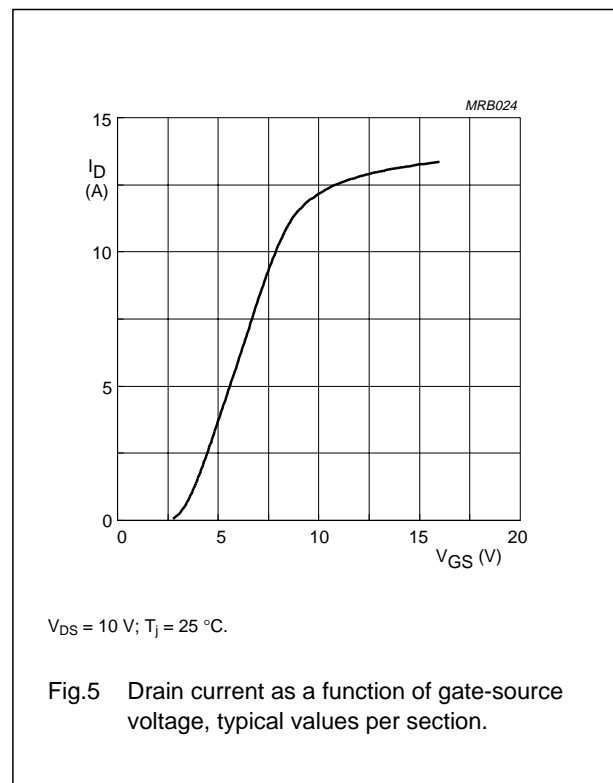
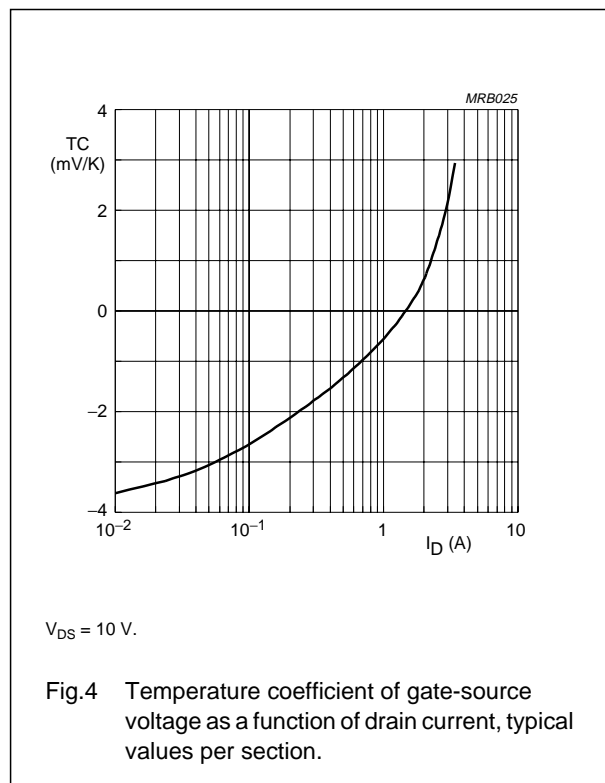


UHF push-pull power MOS transistor

BLF547

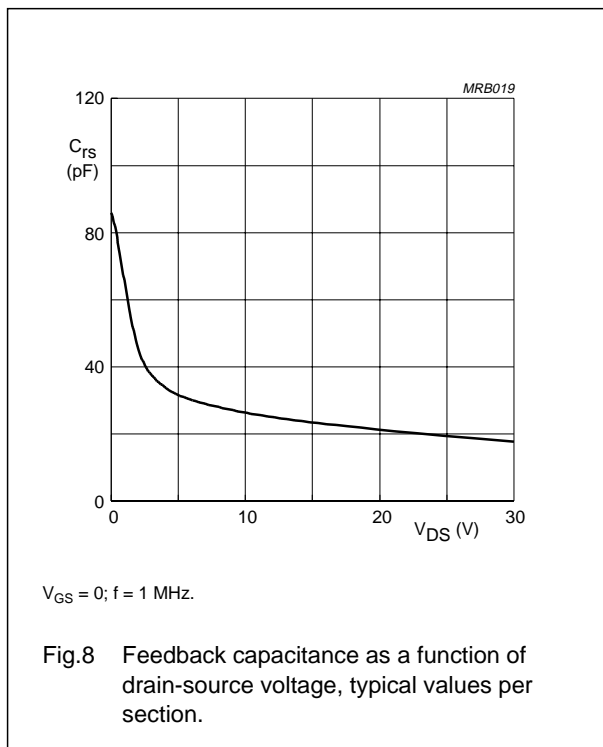
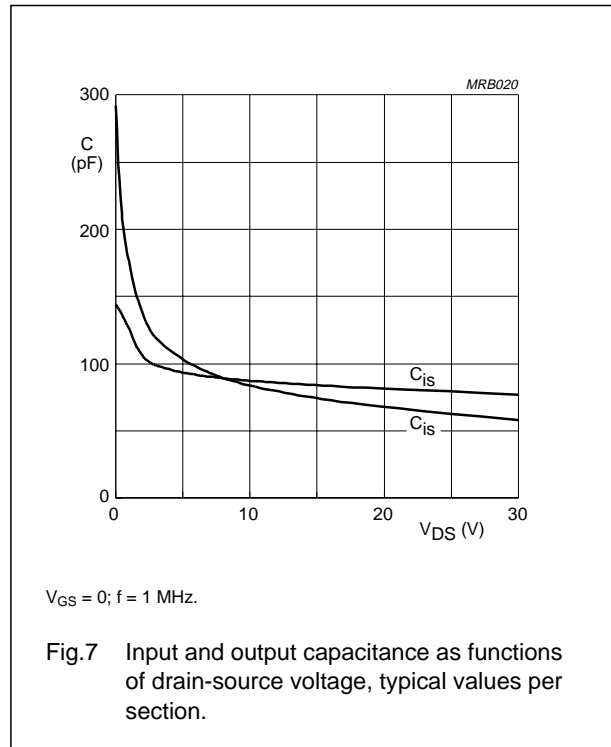
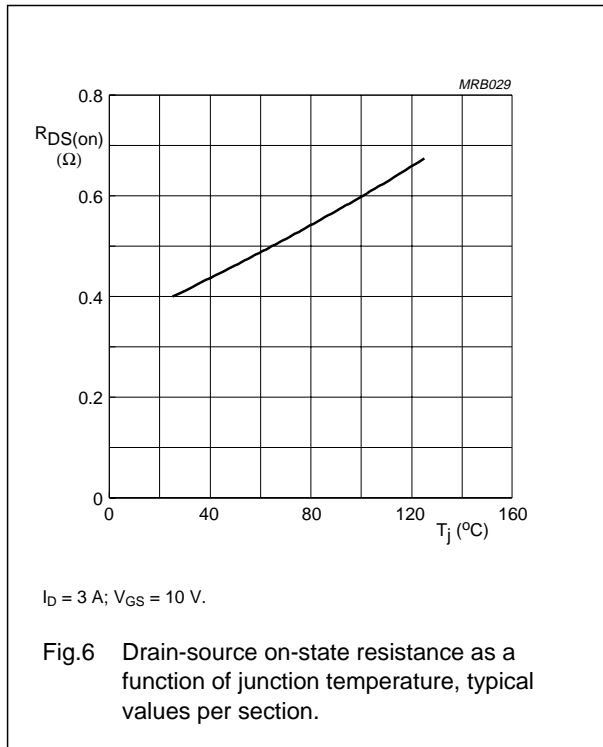
CHARACTERISTICS (per section) $T_j = 25\text{ }^\circ\text{C}$ unless otherwise specified.

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$V_{(BR)DSS}$	drain-source breakdown voltage	$V_{GS} = 0$; $I_D = 25\text{ mA}$	65	–	–	V
I_{DSS}	drain-source leakage current	$V_{GS} = 0$; $V_{DS} = 28\text{ V}$	–	–	2.5	mA
I_{GSS}	gate-source leakage current	$\pm V_{GS} = 20\text{ V}$; $V_{DS} = 0$	–	–	1	μA
$V_{GS(th)}$	gate-source threshold voltage	$I_D = 100\text{ mA}$; $V_{DS} = 10\text{ V}$	1	–	4	V
g_{fs}	forward transconductance	$I_D = 3\text{ A}$; $V_{DS} = 10\text{ V}$	1.5	2.1	–	S
$R_{DS(on)}$	drain-source on-state resistance	$I_D = 3\text{ A}$; $V_{GS} = 10\text{ V}$	–	0.4	0.5	Ω
I_{DSX}	on-state drain current	$V_{GS} = 15\text{ V}$; $V_{DS} = 10\text{ V}$	10	13	–	A
C_{is}	input capacitance	$V_{GS} = 0$; $V_{DS} = 28\text{ V}$; $f = 1\text{ MHz}$	–	77	85	pF
C_{os}	output capacitance	$V_{GS} = 0$; $V_{DS} = 28\text{ V}$; $f = 1\text{ MHz}$	–	62	70	pF
C_{rs}	feedback capacitance	$V_{GS} = 0$; $V_{DS} = 28\text{ V}$; $f = 1\text{ MHz}$	–	18	21	pF



UHF push-pull power MOS transistor

BLF547



UHF push-pull power MOS transistor

BLF547

APPLICATION INFORMATION FOR CLASS-B OPERATION

$T_h = 25\text{ }^\circ\text{C}$; $R_{th,mb-h} = 0.15\text{ K/W}$, unless otherwise specified.

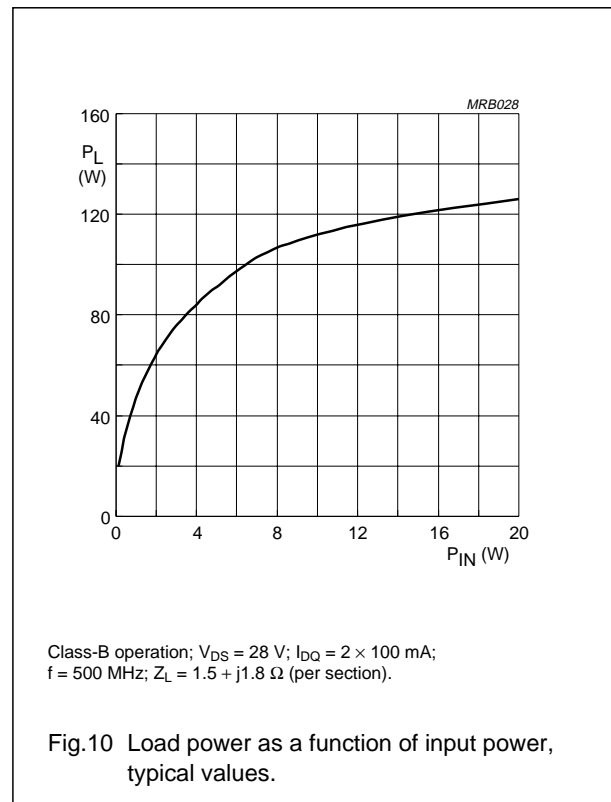
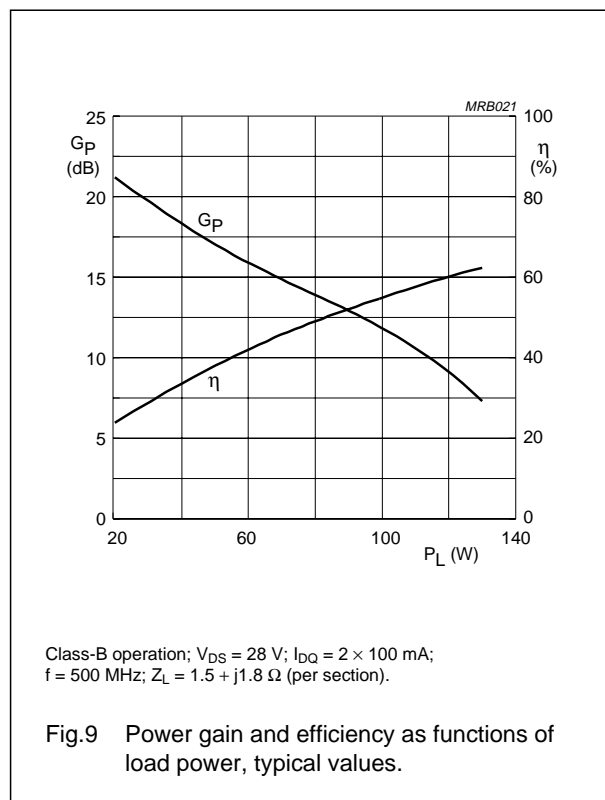
RF performance in a common source, push-pull, class-B test circuit.

MODE OF OPERATION	f (MHz)	V _{DS} (V)	I _{DQ} (mA)	P _L (W)	G _P (dB)	η _D (%)
CW, class-B	500	28	2 × 100	100	> 10 typ. 12	> 50 typ. 55

Ruggedness in class-B operation

The BLF547 is capable of withstanding a load mismatch corresponding to VSWR = 10 through all phases under the following conditions:

V_{DS} = 28 V; f = 500 MHz at rated output power.



UHF push-pull power MOS transistor

BLF547

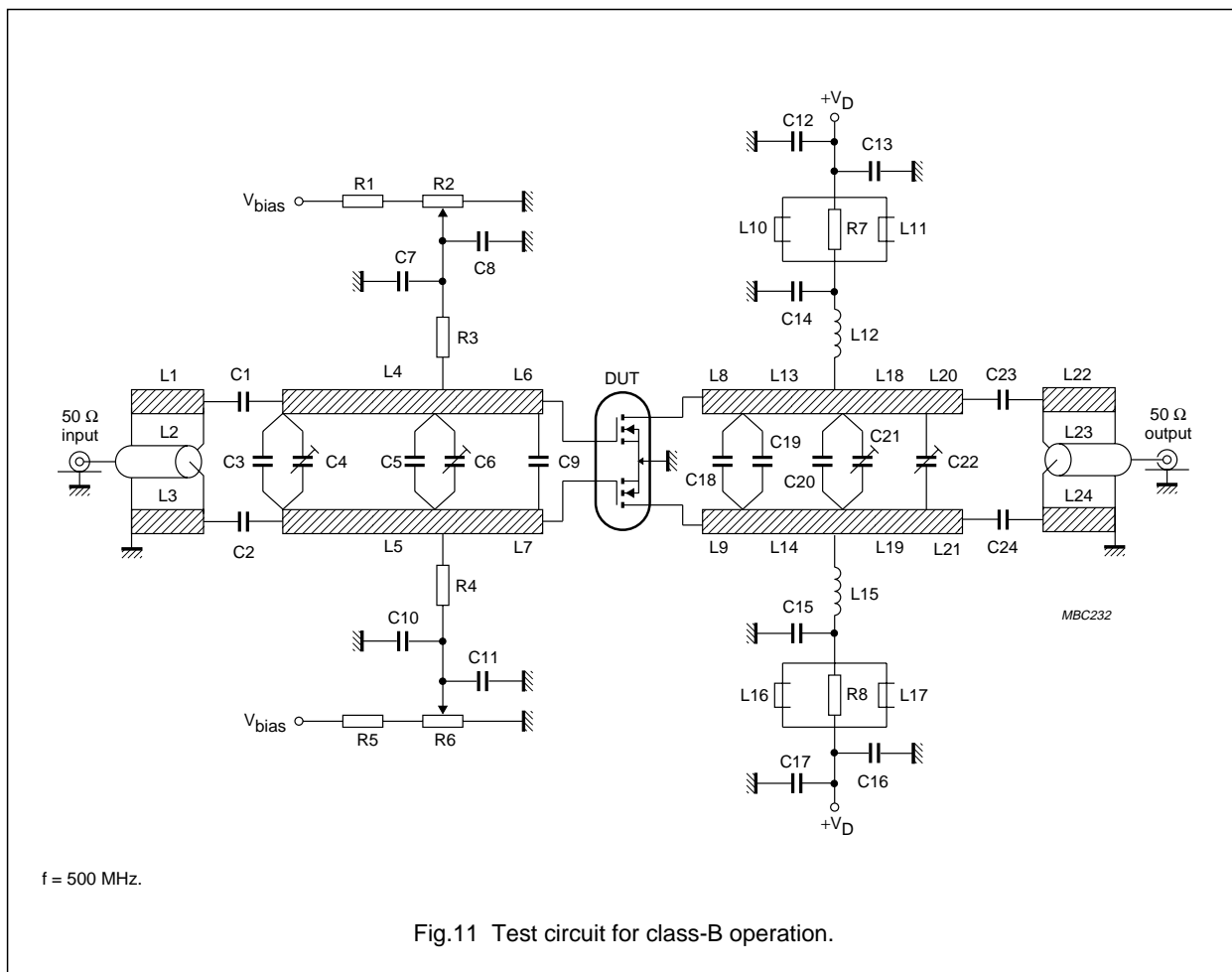


Fig.11 Test circuit for class-B operation.

List of components (see class-B test circuit)

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C1, C2	multilayer ceramic chip capacitor (note 1)	15 pF		
C3	multilayer ceramic chip capacitor (note 1)	16 pF		
C4	film dielectric trimmer	2 to 9 pF		2222 809 09005
C5	multilayer ceramic chip capacitor (note 2)	15 pF		
C6, C21, C22	film dielectric trimmer	2 to 18 pF		2222 809 09006
C7, C10, C14, C15	multilayer ceramic chip capacitor (note 1)	390 pF		
C8, C11, C12, C17	multilayer ceramic chip capacitor	100 nF		2222 852 47104
C9	multilayer ceramic chip capacitor (note 3)	2 × 68 pF in series		

UHF push-pull power MOS transistor

BLF547

COMPONENT	DESCRIPTION	VALUE	DIMENSIONS	CATALOGUE NO.
C13, C16	electrolytic capacitor	10 μ F, 63 V		2222 030 28109
C18	multilayer ceramic chip capacitor (note 2)	10 pF		
C19	multilayer ceramic chip capacitor (note 2)	27 pF		
C20	multilayer ceramic chip capacitor (note 2)	8.2 pF		
C23, C24	multilayer ceramic chip capacitor (note 1)	30 pF		
L1, L3, L22, L24	stripline (note 4)	34.5 Ω	length 66.5 mm width 4 mm	
L2, L23	semi-rigid cable	50 Ω	length 66.5 mm width 3.6 mm	
L4, L5	stripline (note 4)	22.3 Ω	length 35 mm width 7 mm	
L6, L7	stripline (note 4)	22.3 Ω	length 10 mm width 7 mm	
L8, L9	stripline (note 4)	22.3 Ω	length 5.5 mm width 7 mm	
L10, L11, L16, L17	grade 3B Ferroxcube RF choke			4312 020 36640
L12, L15	1 turn enamelled 1.5 mm copper wire	17 nH	length 5 mm int. dia. 9 mm leads 2 \times 5 mm	
L13, L14	stripline (note 4)	22.3 Ω	length 15 mm width 7 mm	
L18, L19	stripline (note 4)	22.3 Ω	length 36 mm width 7 mm	
L20, L21	stripline (note 4)	22.3 Ω	length 8.5 mm width 7 mm	
R1, R5	0.4 W metal film resistor	24.7 k Ω		2322 151 72473
R2, R6	10 turn potentiometer	5 k Ω		
R3, R4	0.4 W metal film resistor	10.5 k Ω		2322 151 71053
R7, R8	1 W metal film resistor	10 Ω		2322 151 71009

Notes

1. American Technical Ceramics (ATC) capacitor, type 100B or other capacitor of the same quality.
2. American Technical Ceramics (ATC) capacitor, type 175B or other capacitor of the same quality.
3. American Technical Ceramics (ATC) capacitor, type 100A or other capacitor of the same quality.
4. The striplines are on a double copper-clad printed circuit board, with PTFE fiber-glass dielectric ($\epsilon_r = 2.2$), thickness 0.79 mm.

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BLF547

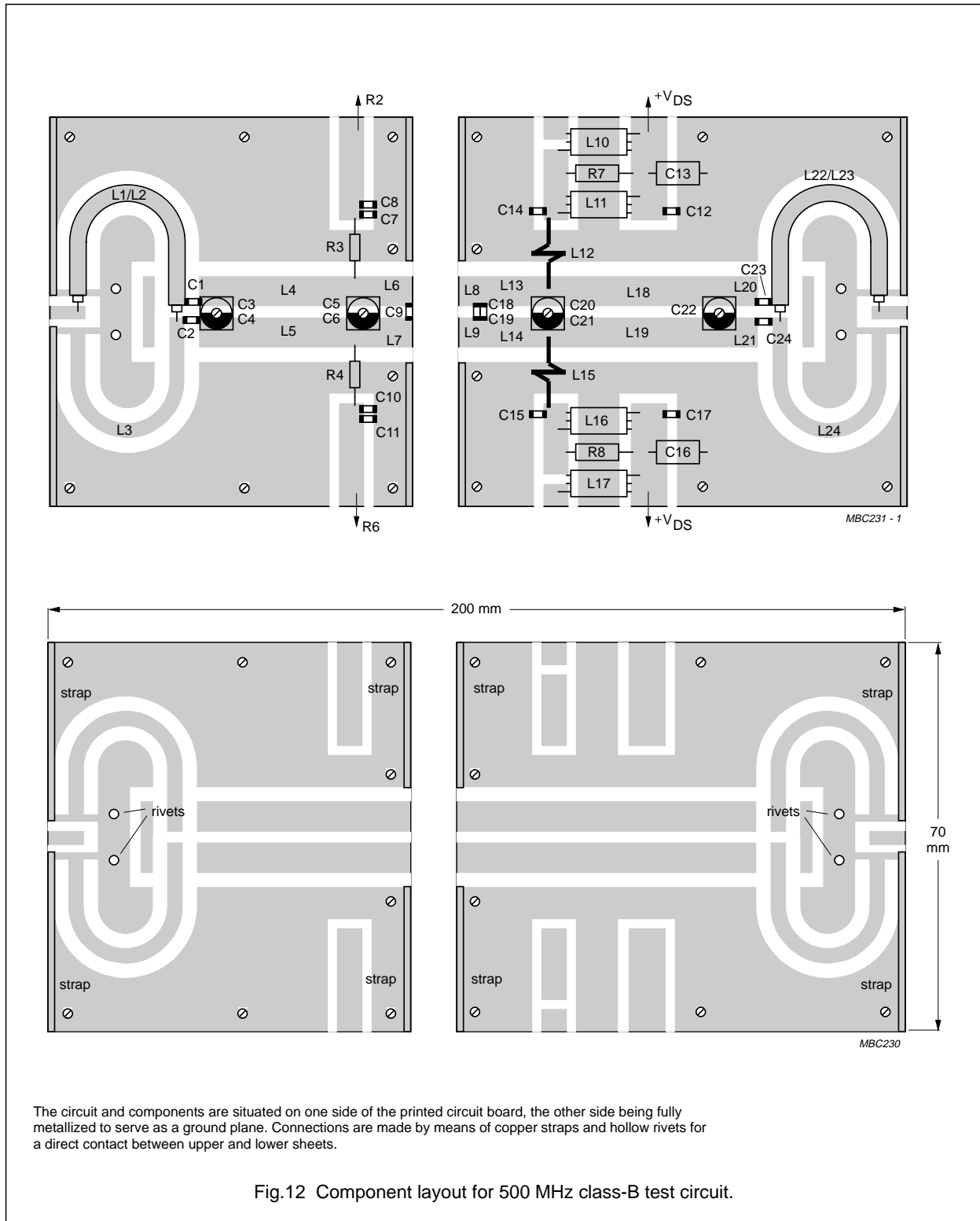
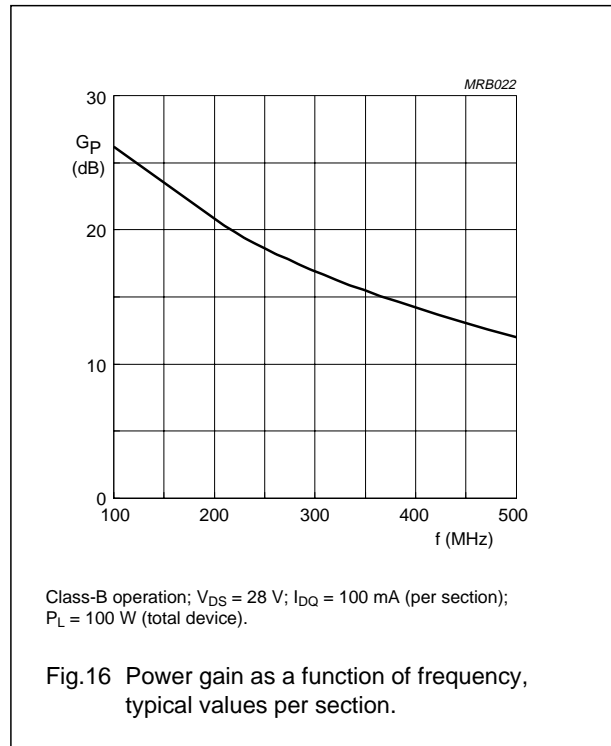
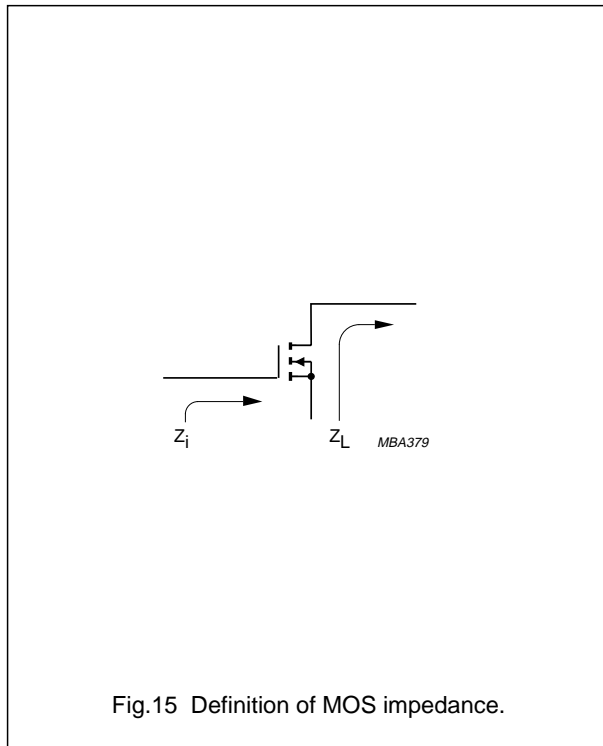
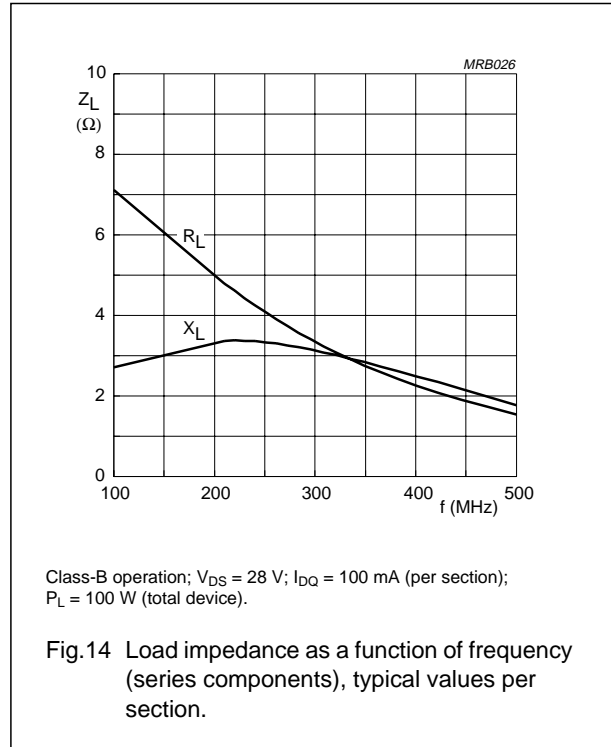
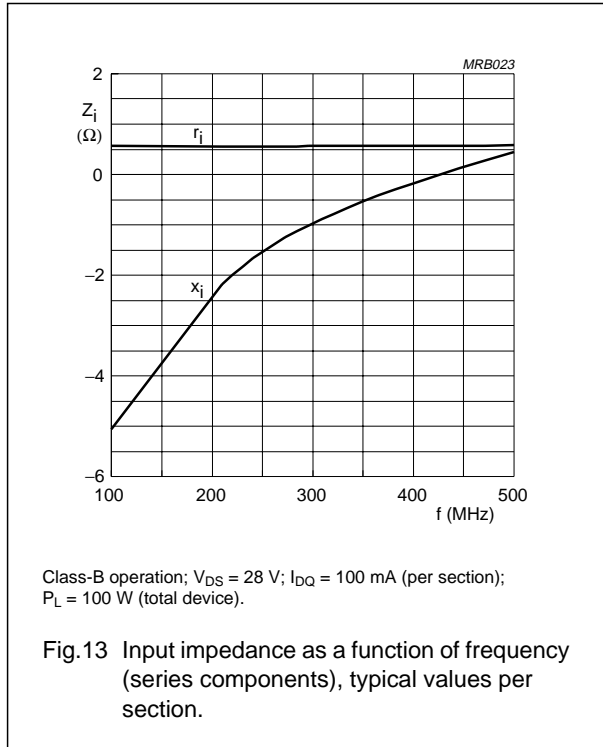


Fig.12 Component layout for 500 MHz class-B test circuit.

UHF push-pull power MOS transistor

BLF547



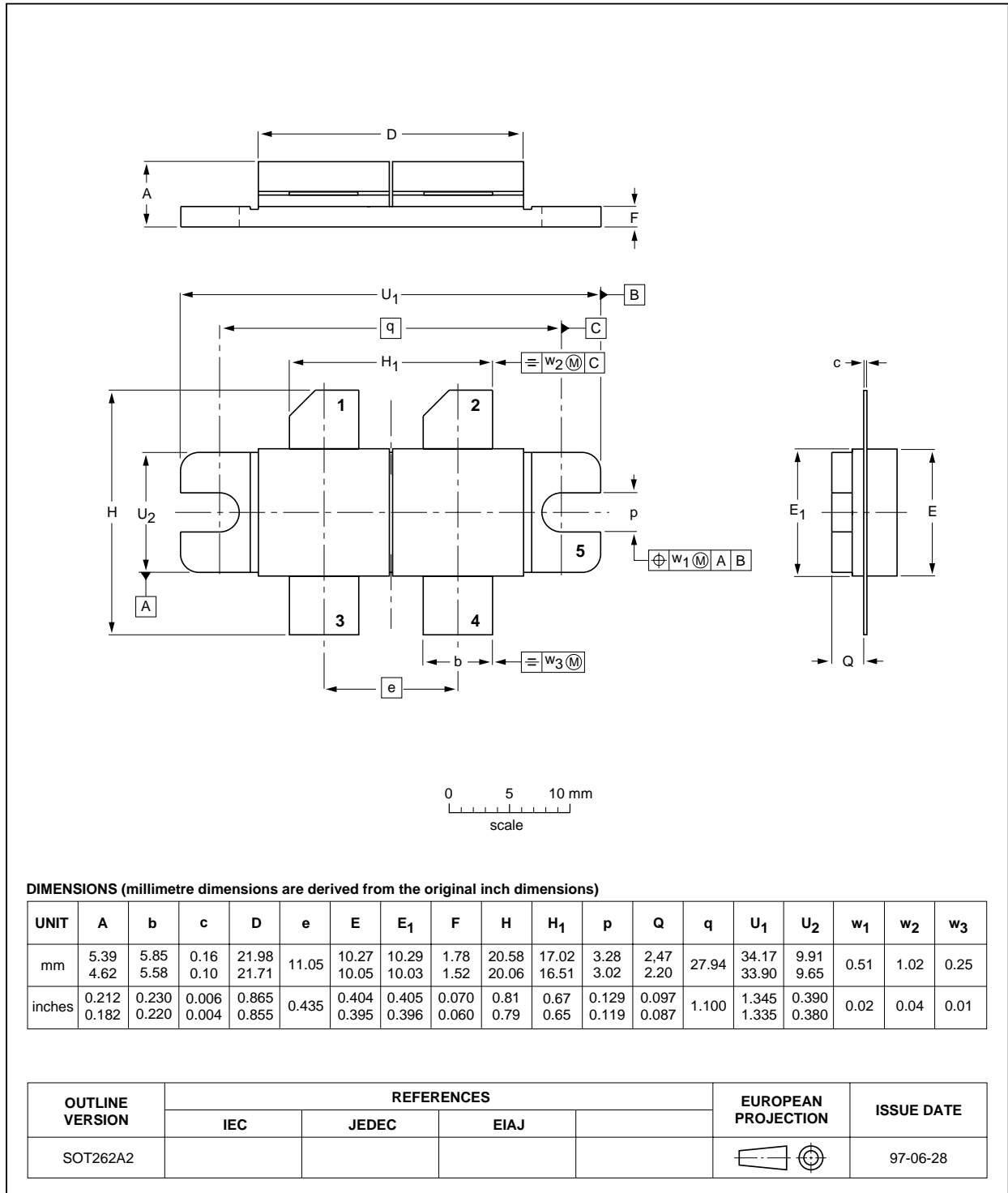
UHF push-pull power MOS transistor

BLF547

PACKAGE OUTLINE

Flanged double-ended ceramic package; 2 mounting holes; 4 leads

SOT262A2



UHF push-pull power MOS transistor

BLF547

DEFINITIONS

Data Sheet Status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

LIFE SUPPORT APPLICATIONS

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