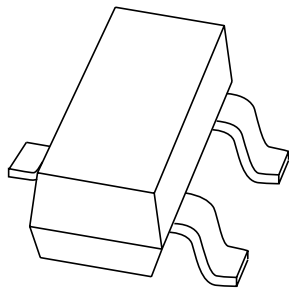


# DATA SHEET



## **BF550** PNP medium frequency transistor

Product specification  
Supersedes data of 1997 Jul 07

1999 Apr 15

## PNP medium frequency transistor

BF550

## FEATURES

- Low current (max. 25 mA)
- Low voltage (max. 40 V).

## APPLICATIONS

- Medium frequency applications in thick and thin film circuits.

## DESCRIPTION

PNP medium frequency transistor in a SOT23 plastic package.

## MARKING

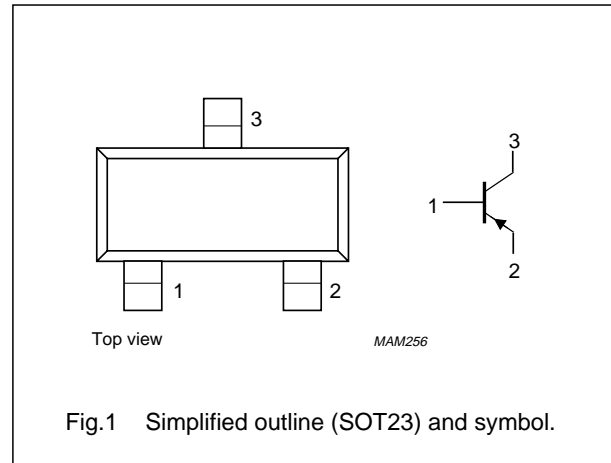
| TYPE NUMBER | MARKING CODE <sup>(1)</sup> |
|-------------|-----------------------------|
| BF550       | LA*                         |

## Note

- \* = p : Made in Hong Kong.  
\* = t : Made in Malaysia.

## PINNING

| PIN | DESCRIPTION |
|-----|-------------|
| 1   | base        |
| 2   | emitter     |
| 3   | collector   |



## LIMITING VALUES

In accordance with the Absolute Maximum Rating System (IEC 134).

| SYMBOL    | PARAMETER                     | CONDITIONS                                       | MIN. | MAX. | UNIT             |
|-----------|-------------------------------|--|------|------|------------------|
| $V_{CBO}$ | collector-base voltage        | open emitter                                     | –    | –40  | V                |
| $V_{CEO}$ | collector-emitter voltage     | open base  | –    | –40  | V                |
| $V_{EBO}$ | emitter-base voltage          | open collector                                   | –    | –4   | V                |
| $I_C$     | collector current (DC)        |  | –    | –25  | mA               |
| $I_{CM}$  | peak collector current        |  | –    | –25  | mA               |
| $P_{tot}$ | total power dissipation       | $T_{amb} \leq 25\text{ }^\circ\text{C}$ ; note 1 | –    | 250  | mW               |
| $T_{stg}$ | storage temperature           |  | –65  | +150 | $^\circ\text{C}$ |
| $T_j$     | junction temperature          |  | –    | 150  | $^\circ\text{C}$ |
| $T_{amb}$ | operating ambient temperature |  | –65  | +150 | $^\circ\text{C}$ |

## Note

1. Transistor mounted on an FR4 printed-circuit board.

## PNP medium frequency transistor

BF550

## THERMAL CHARACTERISTICS

| SYMBOL        | PARAMETER                                   | CONDITIONS | VALUE | UNIT |
|---------------|---|------------|-------|------|
| $R_{th\ j-a}$ | thermal resistance from junction to ambient | note 1     | 500   | K/W  |

## Note

1. Transistor mounted on an FR4 printed-circuit board.

## CHARACTERISTICS

$T_{amb} = 25\text{ °C}$  unless otherwise specified.

| SYMBOL    | PARAMETER                 | CONDITIONS  | MIN. | TYP. | MAX. | UNIT |
|-----------|---------------------------|---|------|------|------|------|
| $I_{CBO}$ | collector cut-off current | $I_E = 0; V_{CB} = -30\text{ V}$                                | –    | –    | –50  | nA   |
| $I_{EBO}$ | emitter cut-off current   | $I_C = 0; V_{EB} = -3\text{ V}$                                 | –    | –    | –100 | nA   |
| $h_{FE}$  | DC current gain           | $I_C = -1\text{ mA}; V_{CE} = -10\text{ V}$                     | 50   | –    | –    |      |
| $V_{BE}$  | base-emitter voltage      | $I_C = -1\text{ mA}; V_{CE} = -10\text{ V}$                     | –    | 750  | –    | mV   |
| $C_{re}$  | feedback capacitance      | $I_C = -1\text{ mA}; V_{CB} = -10\text{ V}; f = 1\text{ MHz}$   | –    | 0.5  | –    | pF   |
| $f_T$     | transition frequency      | $I_C = -1\text{ mA}; V_{CE} = -10\text{ V}; f = 100\text{ MHz}$ | –    | 325  | –    | MHz  |

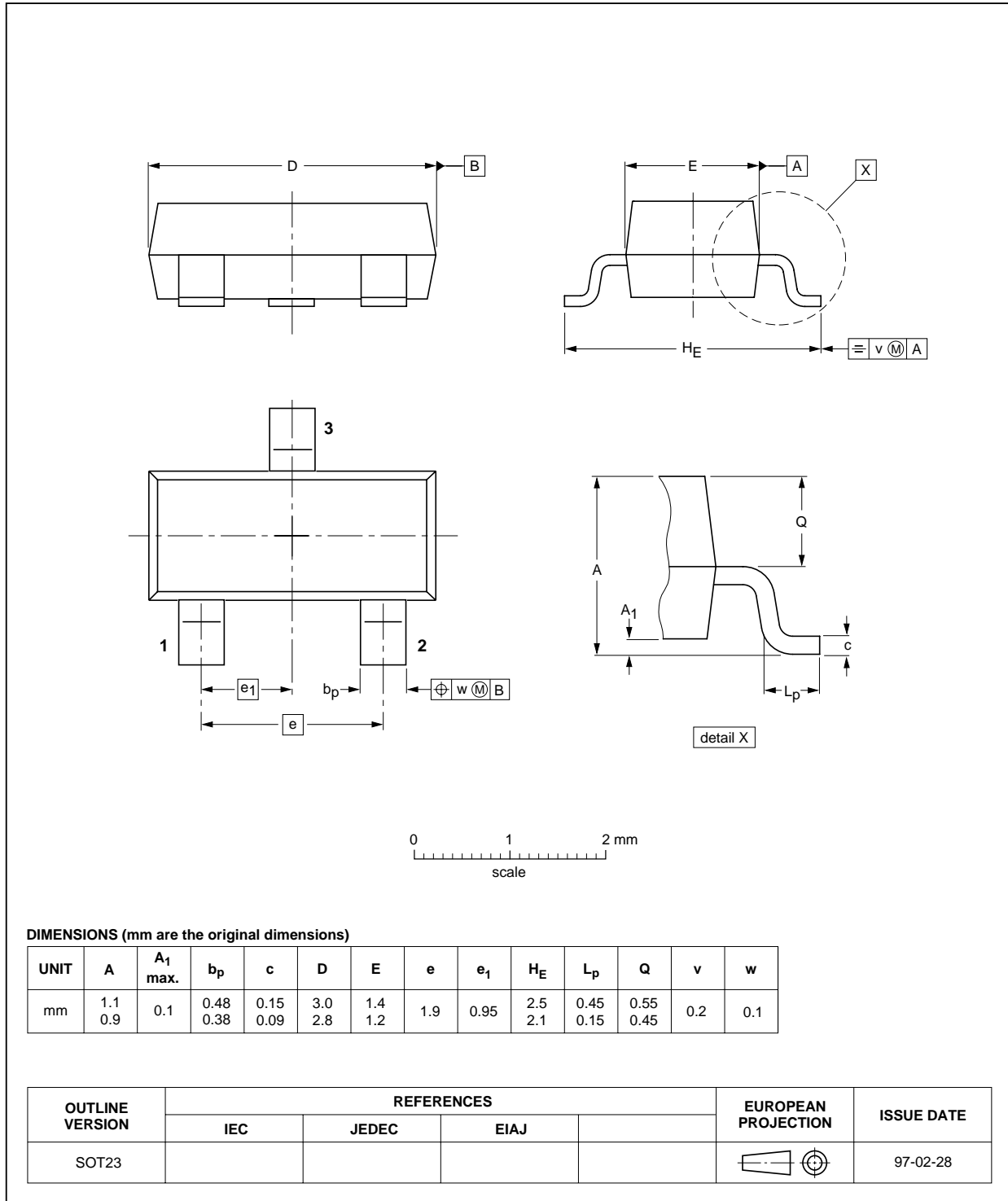
PNP medium frequency transistor

BF550

PACKAGE OUTLINE

Plastic surface mounted package; 3 leads

SOT23



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**PNP medium frequency transistor****BF550**

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**DEFINITIONS**

| <b>Data Sheet Status</b>  |   |
|---|---|
| Objective specification   | This data sheet contains target or goal specifications for product development.       |
| Preliminary specification   | This data sheet contains preliminary data; supplementary data may be published later. |
| Product specification   | This data sheet contains final product specifications.                                |
| <b>Limiting values</b>  |   |
| Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability. |   |
| <b>Application information</b>  |   |
| Where application information is given, it is advisory and does not form part of the specification.   |   |

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PNP medium frequency transistor

BF550

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**NOTES**

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PNP medium frequency transistor

BF550

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**NOTES**

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