8368602 SOLITRON DEVICES INC LL DE 8368602 0001261 2 7-33-67

ENGINEERING DEVICE SPECIFICATION

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NO. 6079/2N2698 SILICON TRANSISTOR

GENERAL DESCRIPTION

This device is an NPN Triple Diffused Planar Power Transistor packaged in an HD case, designed primarily for switching applications at or below a collector current of 5 amperes.

DESIGN LIMITS

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General Assembly & Parts List DR 4587	
	-65°C to 200°C
Storage Temperature, T Operating Junction Temperature	-65°C to 200°C
Collector Current	5 A Max.
Base Current and a second se	.5 A Max.
Continuous Power Dissipation at 100°C	10 Watts
Thermal Resistance	10°C/W

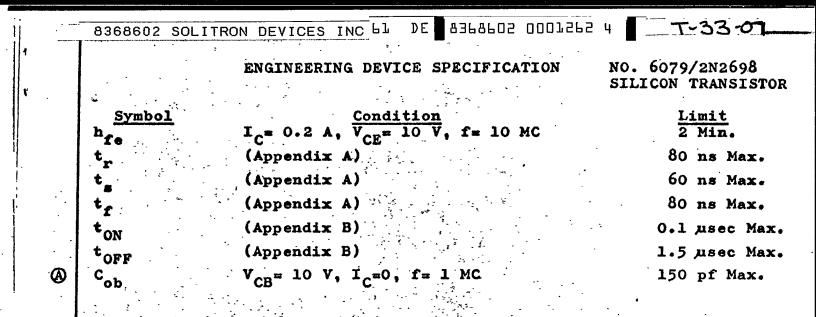
PERFORMANCE CHARACTERISTICS, T_C= 25 + 3°C (unless noted)

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Symbol Condition	Limit
BY _{CBO} I _{CBO} = 10 μA.	100 V Min.
$I_{FBO} = 10 \ \mu A$	8 V Min.
BV _{CEO} I _{CEO} 10 MA	80 V Min.
	50 ДА Мах.
	10 µA Max.
\mathbf{I}_{CBO}	0.1 uA Max.
$v_{EB} = 8 v$	10 JIA Max.
V _{CEO} (sus) I _C = 100 MA*	70 V Min.
h_{FE} $I_C = 1 A, V_{CE} = 2 V$	40 - 120
$h_{FE} = 1 A, V_{CE} = 2 V, T_{C} = -55 C$	10 Min.
h_{FE} $I_C = 5 A, V_{CE} = 6 V$	15 Min.
$V_{CE}(Sat)$ I _C = 1.0 A, I _B = 100 MA*	0.5 V Max.
$V_{CE}(Sat)$ $I_{C}= 5 A, I_{B}= 500 MA^{+}$	3.0 V Max.
$V_{BE}(Sat)$ I _C = 1.0 Å, I _B = 100 MÅ ⁺	1.5 V Max.
$V_{BE}(Sat)$ $I_{C} = 5.0 \text{ A}, I_{B} = 500 \text{ MA}.*$	2.5 V Max.

* Pulsed 330 µs, 2% duty cycle, square wave

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